

DOT-TSC-OST-71-2

PROGRAMMER'S REFERENCE MANUAL  
for  
DYNAMIC DISPLAY  
SOFTWARE SYSTEM



January 1971

PROGRAMMER'S MANUAL

Prepared for

OFFICE OF THE SECRETARY

TRANSPORTATION SYSTEMS CENTER

55 Broadway

Cambridge, Mass. 02142

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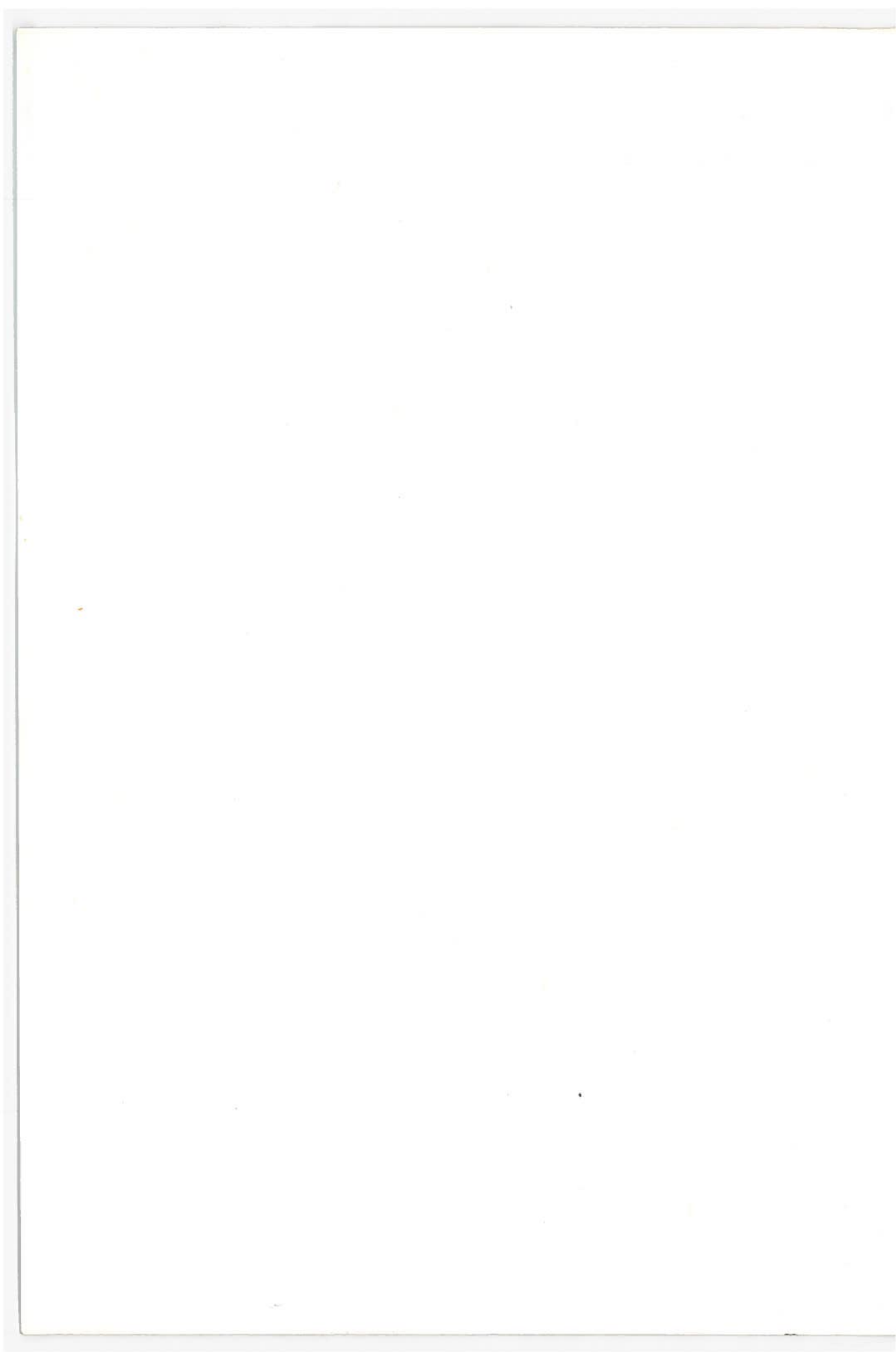
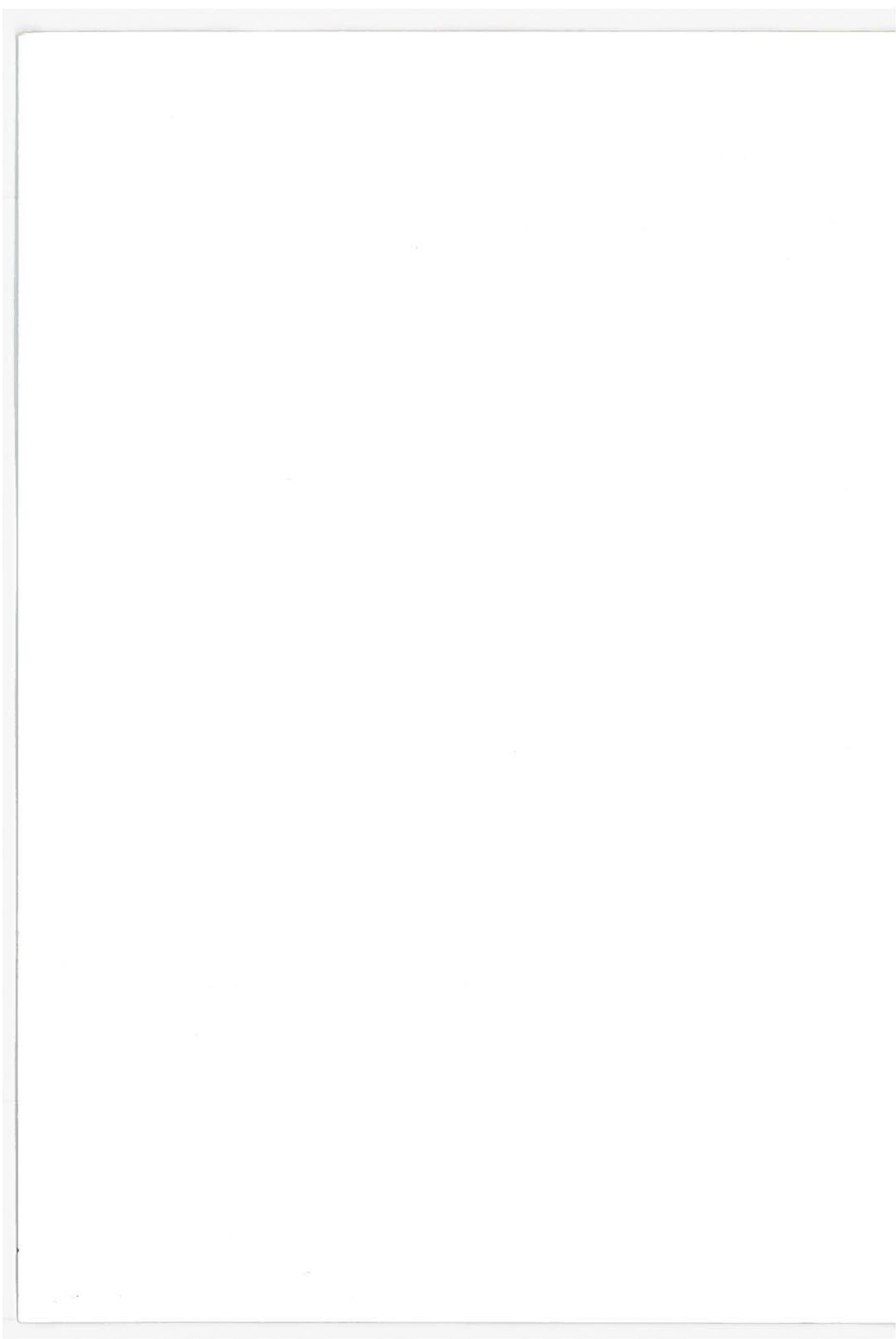


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Abstract

I Introduction

In 1968, the display systems group of the Systems Laboratory of the NASA/Electronics Research Center undertook a research task in the area of computer controlled flight information systems for aerospace application. The display laboratory of the Transportation Systems Division of the Transportation Systems Center for the Department of Transportation is the direct descendant of the above display laboratory. The vehicle for conducting this research consists of a Honeywell DDP-516 computer interfaced with a Sanders Associates ADDS/900 graphical display generator that controls various CRT devices. Input devices for indicating user response, including light pen, tablet, track ball and others can be interfaced with the computer and monitored by a simulation program.

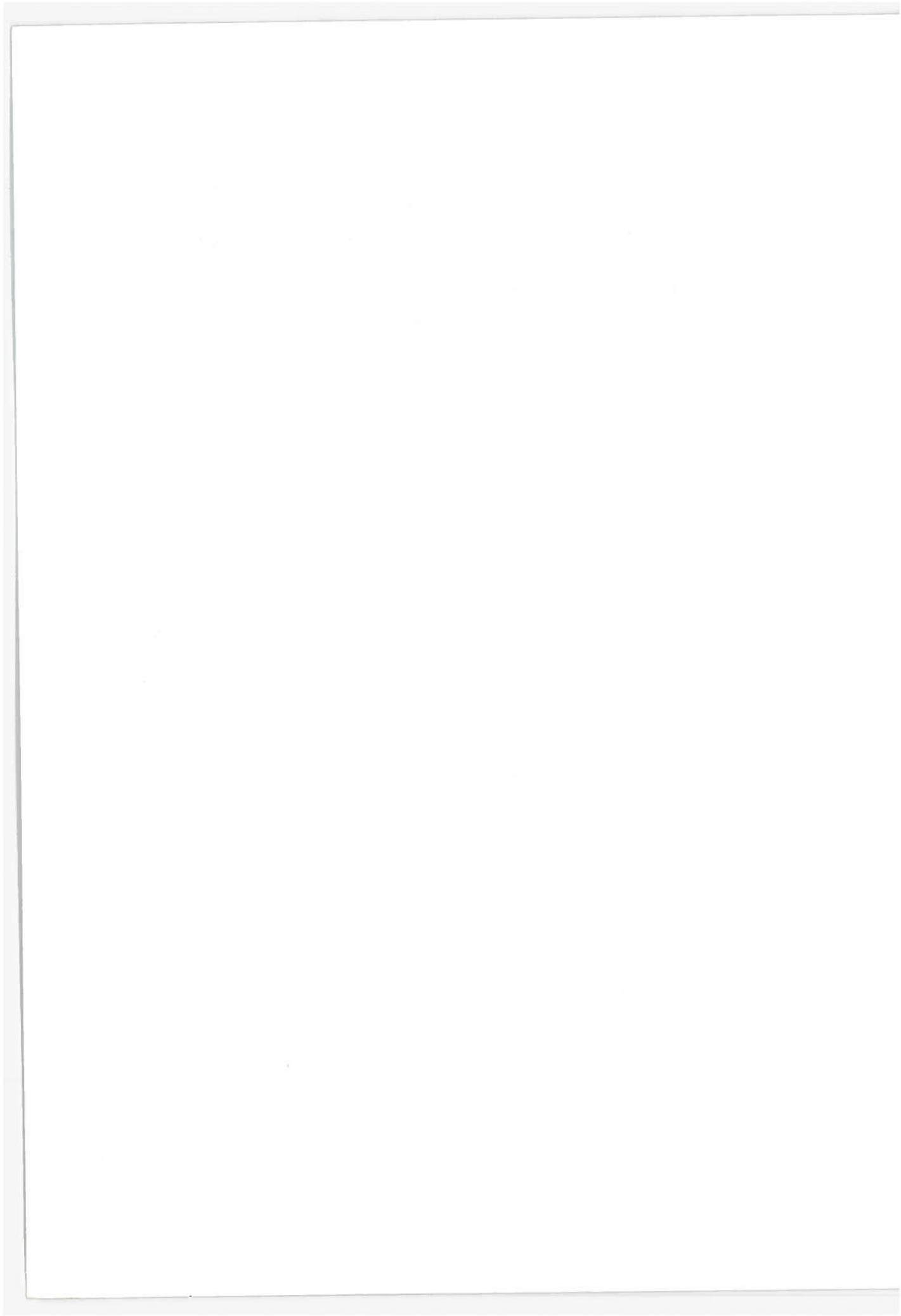
With the Dynamic Display Software System, the user can interactively create the geometric characteristics of the desired display and specify dynamic linkages with a program to simulate the display environment. After the desired set of indicators is specified, the system serves as a simple real-time simulator to evaluate the usefulness of the displays. In addition, the system may run on-line with PDP-10, which allows display of conditions in a more complicated simulation environment. The user has the capability of making changes to previously created indicators to provide an evolutionary means of developing environment indicator systems. The system is described in this manual.

The section "SYSTEM CONCEPTS" provides a brief general description of the three phases of system operation and interaction of the phases.

The section "DATA STRUCTURE" shows how the display data is arranged in core and describes auxiliary blocks used for dynamic updating of the displayed data.

The final section "PROGRAM DESCRIPTIONS AND FLOWCHARTS" gives a detailed description of all of the programs and subroutines used in the system.

For a detailed discussion on system use see the document, "A GUIDE TO THE USE OF THE DYNAMIC DISPLAY SOFTWARE SYSTEM".



## II. SYSTEM CONCEPTS

The system is divided into two large functional parts and one smaller part to allow efficient use of core. These parts are the create/edit phase, where display lists are created and matched via dynamic expressions to the outputs of a simulation; the simulation phase, where display and update of the data structure and operation of the simulation are under control of the real time monitor; and the input/output phase, which supervises transfer of display files between the create/edit and simulation phases, and changes all user entered specifications of dynamics into executable object code via a small compiler subprogram.

### 1. Create/Edit Phase

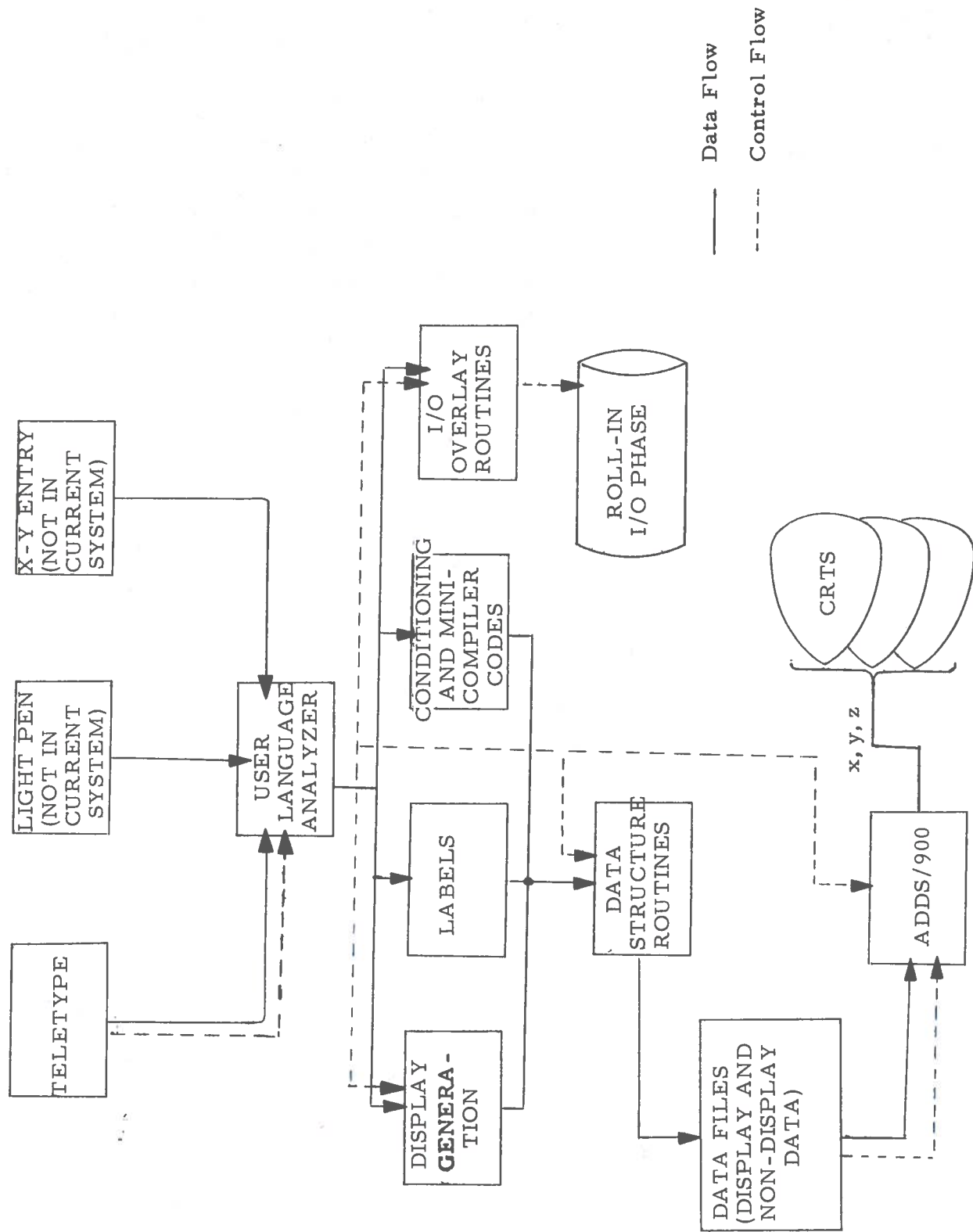
The first phase of system operation is shown functionally in Figure II. 1. This phase consists of: (a) a user language, which provides the interface between the user and the system, and preliminary conversion of alphanumeric input into display elements; (b) data structuring routines, which handle storage allocation and the creation and maintenance of the data structure; and (c) connector programs, which handle overlay of the other system phases upon user request.

Graphic components specified via the user language are displayed immediately. Also specified utilizing the user language are dynamic expressions that will modify the graphic components using the update program in the simulation phase. The language, however, does not generate object code for dynamic expressions but stores the expressions as packed characters for later processing by the mini-compiler in the input/output phase.

### 2. Input/Output - Mini-compiler

This phase of operation services input and output of the data structure and free storage ring onto paper tape or disc. Prior to saving the data structure, a mini-compiler using pointers internal to the data structure, compiles each dynamic expression of FORTRAN-like source code entered during the immediately preceding create/edit phase. Once saved, the data structure may be loaded back into create/edit phase for additions or deletions or into the simulation phase. The input/output phase of operation is functionally shown in Figure II. 2.





— Data Flow  
 - - - Control Flow

Figure II.1  
 Functional Design of Create/Edit Phase

### 3. Simulation Phase

The Simulation Phase provides an environment whereby the displayed data may operate dynamically. Provision is made, under user option, for input from or output to any external device. Inputs may be used directly as dynamic parameters, or they may be used by a model and/or background program which may alter predefined registers. The update program uses this data and mini-compiler code to make changes to the display list. It is at this point in the display system that data provided by a simulation in the PDP-10 would be used to alter the display. See Figure II. 3.

The Monitor supervises the scheduling of the various routines and allows the user to modify the update cycle time if the system overruns the time constraint.

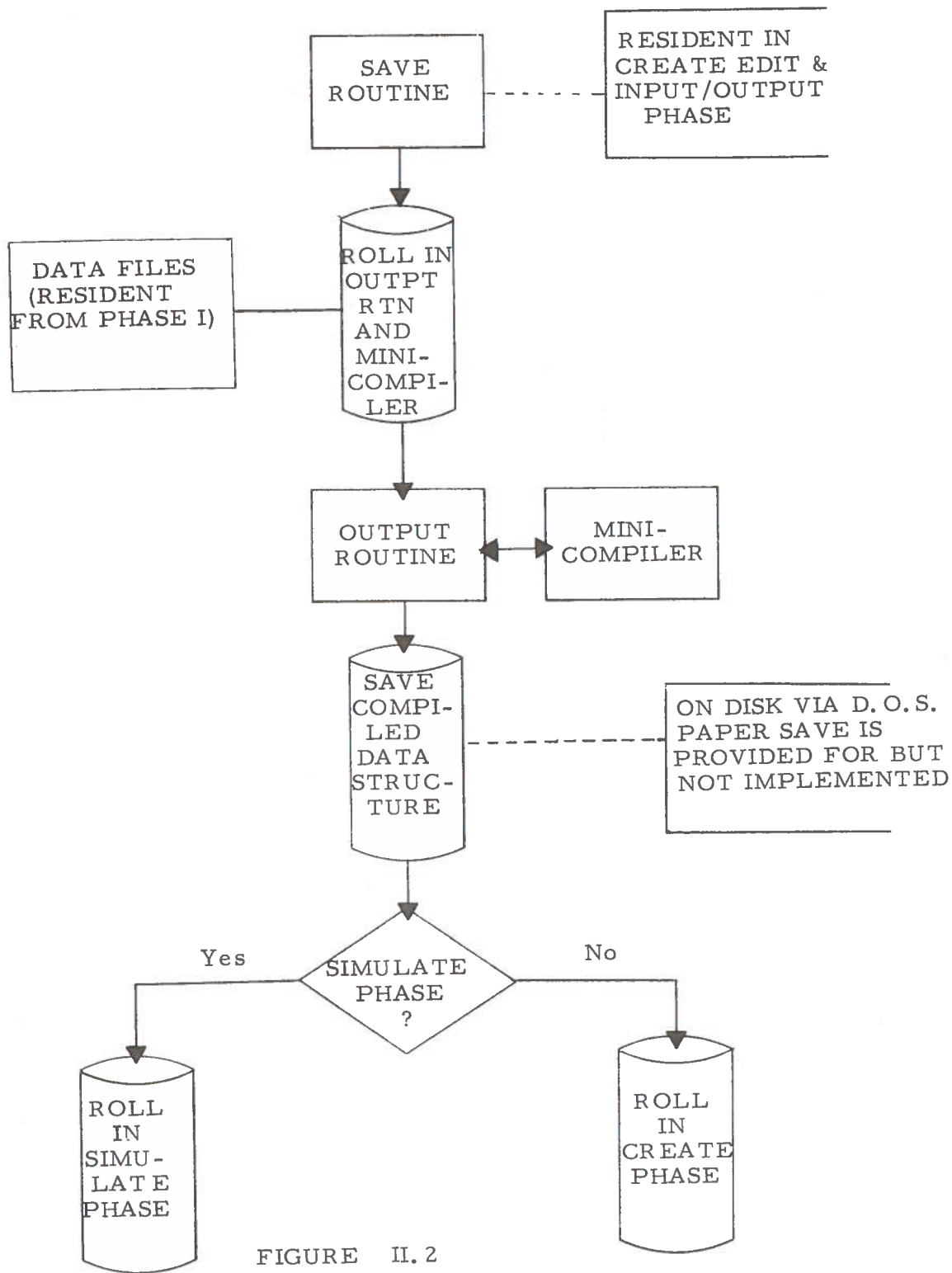
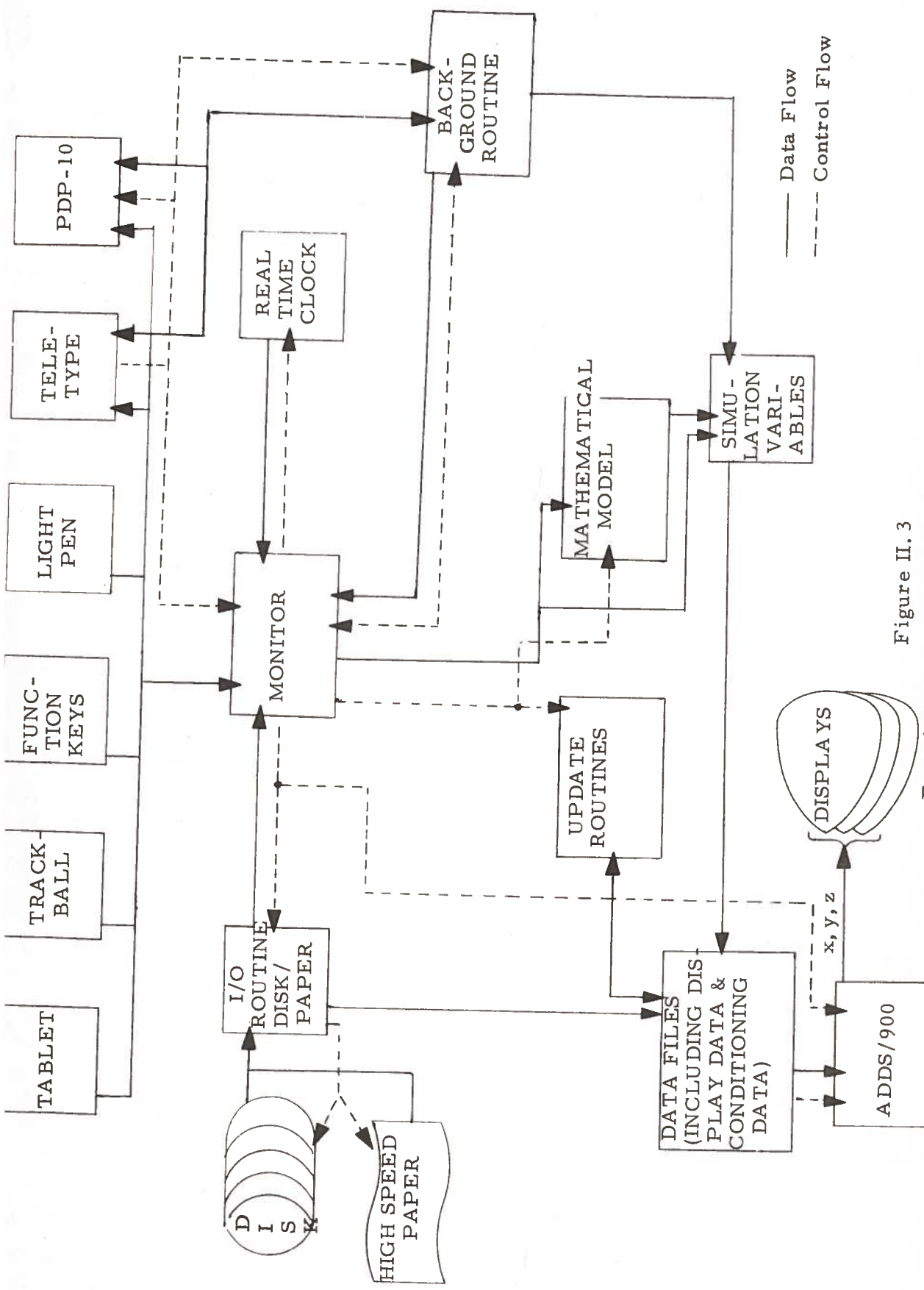


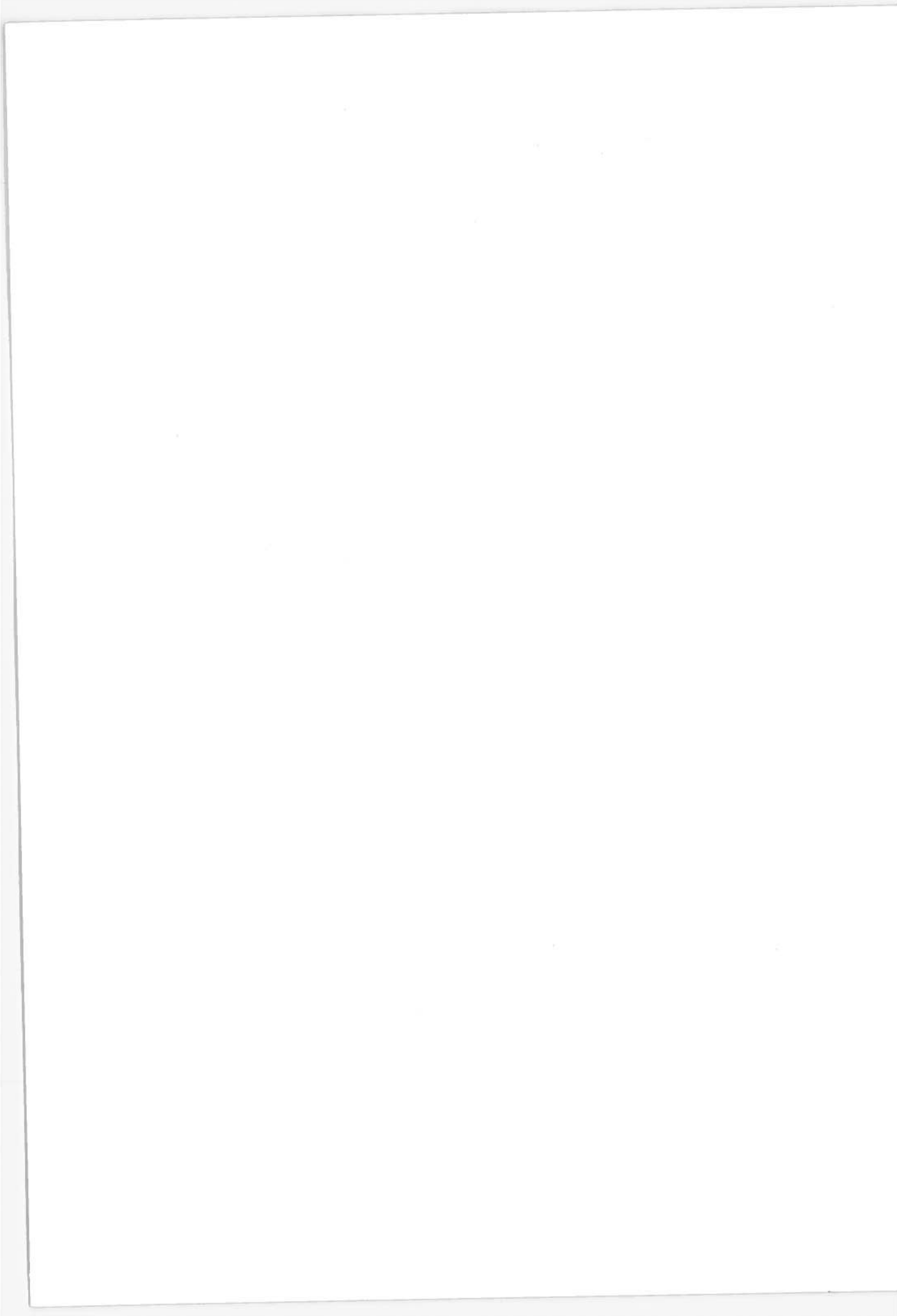
FIGURE II.2

FUNCTIONAL DESIGN OF INPUT-OUTPUT PHASE



— Data Flow  
 - - - Control Flow

Figure II. 3  
 Functional Design of Simulation Phase



### III DATA STRUCTURE

1. The display data format is a hierarchical ring structure with auxiliary blocks which describe the parameters to be used for dynamic updating of the data.

The highest level in the hierarchy is the frame ring which is a ring of frame blocks. Attached to a frame block is an indicator ring. There may also be an auxiliary block for register definition attached to a frame block.

An indicator ring consists of indicator blocks which belong to one particular frame. Each indicator block has an entity ring associated with it. The entity ring consists of entity blocks which all belong to the same indicator. An indicator block may have an auxiliary conditioning block attached to it.

Entity blocks have component blocks and conditioning blocks attached.

The entity blocks and the component blocks basically contain display data; that is data which is sent to the display unit. This includes vectors, characters, and coordinate converter information. These blocks are tied together using display jumps to allow automatic display of a frame.

The conditioning blocks contain information which is used by the user language processor to direct dynamic changes of the display according to the state of the simulation model. These blocks are tied to the ringed display data by pointers. See Figure III. 1.

#### 2. Descriptions of Data Formats

##### A. Frame Block

A frame block is accessed by reference to a permanently assigned word (FRHD) containing the start address of the first frame on the frame ring.

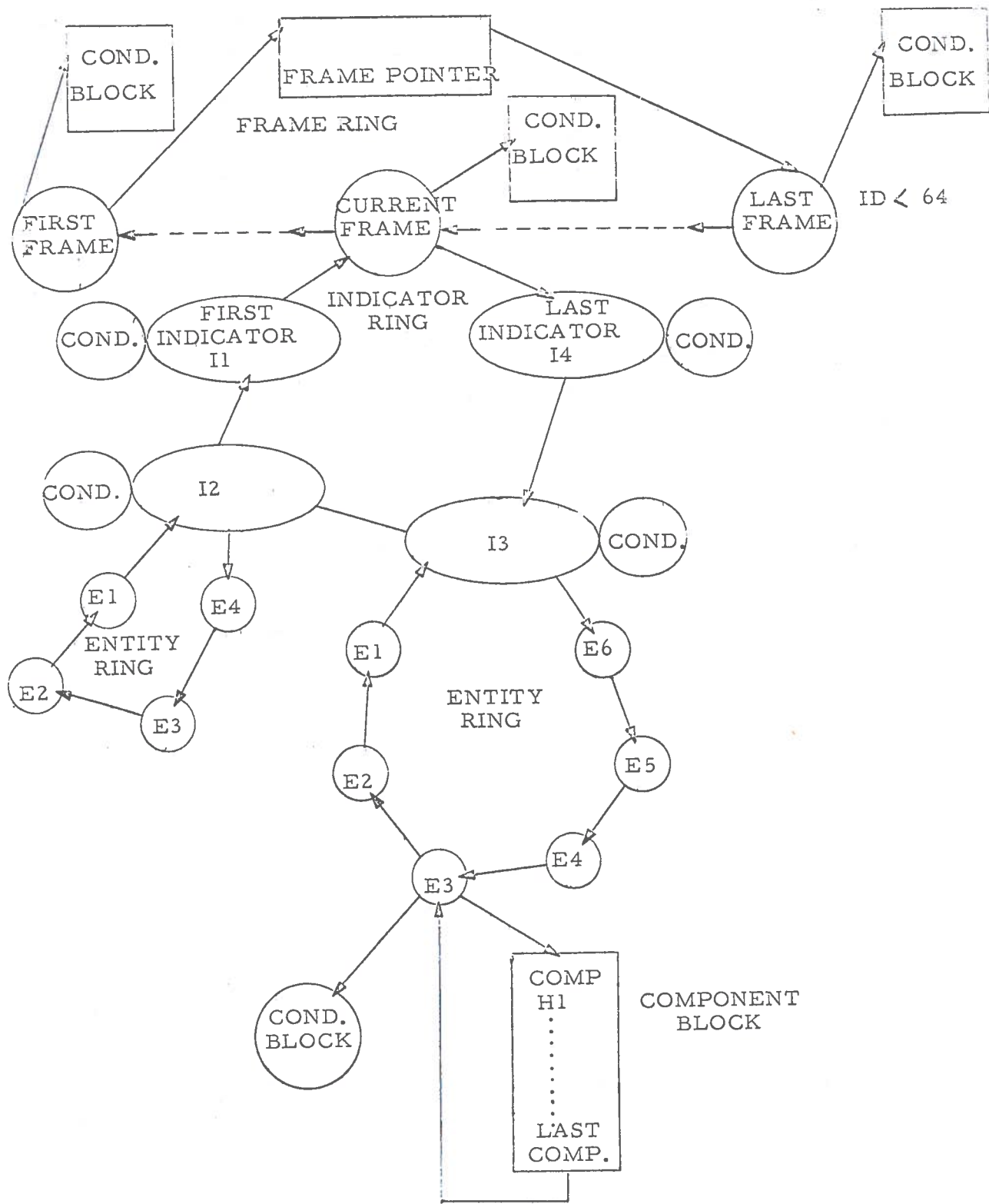


Figure III. 1  
Ring Organization of the Data Structure

Format: See Figure III. 2.

Word 1: word 1 contains the storage block type (1) and the ID of the frame.

Word 2: word 2 contains a pointer to the next frame in the ring. If it is the last frame in the ring, then the pointer is to the frame header (FRHD).

Word 3: the third word contains a pointer to a register definition conditioning block if any auxiliary registers have been defined. If not, it contains zeroes.

Word 4 and 5: these two words comprise a 'display jump' to word 3 of the last created indicator on this frame. If there are no indicators, then the jump is to word 6 ("WAIT"). As indicators are added, the contents of word 5 replace the contents of NEXT INDICATOR (word 10) of the new indicator block, and the address of the new indicator replaces the contents of word 5.

Word 6: contains a 'wait' to allow refresh synchronization.

Word 7 and 8: Jump back to word 4 for refresh.

#### B. Indicator Block

Indicators are accessed through word 5 (jump address) of a frame block.

Format: See Figure III. 3.

Word 1: word 1 contains the block type (2) and the ID of the indicator.

Word 2: pointer to indicator conditioning block. This word has a value of zero if there are no dynamic conditions.

Word 3: NOOP (Display Command)

Word 4: Enter Register mode for display indicator select.



Word 5: assignment of channel on which indicator is to be displayed.

Word 6 and 7: Display jump to last attached entity of this indicator. If there are no entities then jump is to word 8 of the same block.

Word 8: NOOP

Word 9 and 10: display jump to next indicator. If this is the last indicator then jump is to word 6 of the frame block.

C. Entity Block

Format: See Figure III. 4.

Word 1: contains block type (3) and entity ID.

Word 2: pointer to entity conditioning block. If no such block then value is zero.

Word 3: Enter Register Mode.

Word 4 - 9: Rotation, Translation Registers.

Word 10: Intensity, Texture, Blink control.

Word 11: enter coordinate mode.

Word 12 and 13: Make blanked beam move to the 0, 0 coordinate.

Word 14 and 15: Display jump to components. If there are no components

Word 16: NOOP

Word 17 and 18: Display jump to next entity. If this is last entity on ring, then jump is to word 8 of Indicator Block.

Word 19 and 20: Pointers to conditioning block end and component block end respectively.

Word 21 and 22: Spare

#### D. Conditioning Blocks

Conditioning blocks contain information which is used by the language processor in the Simulation Phase of system operation to control the display dynamics. Indicators and entities have associated conditioning blocks.

The information in a conditioning block is in the form of individual actions to be performed by the language processor. These action commands are inserted into the conditioning block as the user inputs them via the keyboard. The initial entry into the conditioning block for an indicator or an entity causes a pointer to the conditioning block to be entered into the appropriate indicator or entity block. An 'end' command code is placed into the conditioning block whenever the user changes the level of operations (i. e., goes from indicator to entity or entity to indicator or frame).

In the event that the user's conditioning information includes calculations, mini-compiler source code (a subset of FORTRAN), is stored directly in the conditioning block. Subsequently, the mini-compiler produces object code which is stored in areas of core not necessarily related to the conditioning block. This code is then accessed via pointers from the conditioning block, which are located at the position where the code should be executed. Excess storage from the already compiled source code is then released to the free storage ring via RELF, and the source code is destroyed.

		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
A	1	Storage Block Type 1										ID (Name)					
	2	Pointer to Next Frame															
	3					Pointer to Register Definition											
	4	JUMP															
	5	Address of Last Indicator Attached															
	6	WAIT															
	7	JUMP															
	8	A															

FIGURE III. 2  
FRAME BLOCK

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	
1		2															ID
2		Pointer to Indicator 'Conditioning' Block															
3		NOOP															
4		Load Mode										Reg					
5		Reg #			Channel												
6		JUMP															
7		First Entity															
8		NOOP															
9		JUMP															
10		Next Indicator															

ADDS/900  
EXECUTABLE  
COMMANDS

FIGURE III. 3  
INDICATOR BLOCK

ADDS/900  
EXECUTABLE  
COMMANDS.

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1	3													ID		
2	Pointer to Entity Conditioning Block															
3	Load Mode							Register			0 0 0 0					
4	Reg #	X		Pre-Rotation												
5		Y		Translation												
6		Sin		Angle of												
7		Cos		Rotation												
8		X		Post Rotation												
9		Y		Translation												
10	Load Axis Control						Intensity Texture									
11	Load Mode							Coor- dinate			0 0 0,1					
12	Load X ABS		0													
13	Load Y ABS		0													
14	JUMP															
15	Data Components															
16	NOOP															
17	JUMP															
18	Next Entity or Indicator															
19	Pointer to Conditioning Block End															
20	Pointer to Component Block End															
21	SPARE															
22	SPARE															

FIGURE III. 4  
ENTITY BLOCK

## E. Components

Components contain the beam moving commands for the display to draw lines, characters, etc. The components are attached to an entity with display jumps. They are entered into the data structure when the user gives the appropriate keyboard signals to create picture parts. If a component has expressions describing dynamic changes associated with it, then this information is entered into the conditioning block with a pointer to the entity so that the entity can be appropriately modified by the language processor during the simulation phase.

The first user's language drawing command of each entity causes a component block to be opened. Additional component blocks are chained to the first component block as needed.

### Display Command Formats for Component Blocks

#### 1. Blank Line

```
LOAD X REL
MOVE Y REL
```

#### 2. Point

```
LOAD X REL
MOVE Y REL
DRAW POINT
```

#### 3. Line

```
LOAD X REL
DRAW Y REL
```

#### 4. Arc

```
LOAD X REL
MOVE Y REL
LOAD X REL
DRAW Y REL
```

Blank move from center

One Segment

.  
.  
.  
.  
.  
The rest of the segments

LOAD X REL            Blanked move back to center  
MOVE Y REL

5. Alphanumerics

LOAD X INDEX REG - For char. spacing  
LOAD MODE CHAR  
LOAD CHAR SIZE  
CHAR1 CHAR2  
CHAR3 CHAR4  
CHAR5 CHAR6  
CHAR7 CHAR8  
LOAD MODE COORD

6. Tick Marks for Lines

LOAD X REL  
MOVE Y REL            Position  
LOAD X INDEX REG - Set up counter  
N                      - No. of tick marks

.  
A LOAD X REL            Draw the mark

DRAW Y REL

LOAD X REL

MOVE Y REL

Set for next

DSKZ - DECR. INDEX AND SKIP 2 WORDS ON ZERO  
JUMP TO A (2 WORDS)

7. Scales for Lines

LOAD X REL

MOVE Y REL

Position  $X_i, Y_i$

LOAD CHAR SIZE

LOAD X INDEX REG - For char. spacing

LOAD X REL  
MOVE Y REL  
LOAD MODE CHAR  
RANDOM CHAR

. As needed  
LOAD MODE COORD

.  
LOAD X REL  
MOVE Y REL Set for next position  
REPEAT

8. Tick marks for Arcs

LOAD X REL Position tick  
MOVE Y REL  
LOAD X REL Draw tick mark  
DRAW Y REL  
LOAD X REL Position to next tick  
MOVE Y REL  
REPEAT  
LOAD X REL Move beam back to center  
MOVE Y REL

9. Scales for Arcs

Same as 7 except reposition beam to center of arc  
at end of sequence with LOAD X REL  
MOVE Y REL

10. Digital Readout

LOAD X REL  
MOVE Y REL Position  
RANDOM CHARS As needed, no more than six  
. . .



#### F. Dynamic Components Definitions

The following components may be dynamically defined:

- 1) Point
- 2) Line
- 3) Blank Line
- 4) Digital Readout

In addition, certain dynamic parameters may be specified to alter the entire entity. These are:

- 1) Rotate
- 2) Translate
- 3) Blank/Unblank, Intensity, Blink, and Texture

These commands cause a conditioning command much like that for the dynamic components, but do not affect words in the component block. Rather, words in the entity block header itself are changed. These commands should not be confused with dynamic components, which are actually displayed.

Specifications for dynamic components are written following the conditions in the conditioning block and each has a pointer to the display data for the component.

See Figure III. 5 for the format of dynamic entry in the conditioning block.

G. Register Definition Block

A register definition block is an auxiliary block attached by a pointer to a frame block. It contains expressions which define new registers in terms of model parameter registers.

Bit 1 = 1 means  
start of expression  
if bit 2 = 0

	1	2	9	16
1				Expressions
0				
0				
0				

NOTE: Expressions replaced by pointer after compilation.

FIGURE III. 5  
REGISTER DEFINITION BLOCK

0 = Data  
1 = New Cond. Command

TRANSLATE *	1 0 0 0 0 0 0 1	expressions
	0	
ROTATE *	1 0 0 0 0 0 1 0	expressions
	0	
NEW FRAME	1 0 0 0 0 0 1 1	ID
BLINK	1 0 0 0 0 1 0 0	
UNBLINK	1 0 0 0 0 1 0 1	
INTENSITY	1 0 0 0 0 1 1 0	0-7
TEXTURE	1 0 0 0 0 1 1 1	0-3
ON	1 0 0 0 1 0 0 0	
OFF	1 0 0 0 1 0 0 1	
SKIP N IF A = B	1 0 0 0 1 0 1 0	N
	0 A	B
SKIP N IF A > B	1 0 0 0 1 0 1 1	N
	0 A	B
SKIP N IF A ≥ B	1 0 0 0 1 1 0 0	N
	0 A	B
SKIP N IF A ≤ B	1 0 0 0 1 1 0 1	N
	0 A	B

FIGURE III.6  
CONDITIONING BLOCK FORMATS

SKIP N IF  $A \leq B$

1	0 0 0 1 1 1 0	N
0	A	B
1	0 0 0 1 1 1 1	N

SKIP N

END OF BLOCK

1	0 0 1 0 0 0 1	
---	---------------	--

CHANNEL

1	0 0 1 0 0 0 0	Channel Designation
---	---------------	---------------------

BLANK, LINE AND POINT \*

1	0 0 1 0 0 1 0	expressions
0		

DIGITAL READOUT

1	0 0 1 0 0 1 1	N
0	Readout Expression	

\*NOTE: Expressions replaced by pointer after compilation

FIGURE III.6 (Con't)  
CONDITIONING BLOCK FORMATS

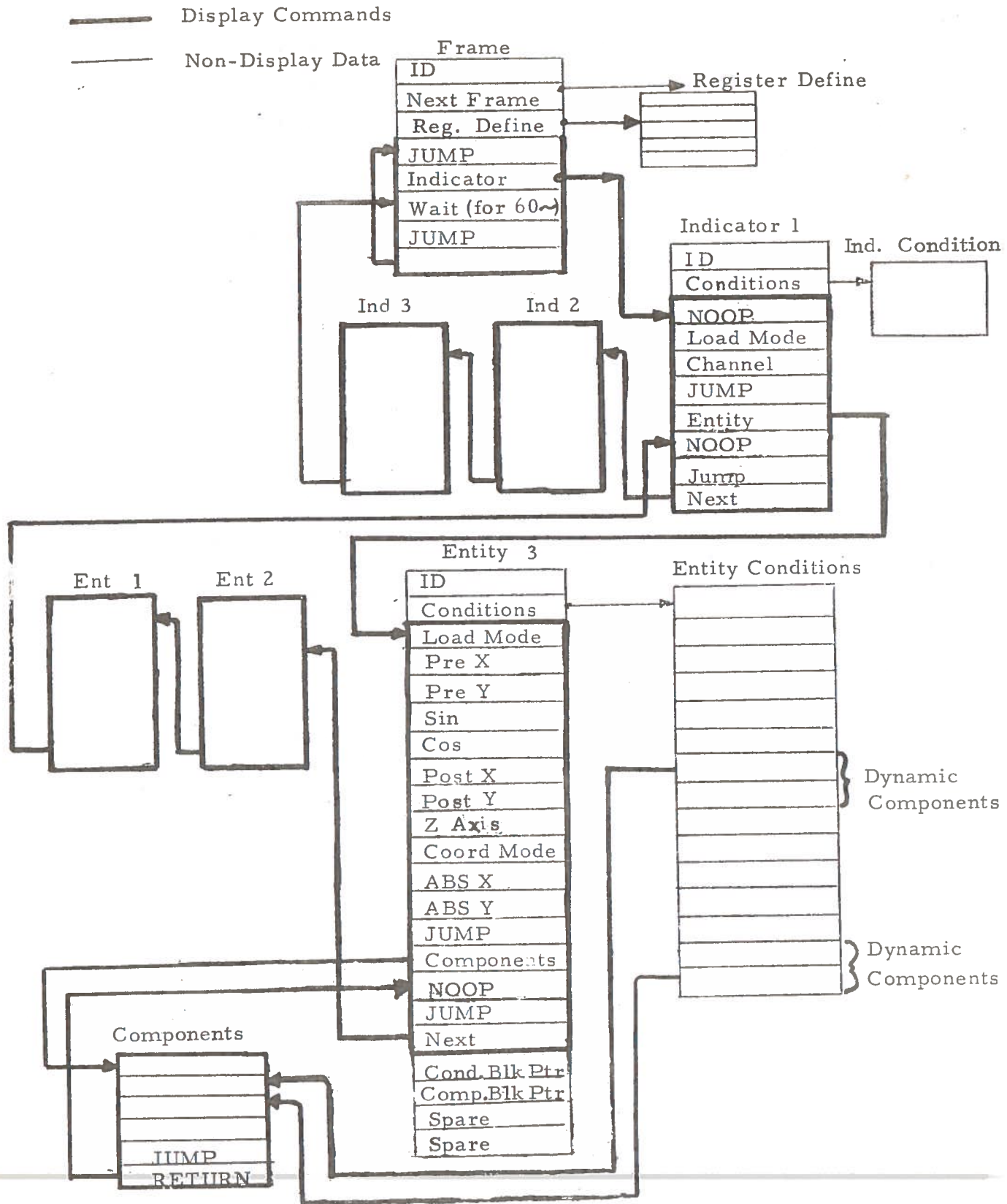
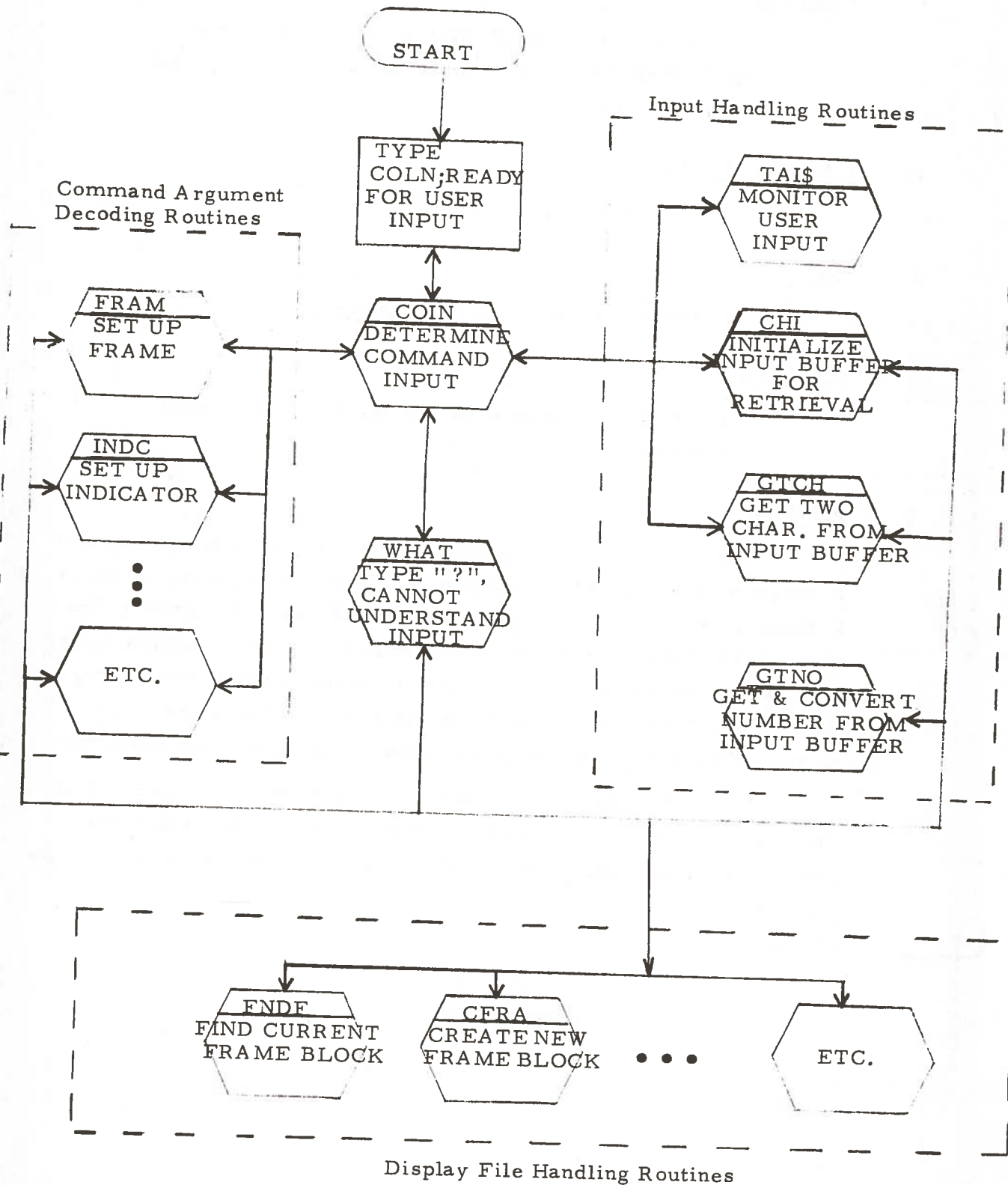


FIGURE III. 7  
HIERARCHICAL DATA STRUCTURE

# USER LANGUAGE ANALYZER

## Command Input Flowchart



## SUBROUTINE

## COIN

1. PURPOSE:

To determine command input from teletype and call appropriate routine to analyze and act on the inputted argument list.

2. CALLING SEQUENCE:

CALL COIN

3. INPUT:

70 character buffer with teletype input.

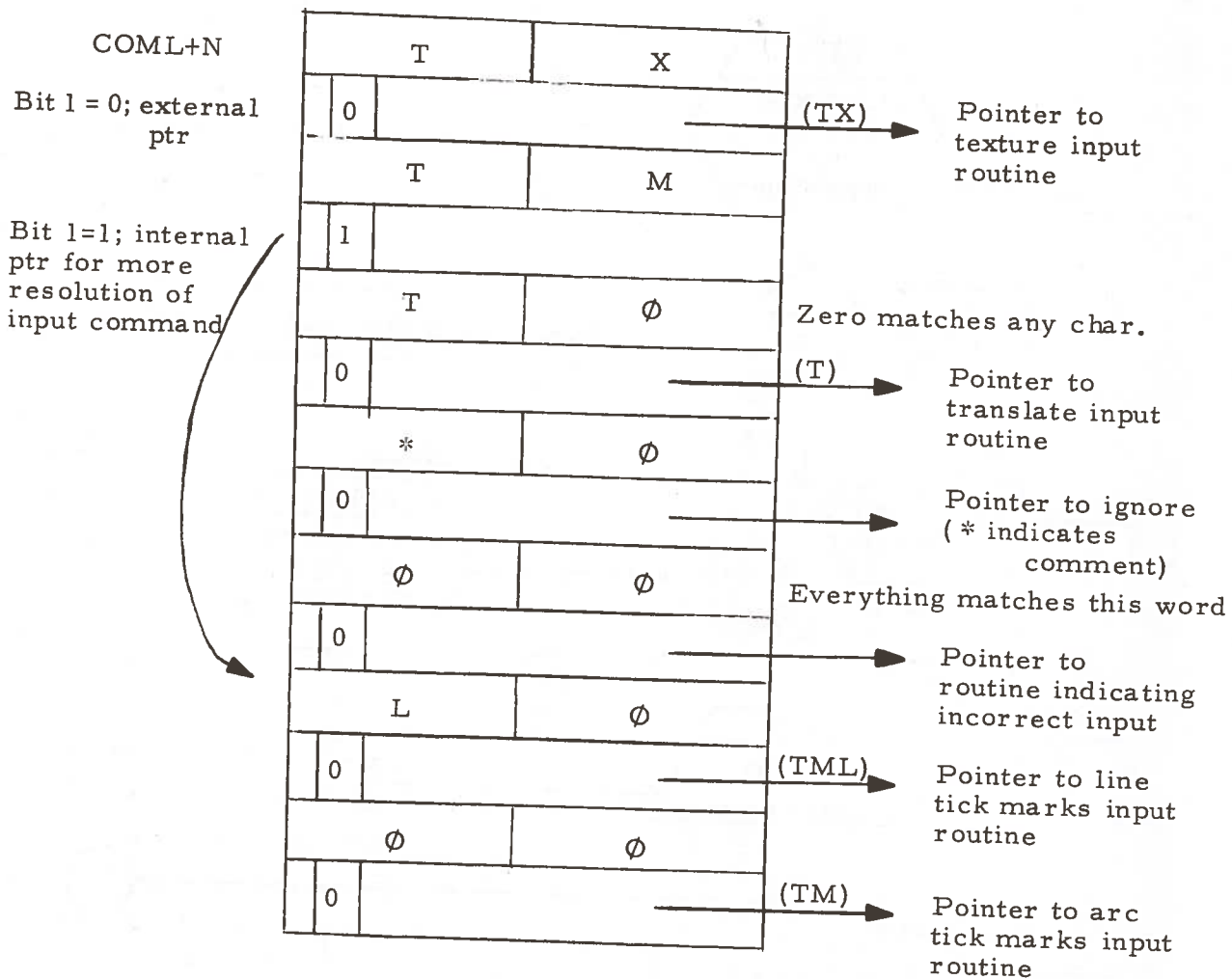
4. OUTPUT:

Calls to routine to analyze argument list of inputted command.

5. ACTION:

Call routine to get input from teletype. Gets two characters from buffer and compares until a match in the command list (COML) is made. (A half-word of zeroes matches with any character.) The next word in the command list after a matched word is examined. If bit 2 is set, further resolution of the command is made by checking the next two characters in the buffer against the characters in the portion of the list pointed to by the word following the matched word.

If bit 2 of the word following the matched word is zero, then the word is a pointer to an external routine, and control calls that routine, after locating a space, tab, comma, or carriage return in the input string.



Example of what COML will look like

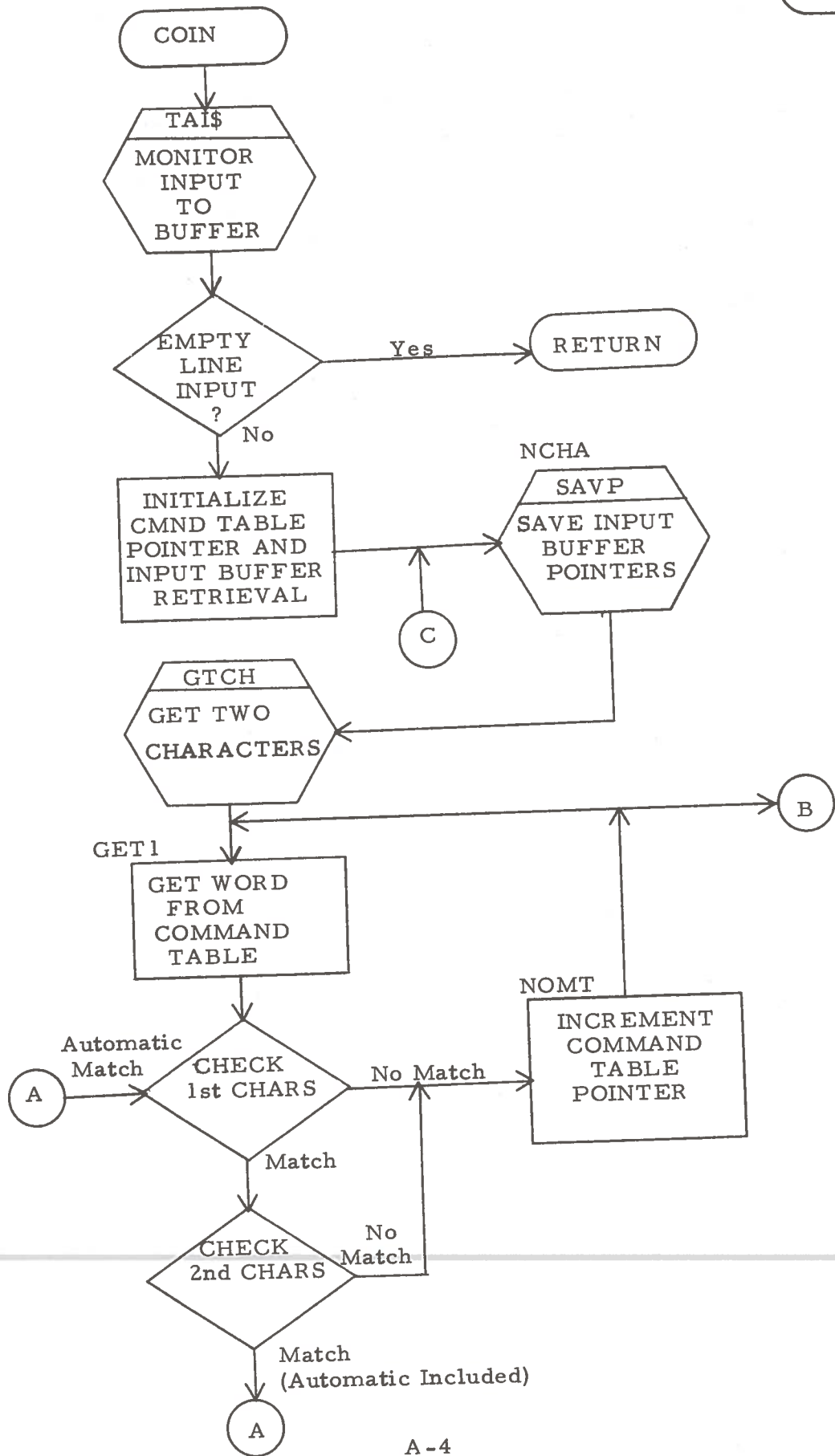
Figure 1

6. EXTERNAL REFERENCES:

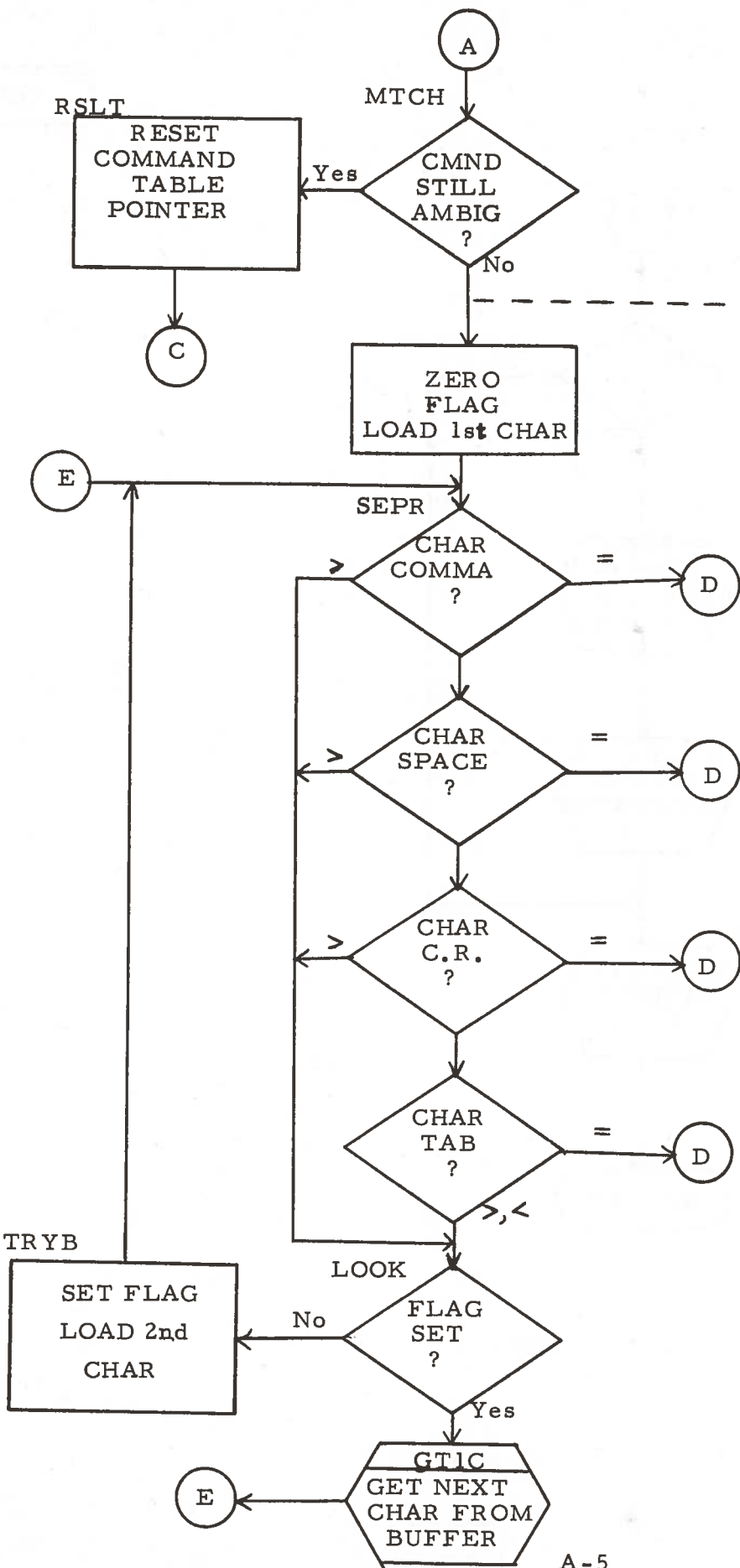
- TAI\$
- CHBF
- CHI
- GTCH
- COML
- GT1C

7. CORE USED: 122<sub>8</sub>

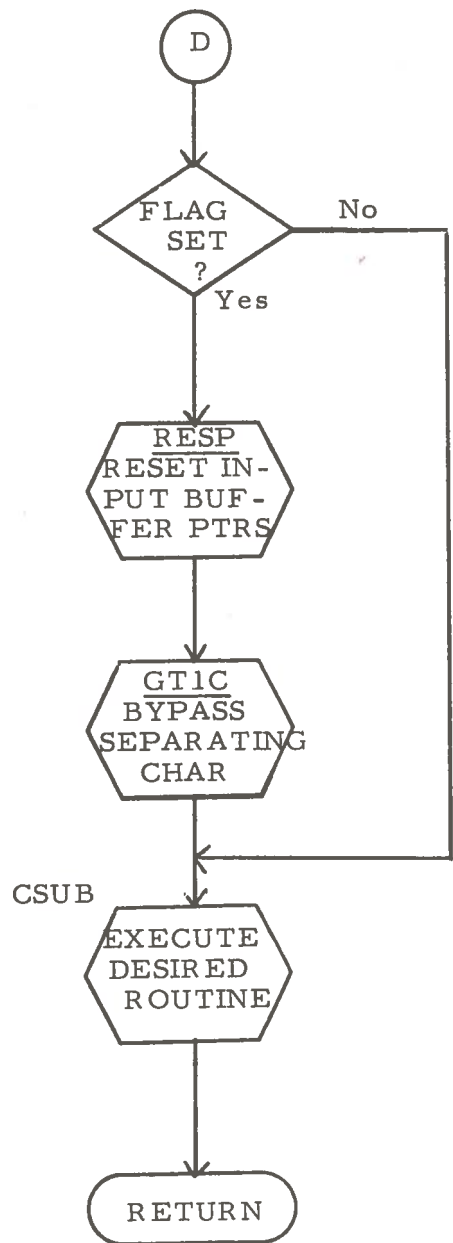




COIN



GET COMMAND SEPARATION BEFORE GOING TO DESIRED ROUTINE



SUBROUTINE

COML

Command List

1. PURPOSE:

Buffer containing characters to be matched against inputted commands and containing pointers to subroutines to analyze arguments. (Description of buffer structure is write-up for Subroutine COIN.)

2. ENTRY POINT:

COML

3. INPUT:

None

4. OUTPUT:

None

5. ACTION:

None

6. EXTERNAL REFERENCES:

Pointers to subroutines to be called when command is interpreted.

7. CORE USED:

172<sub>8</sub>

SUBROUTINE                      COLN

1.    PURPOSE:

Types colon to indicate it is ready for user input.

2.    CALLING SEQUENCE:

Not normally called. Main executive R+N.

3.    INPUT:

None.

4.    OUTPUT:

Outputs colon on teletype.

5.    ACTION:

Outputs colon on teletype, turns on ADDS if off and CLVL 70,  
then calls routine to interpret input commands.

6.    EXTERNAL REFERENCES:

TAO1

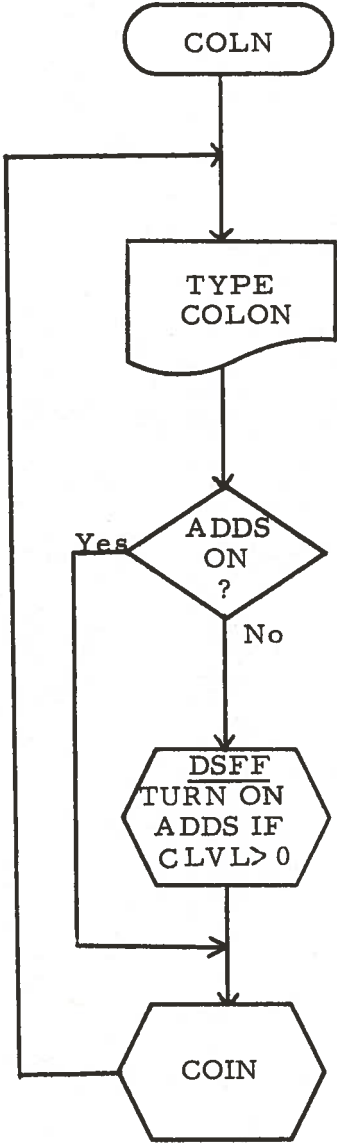
COIN

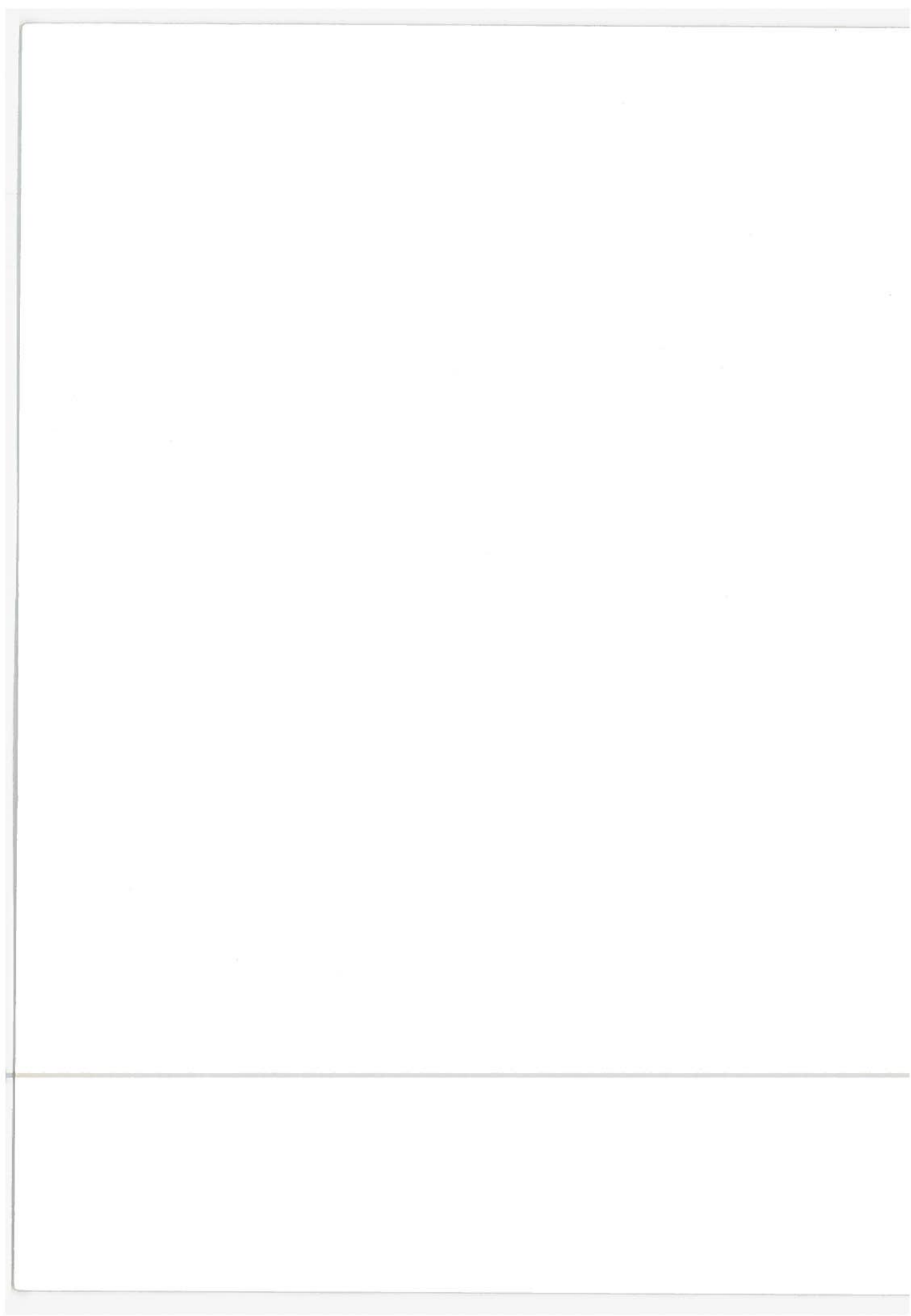
DSFF

7.    CORE USED:

11<sub>8</sub>

COLN





SUBROUTINE:

CENT

1. PURPOSE:

Create an entity block and tie it to the appropriate entity ring.

2. CALLING SEQUENCE:

Call CENT

3. INPUT:

Indicator address in INAD entity ID in ENID

4. OUTPUT:

Start address of frame block in AC/zero in AC if no storage.

5. ACTION:

A block is retrieved from free storage, set up as an entity, attached to the appropriate entity ring and the entity address is returned in the AC.

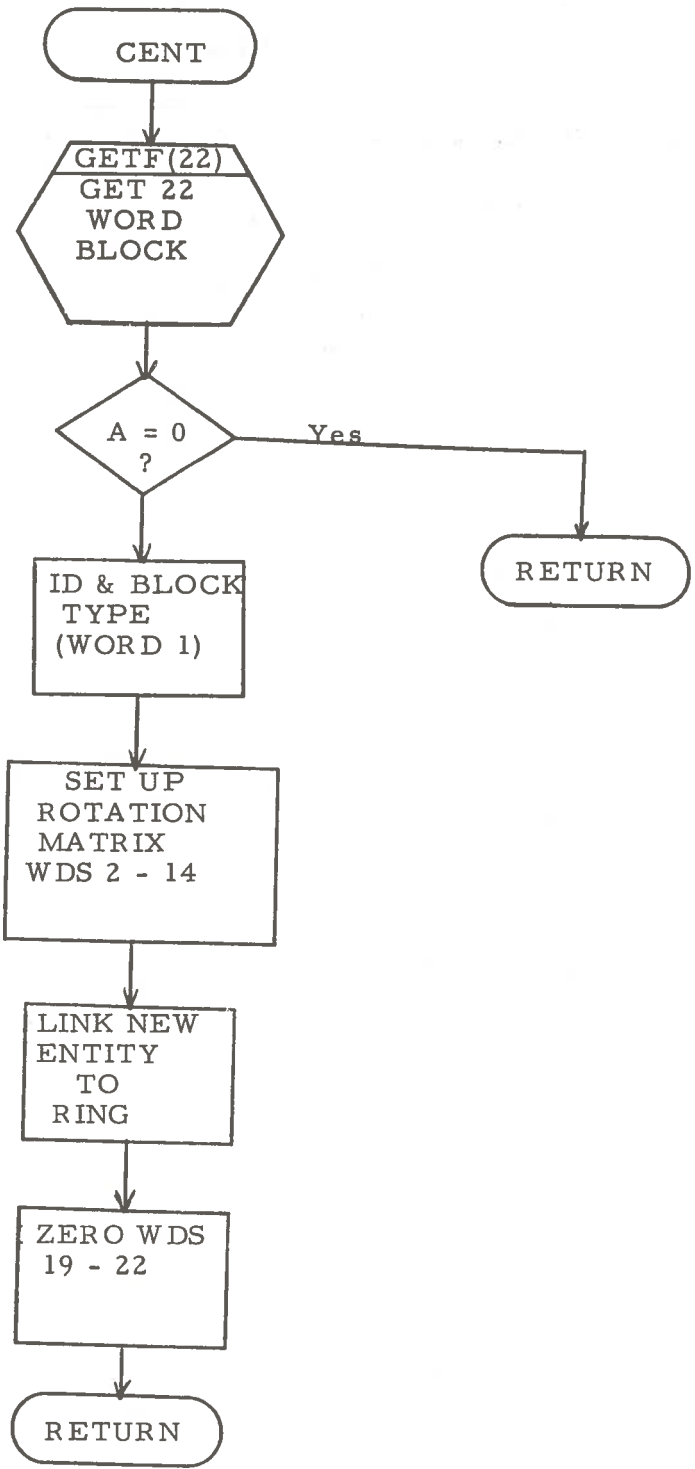
6. EXTERNAL REFERENCES:

INAD  
ENID

7. CORE USED:

117<sub>8</sub>





SUBROUTINE

CFRM

1. PURPOSE:

Create a frame block and tie it to frame ring.

3. CALLING SEQUENCE:

Call CFRM

3. INPUT:

Frame ID in FRID

4. OUTPUT:

Start address of frame block in A / zero in A if no storage.

5. ACTION :

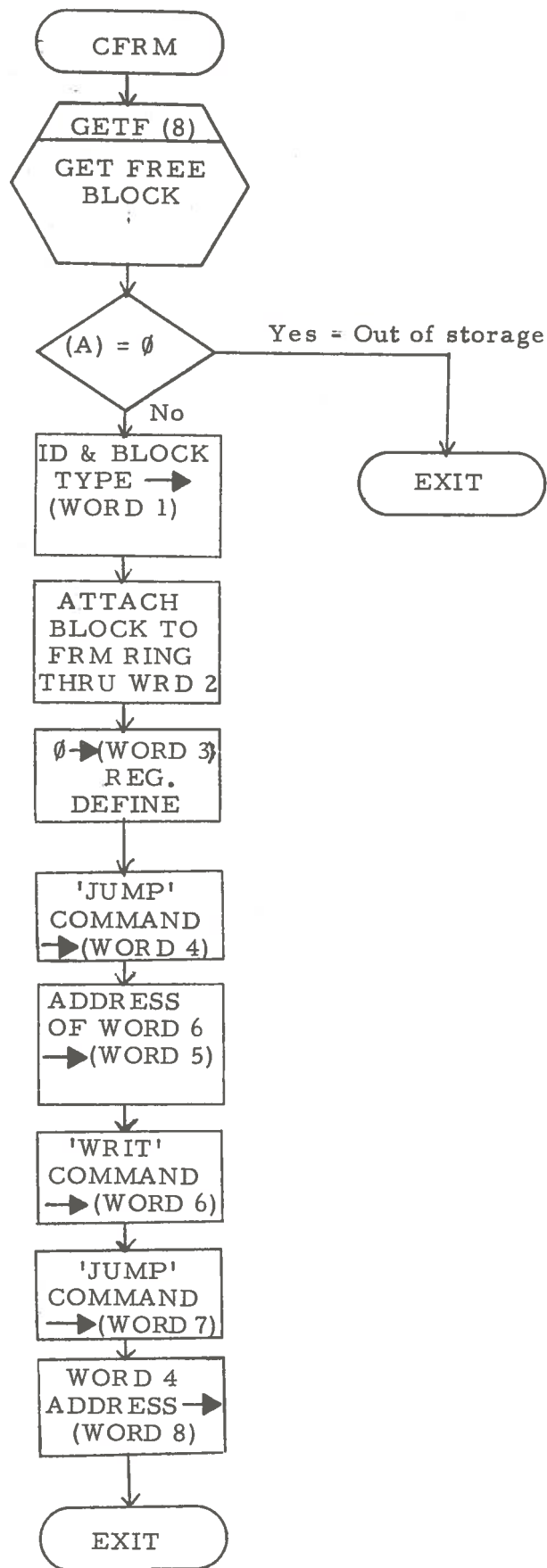
An eight word block is retrieved from free storage, set up as a frame block and attached to the frame ring. The starting address of the frame is returned in the A Register.

6. EXTERNAL REFERENCES:

FRID Frame ID  
FRHD Frame Header

7. CORE USED:

57<sub>8</sub>



## SUBROUTINE

## CHAD

1. PURPOSE:

Converts 16-bit word to left adjusted table of Decimal characters.

2. CALLING SEQUENCE:

LDA        NUM            / 16-bit word to be converted  
CALL       CHAD  
LOC        TABL           / Pointer to table where chars  
                                 are to be packed

Returns with the number of chars in the A-register

3. INPUT:

- a) An octal integer in the A-register
- b) Pointer to 4-word table following call statement

4. OUTPUT:

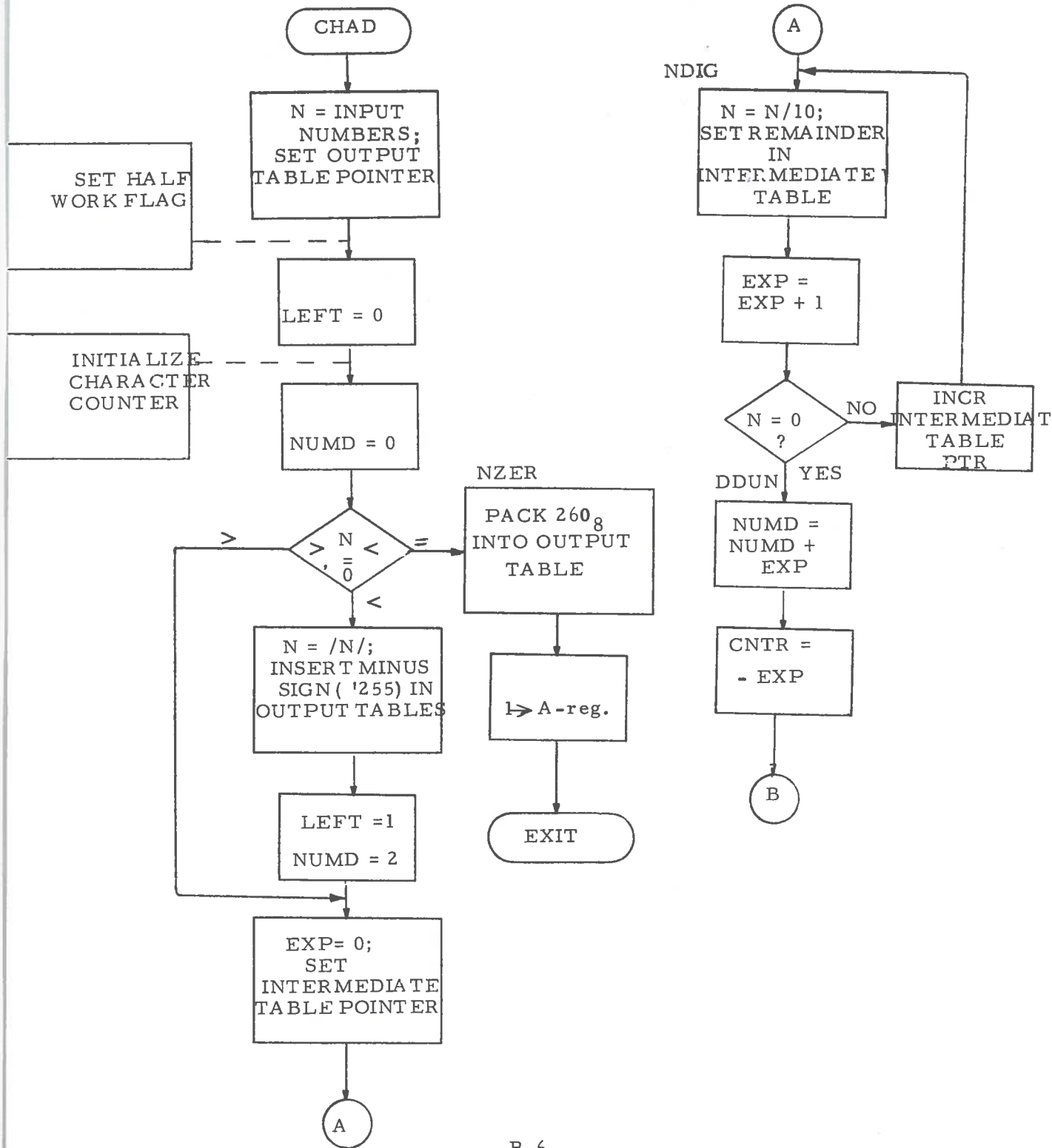
Table of packed decimal characters. Character count in A-register. If odd number of characters, right half of last word contains the null character.

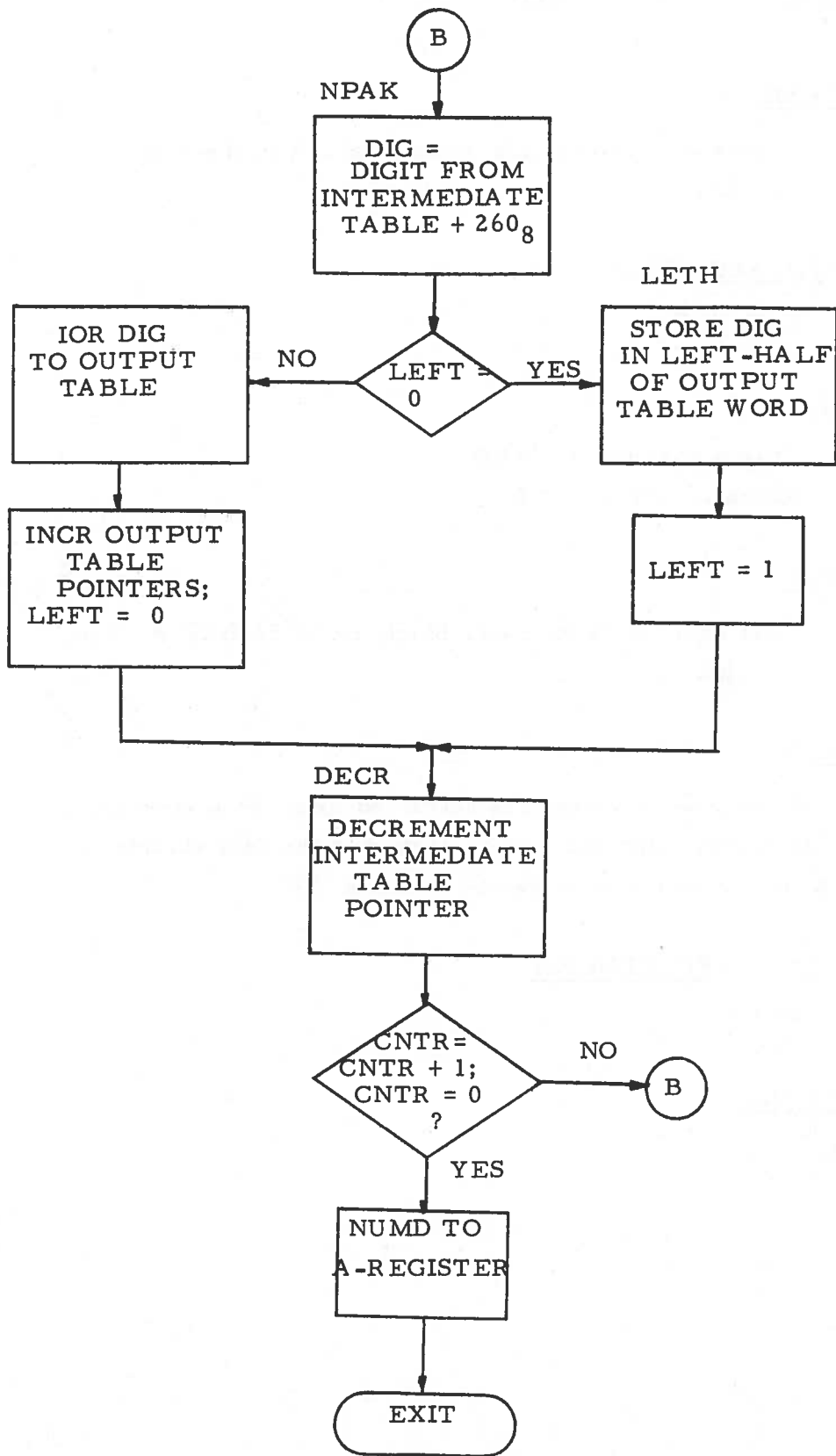
5. ACTION:

Places minus sign in table if number is negative and complements number. If number is zero, packs zero character and returns. Otherwise breaks number down into base ten digits in an intermediate table. Then, backing through intermediate table, the proper char for each digit is packed into the given table. When finished it returns to the calling program.

6. EXTERNAL REFERENCES:    None7. CORE USED:            132<sub>8</sub>

CHAD  
Flowchart





SUBROUTINE: CIND

1. PURPOSE:

Create an indicator block and attach it to the data structure.

2. CALLING SEQUENCE:

Call CIND

3. INPUT:

Frame address in FRAD

Indicator ID in INID

4. OUTPUT:

Start address of indicator block in A / zero in A if no storage.

5. ACTION:

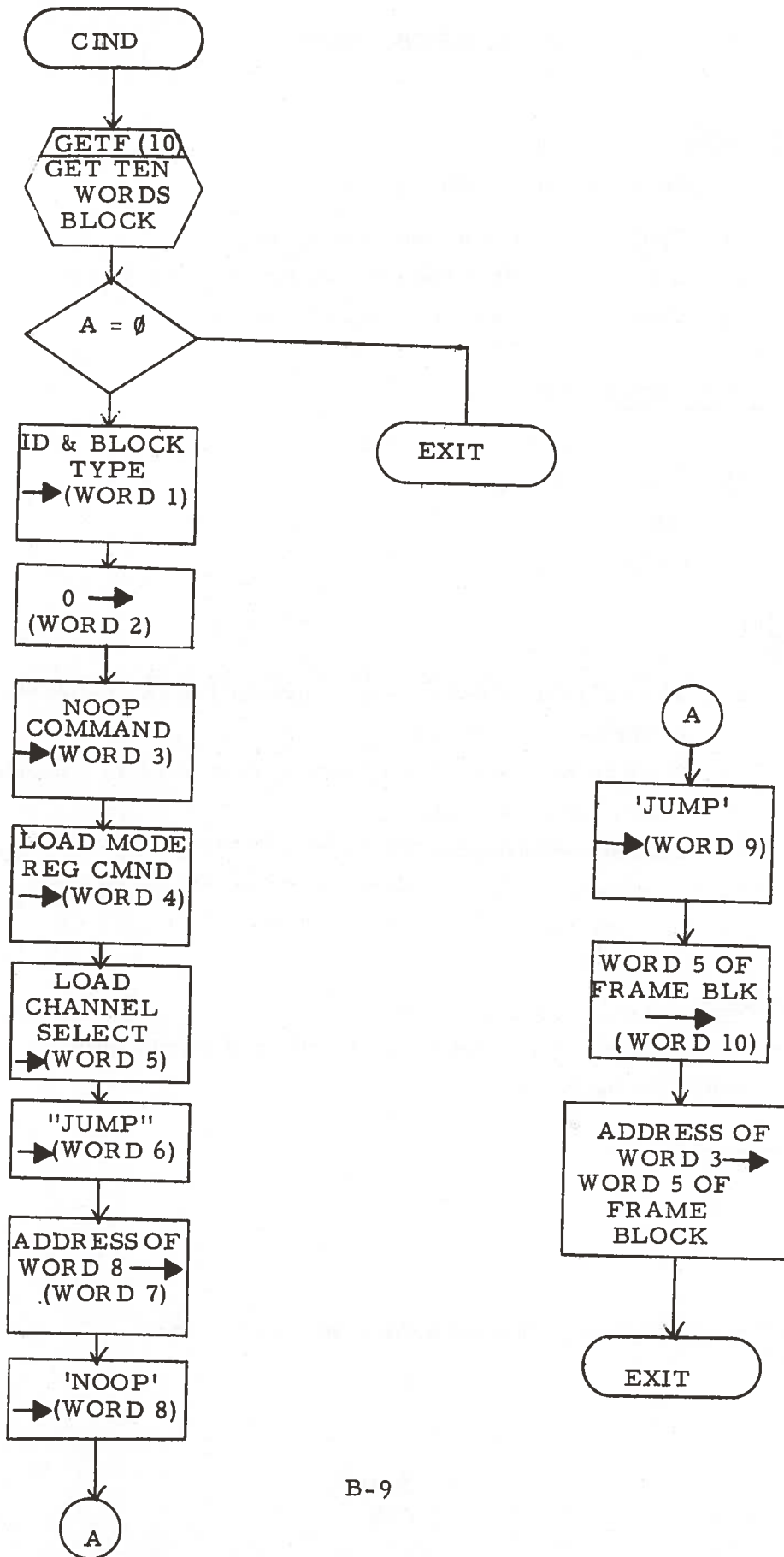
A block of 10 words is retrieved from free storage, set up as an indicator block and attached to the data structure. The SA of the indicator is returned in the AC.

6. EXTERNAL REFERENCES:

FRAD  
INID

7. CORE USED:

64<sub>8</sub>





SUBROUTINE: CLCB, OPCB, INCB

1. PURPOSE:

Handlers for conditioning blocks. \*

- a) OPCB - Opens a conditioning block
- b) INCB - Inserts a word into conditioning block
- c) CLCB - closes a conditioning block

2. CALLING SEQUENCE:

- a) CALL OPCB
- b) LDA WORD  
CALL INCB
- c) CALL CLCB

3. INPUT:

- a) The current working level, current frame, indicator or entity block, free storage
- b) Word to be inserted, location of next word in conditioning block. Free storage
- c) Current working level.

4. OUTPUT:

Set or reset parameters for insertion of words into conditioning block.

\* Define Register block is treated as a conditioning block in Phase 1.

5. ACTION:

a) OPCB - If no conditioning block yet assigned (i. e., conditioning block pointer or register definition block pointer is zero) a  $40_8$  word block is retrieved from free storage and a pointer to it is inserted into the appropriate header. If a previous conditioning block exists, the  $40_8$  word block is linked to the last block.

b) INCB - Store word in A-Reg. in next available conditioning block word. If the block is full (except for two words used for return or linking), a new block is retrieved from free storage and linked to the old by inserting an "end" command and calling OPCB.

c) CLCB - If no words have been inserted into the conditioning block, the entire block is returned to free storage. Otherwise the end code is inserted into the next available word followed by a zero and this address is stored in word 2 of the conditioning block. The remainder of the block is then returned to free storage.

6. EXTERNAL REFERENCES:

FLG2 - Set if Conditioning Block Open

CLVL - Current Level

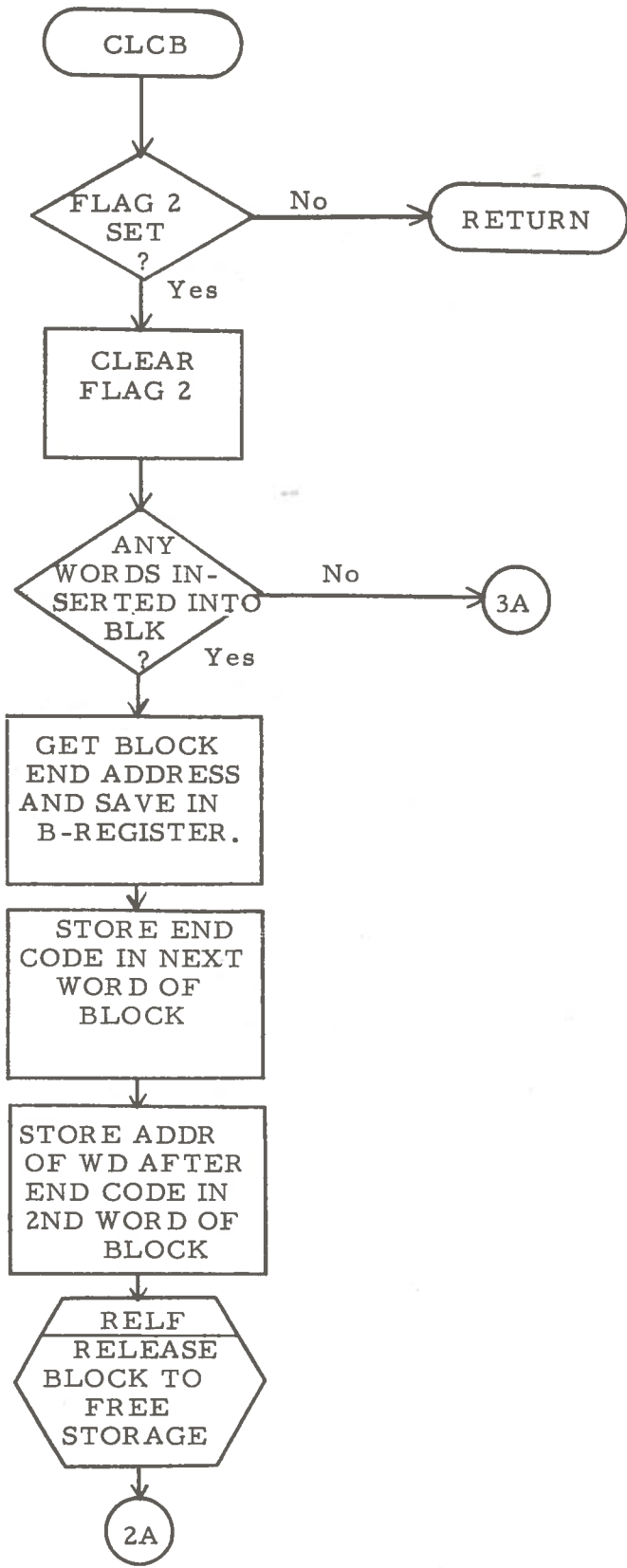
FRAD - Frame Address

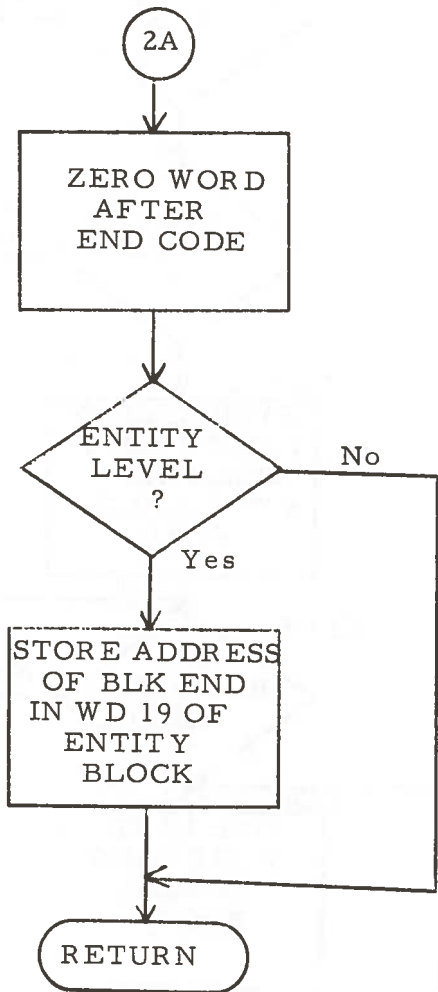
INAD - Indicator Address

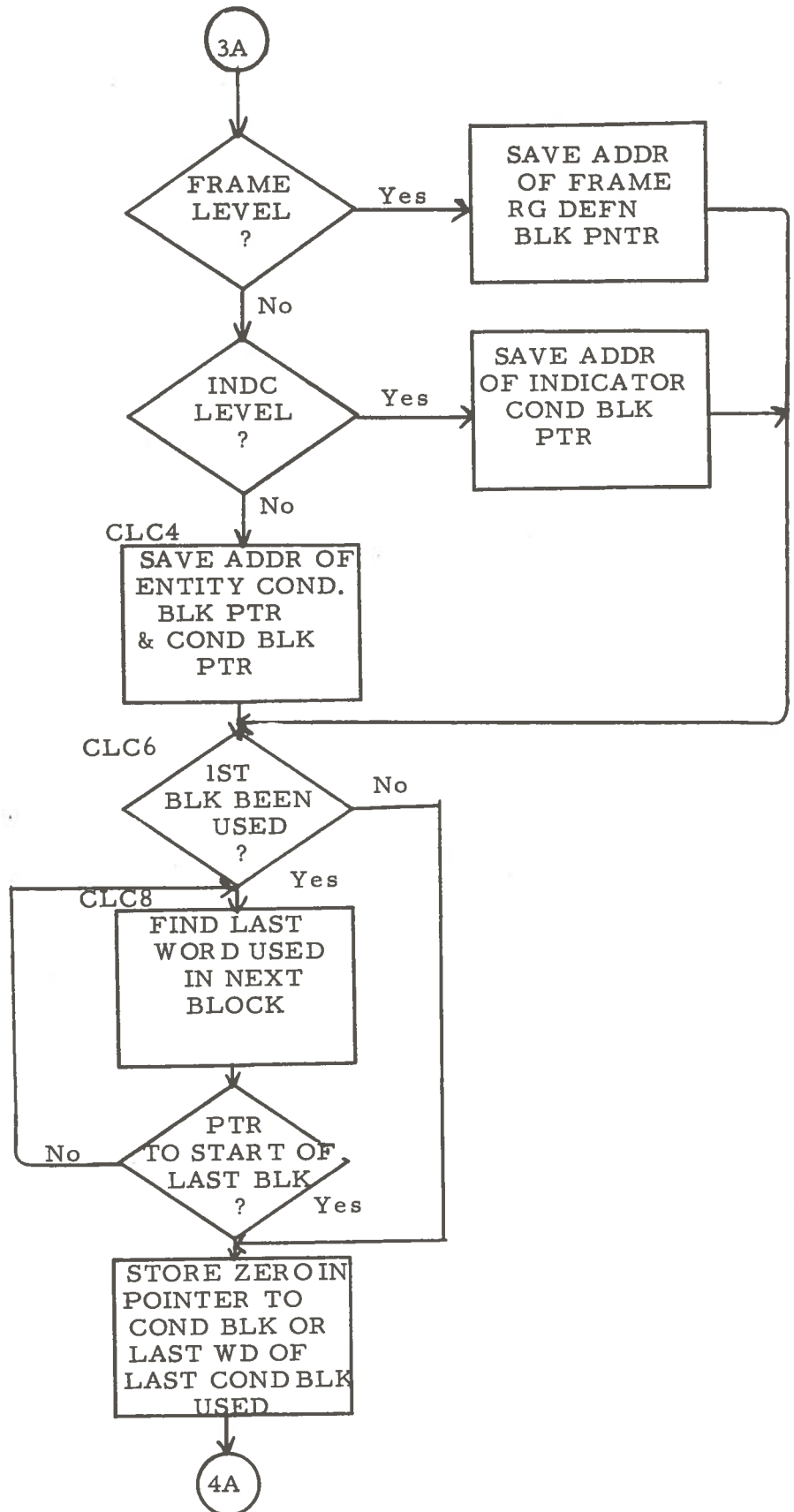
ENAD - Entity Address

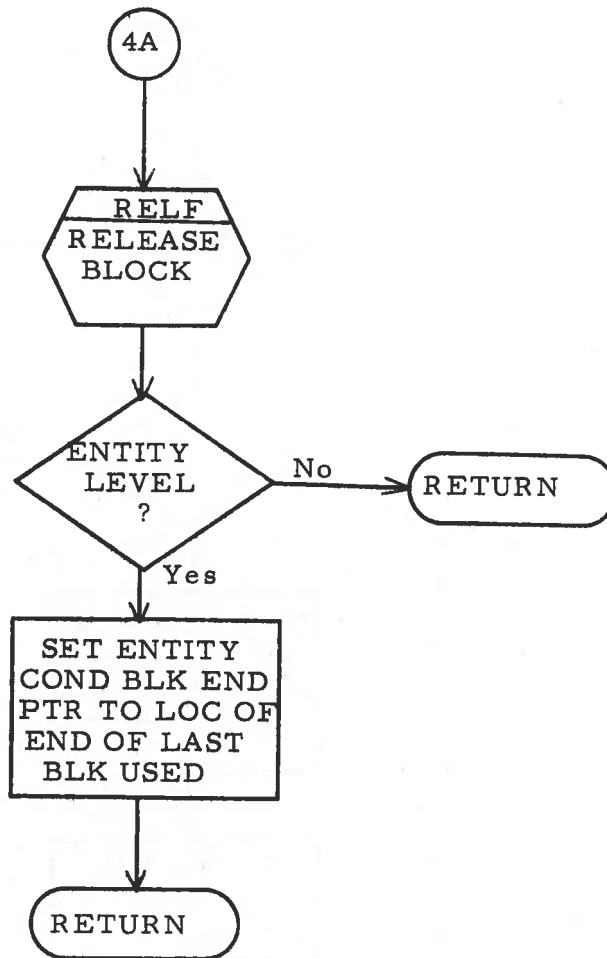
7. CORE USED:

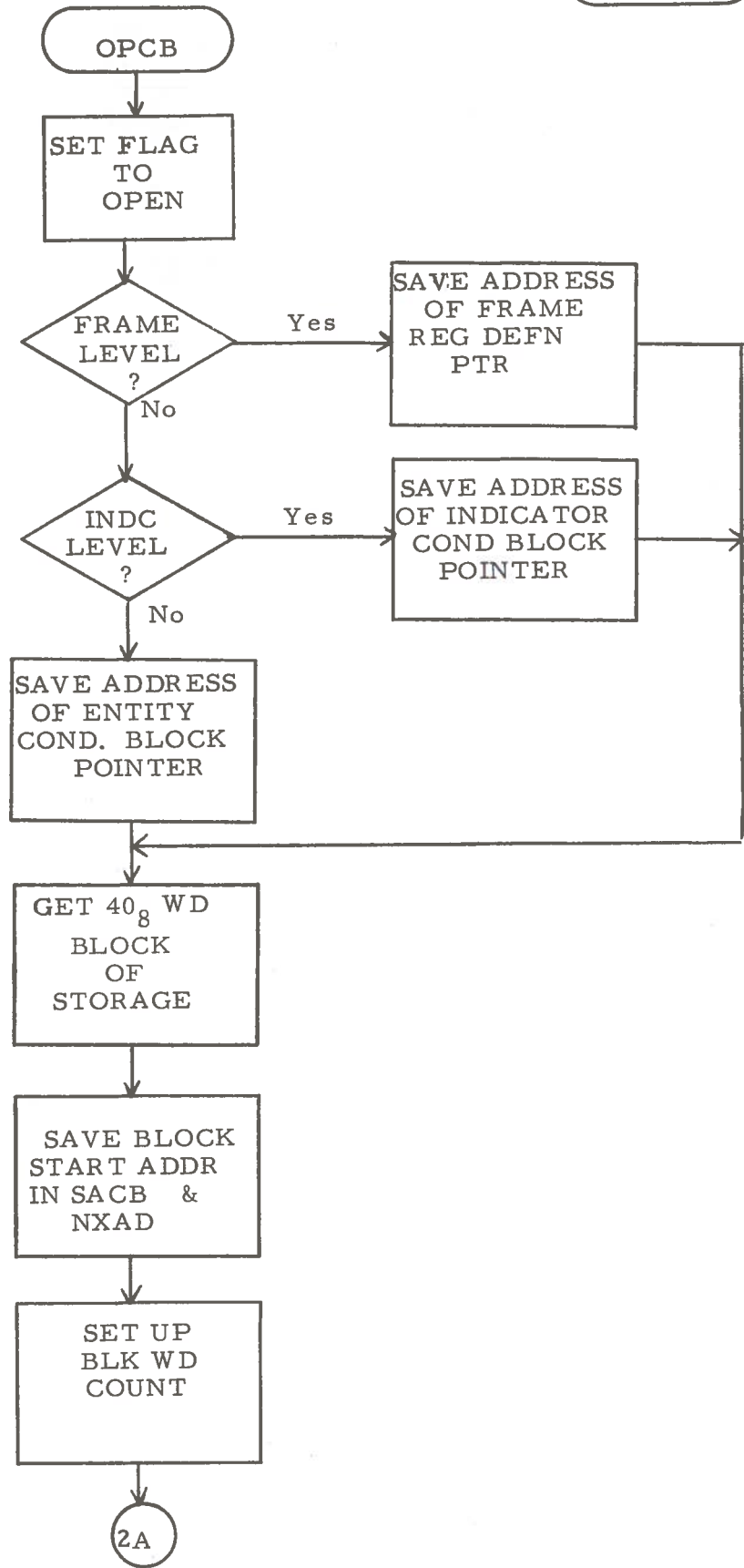
$261_8$

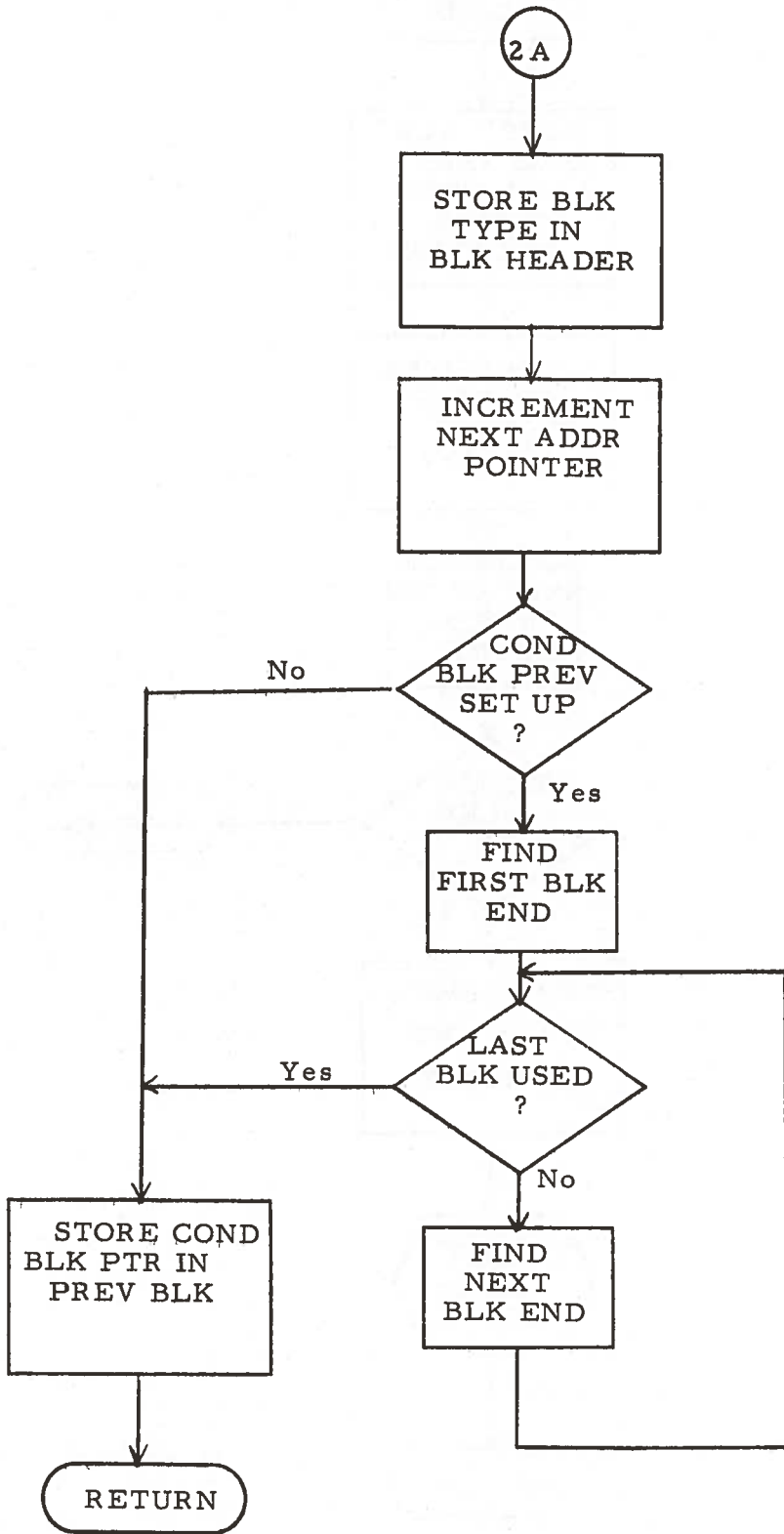




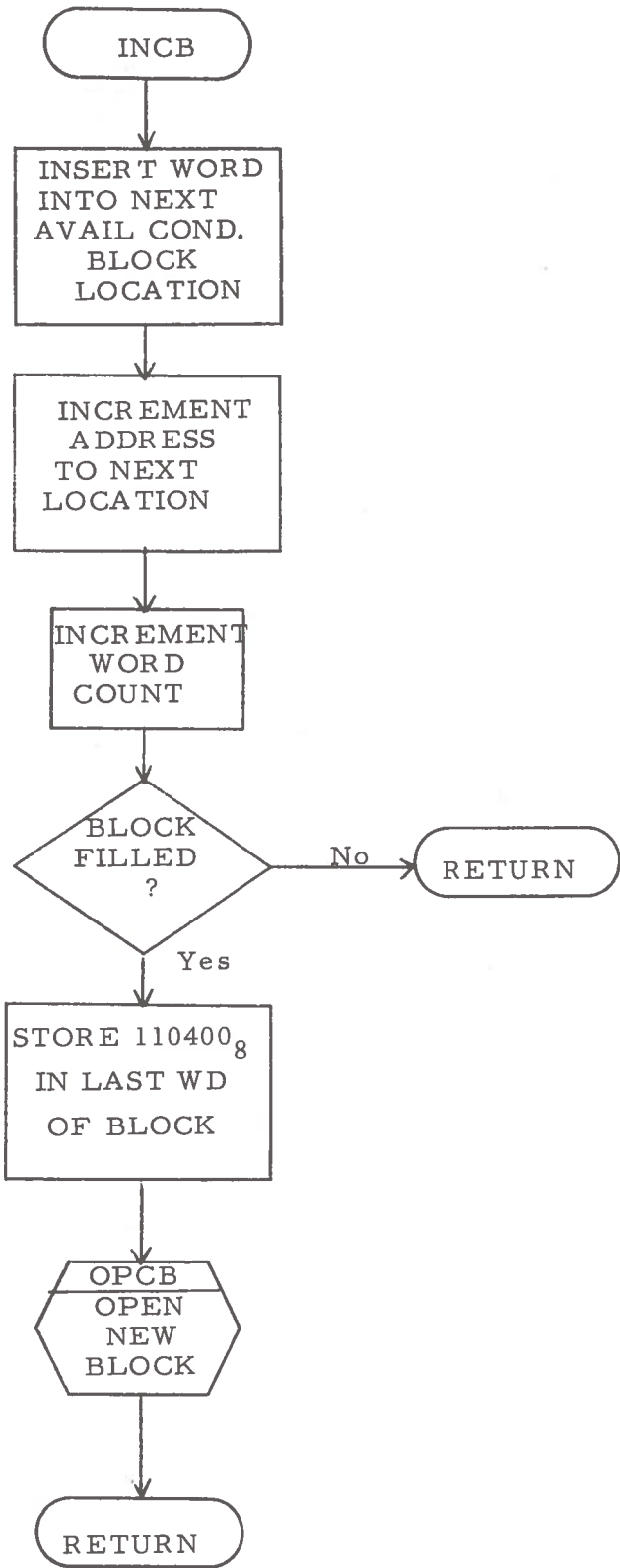












SUBROUTINE           CMOD

1.    PURPOSE:

        Check working level.

2.    CALLING SEQUENCE:

```
          LDA        MODE
          CALL       CMOD
          ***                    / Error Return
          ***                    / Normal Return
```

3.    INPUT:

        Mode desired is indicated by bits 14-16 of A register.

```
          Bit 14 - 0, Entity level illegal
                  1, Entity level legal
          Bit 15 - 0, Indicator level illegal
                  1, Indicator level legal
          Bit 16 - 0, Frame level illegal
                  1, Frame level legal
```

4.    OUTPUT:

        If illegal level, error message is typed.

5.    ACTION:

        The mode indicator (input) is shifted right n times, where n is the current level. If bit 16 is one, after shift, level is correct and a normal return is executed. Otherwise an error message is typed and an error return is made.

6.    EXTERNAL REFERENCES:

```
          TAO$
          CLVL
```

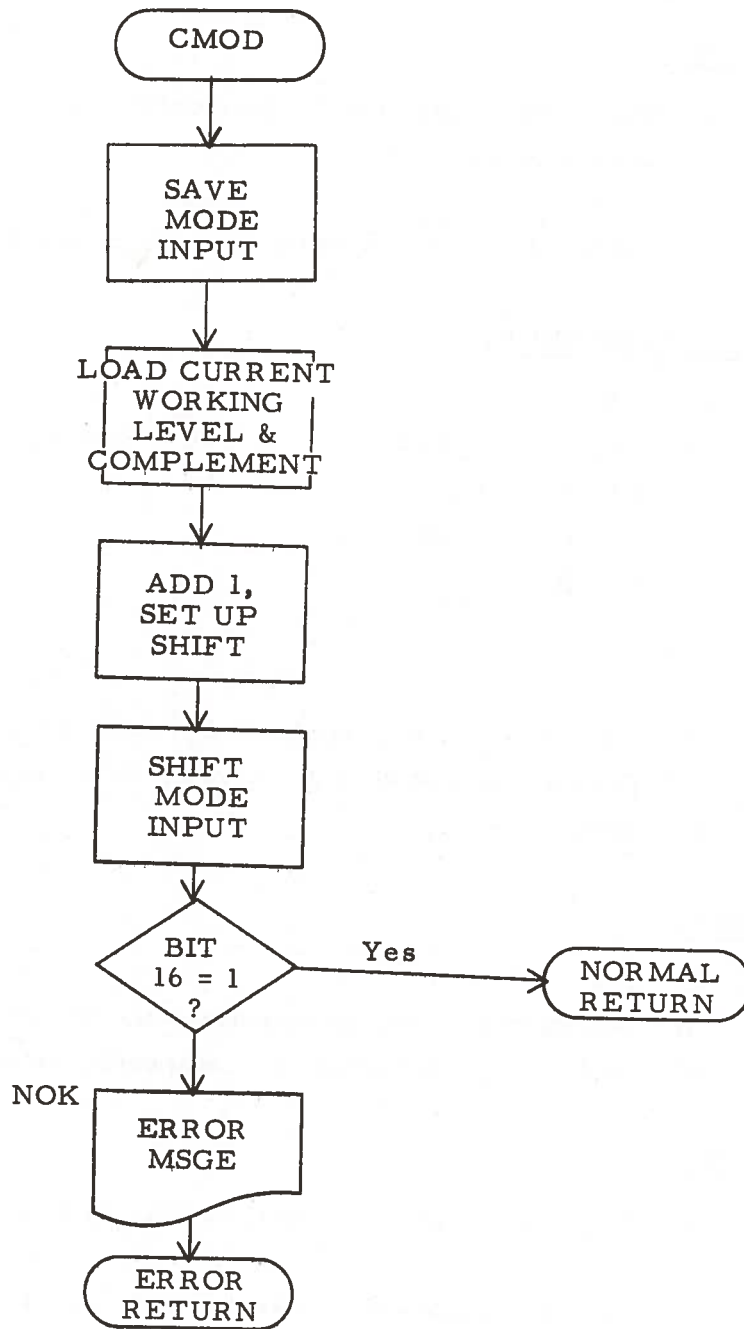
7.    NOTE:

```
          CLVL = 1 - Frame level
                  2 - Indicator level
                  4 - Entity level
```

8. CORE USED:

$42_8$

CMOD



SUBROUTINE: COP, CIN, CLO, CPAD

1. PURPOSE:

- a) Setup for display command insertion into component block
- b) Insert display command
- c) Reset
- d) Address of next available component word

2. CALLING SEQUENCE:

- a) CALL COP
- b) LDA COMN / load display command  
CALL CIN
- c) CALL CLO
- d) CPAD -

3. INPUT:

- a) Address of next available component word
- b) Display command to be inserted in A-register
- c) None

4. OUTPUT:

- a) None
- b) Display command is inserted into component block
- c) Address of next available component word is reset

5. ACTION:

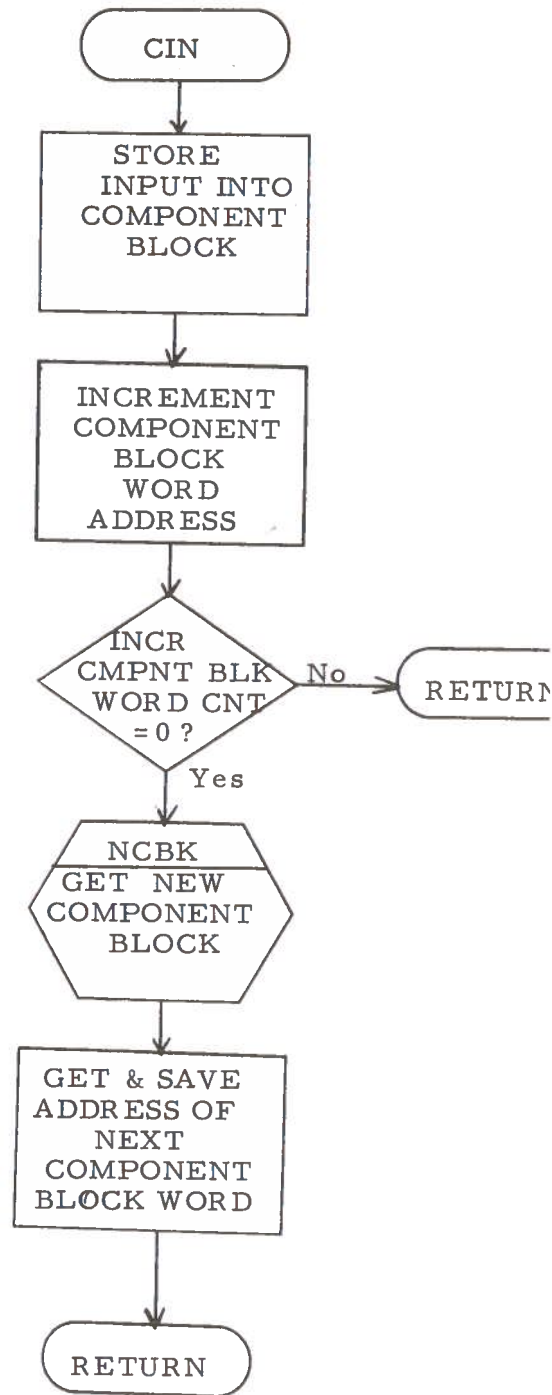
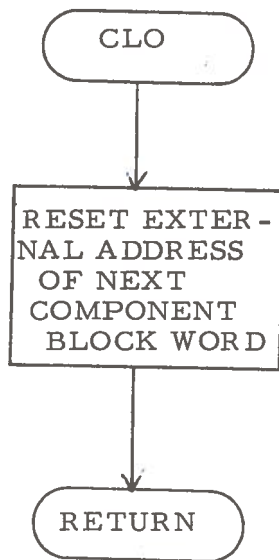
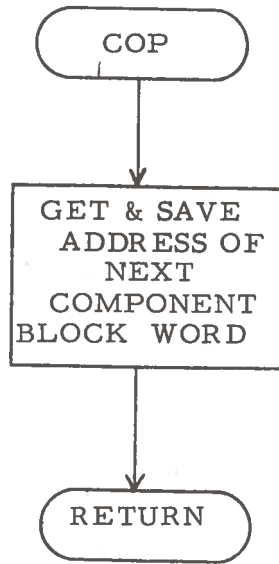
- a) External pointer, ACWD, is retrieved and set up internally
- b) Display command is inserted into next available component word. Internal pointer is incremented. Component word counter is incremented. If needed, a new component block is created.
- c) External pointer, ACWD, is updated.

6. EXTERNAL REFERENCES:

NCBK	Create new component block
ACWD	Next available component word address
NCWD	Two's complement of words remaining in component block

7. CORE USED:

27<sub>8</sub>



SUBROUTINE:

FNDE

1. PURPOSE:

Locate a particular entity on an indicator

2. CALLING SEQUENCE:

Call FNDE

3. INPUT:

Entity ID in ENID

Indicator ID in INID

Frame ID in FRID

4. OUTPUT:

Starting address of entity block in A;  $\emptyset$  in A if block not found.

5. ACTION:

FNDE is called to get the indicator address. The entities of the indicators are searched for one with the given ID. The start address is returned in the A, if the indicator is found; if the indicator is not found then  $\emptyset$  is returned in the A.

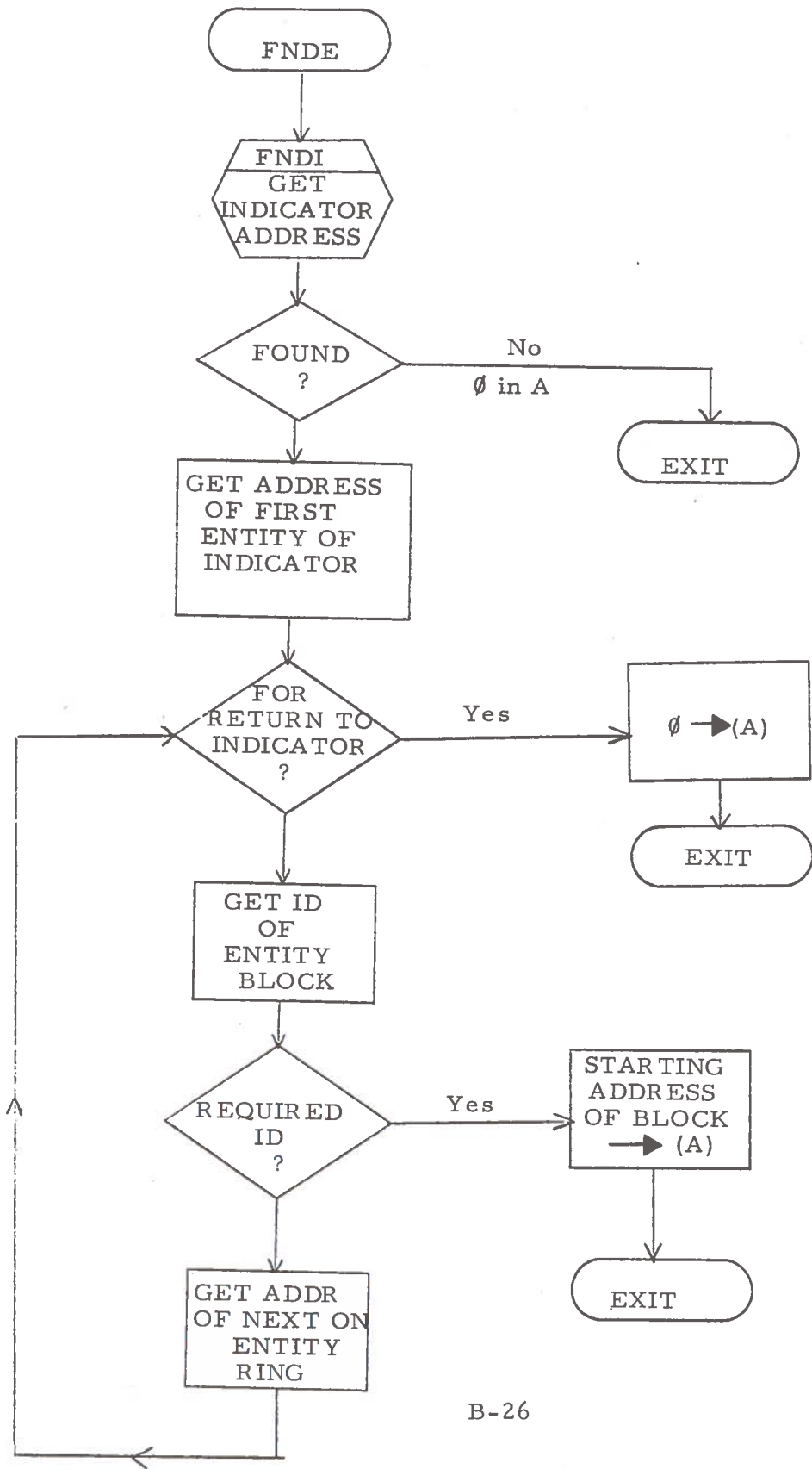
6. EXTERNAL REFERENCES

ENID, COLN, LEAD

7. CORE USED

55<sub>8</sub>





SUBROUTINE:

FNDF

1. PURPOSE:

Locate a particular block on the frame ring.

2. CALLING SEQUENCE:

Call FNDF

3. INPUT:

ID of frame in FRID

4. OUTPUT:

Starting address of frame block in A;  $\emptyset$  in A if block not found

5. ACTION:

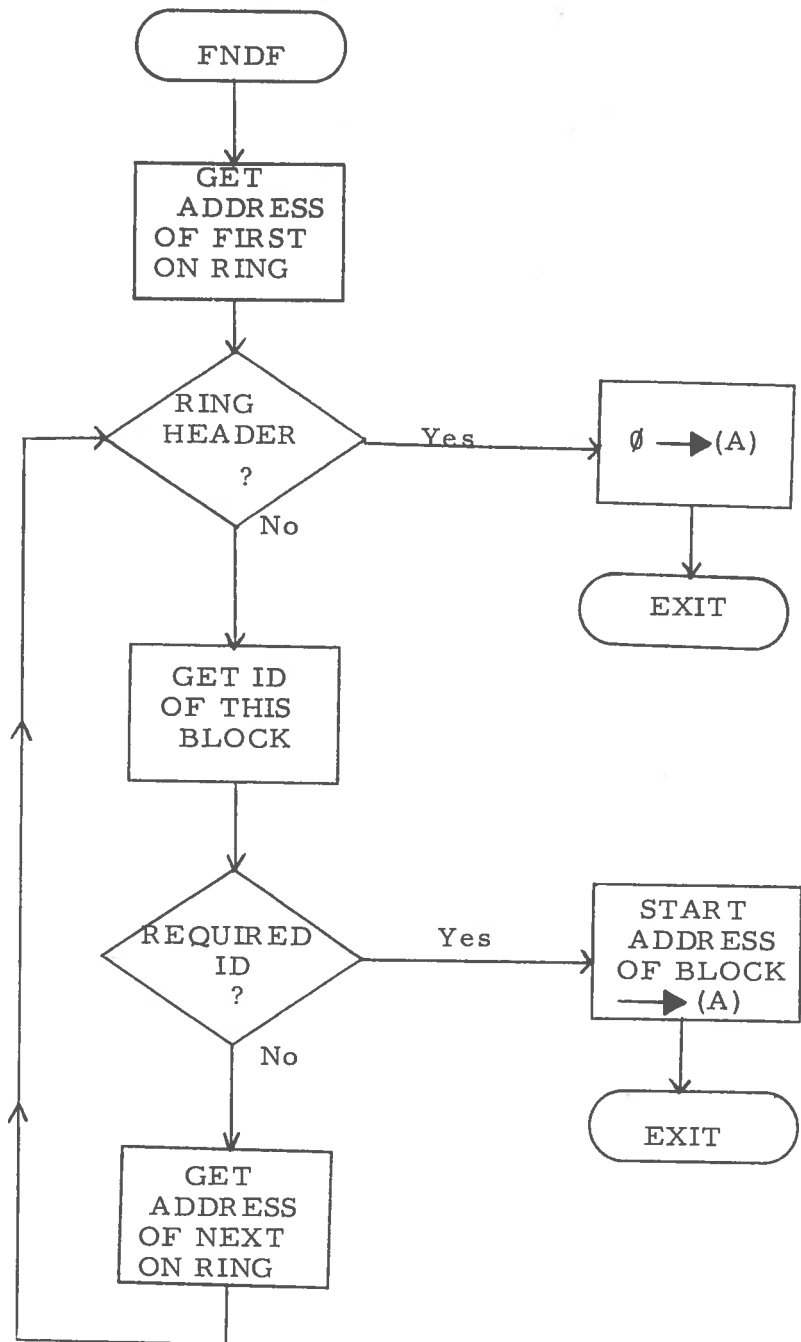
The frame ring is searched for a block with the given ID. If the block is found, the starting address is returned in the A. If the block is not found then  $\emptyset$  is returned in the A register.

6. EXTERNAL REFERENCES:

FRHD, FPNT, FRID, LFAD

7. CORE USED:

$40_8$



SUBROUTINE:

FNDI

1. PURPOSE:

Locate a particular indicator block on a frame.

2. CALLING SEQUENCE:

Call FNDI

3. INPUT:

Indicator ID in INID

4. OUTPUT:

Starting address of indicator block in A;  $\emptyset$  in A if block not found.

5. ACTION:

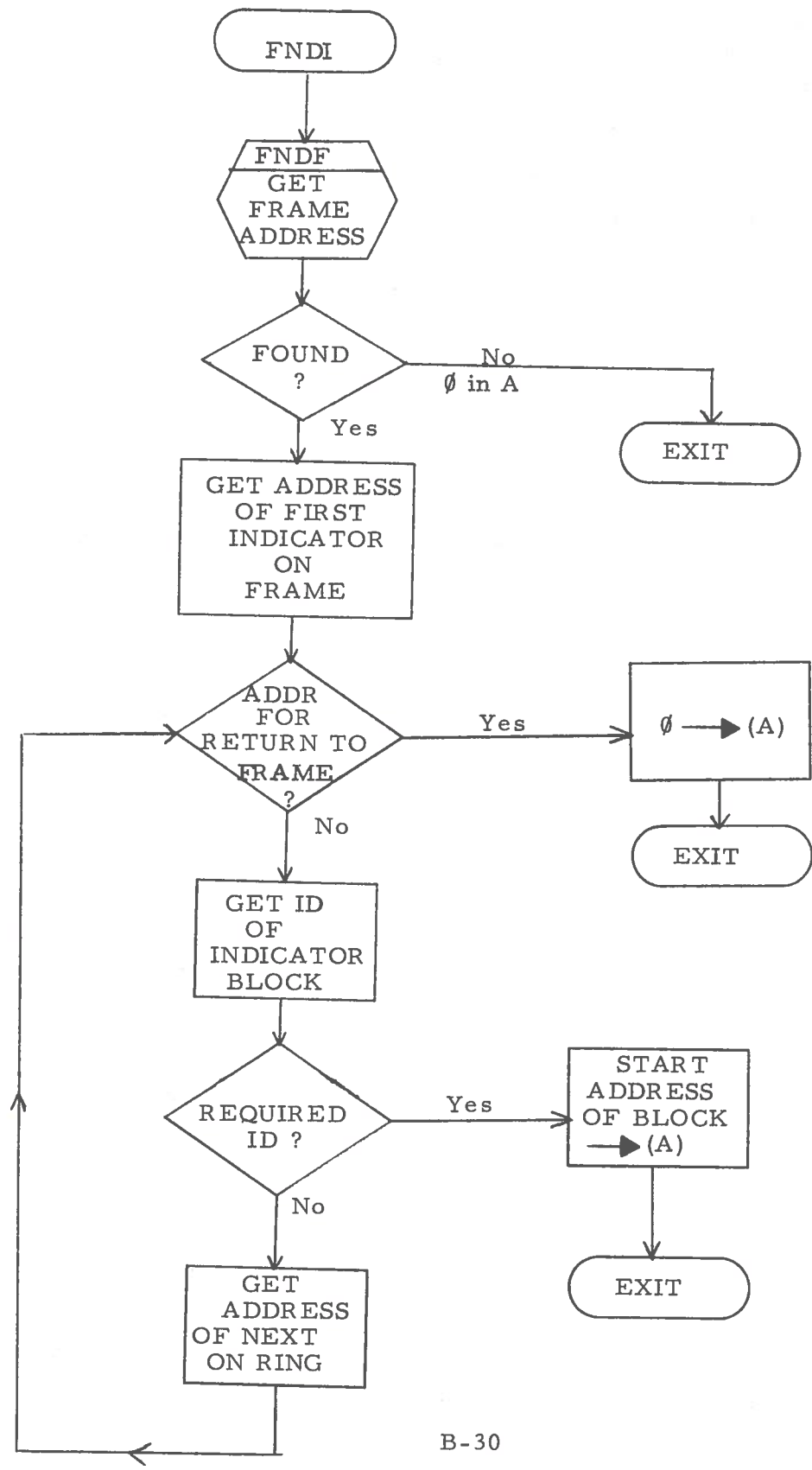
FNDF is called to get the frame address. The indicators of the frame are searched for one with the given ID. If not found  $\emptyset$  is returned in the AC, otherwise the start address is returned in the AC.

6. EXTERNAL REFERENCES

INID, COLN, LIAD

7. CORE USED:

55<sub>8</sub>



SUBROUTINE

GABN, SKER

1. PURPOSE:

Get Registers A, B and number of words to skip and insert into conditional block.

2. CALLING SEQUENCE:

- a) LDA CODE /load skip code  
CALL GABN
- b) CALL SKER

3. INPUT:

- a) Skip code in A-Reg. ;  
Reg. A, B and skip number N from input buffer.
- b) None

4. OUTPUT:

- a) Skip instruction inserted into conditional block.
- b) Error message.

5. ACTION:

a) Gets Registers A, B. Pack and save. Get skip number, if less than or equal to zero, output error message and return; otherwise, add to skip instruction. Insert skip command and A, B into conditional block. Exit with normal return.

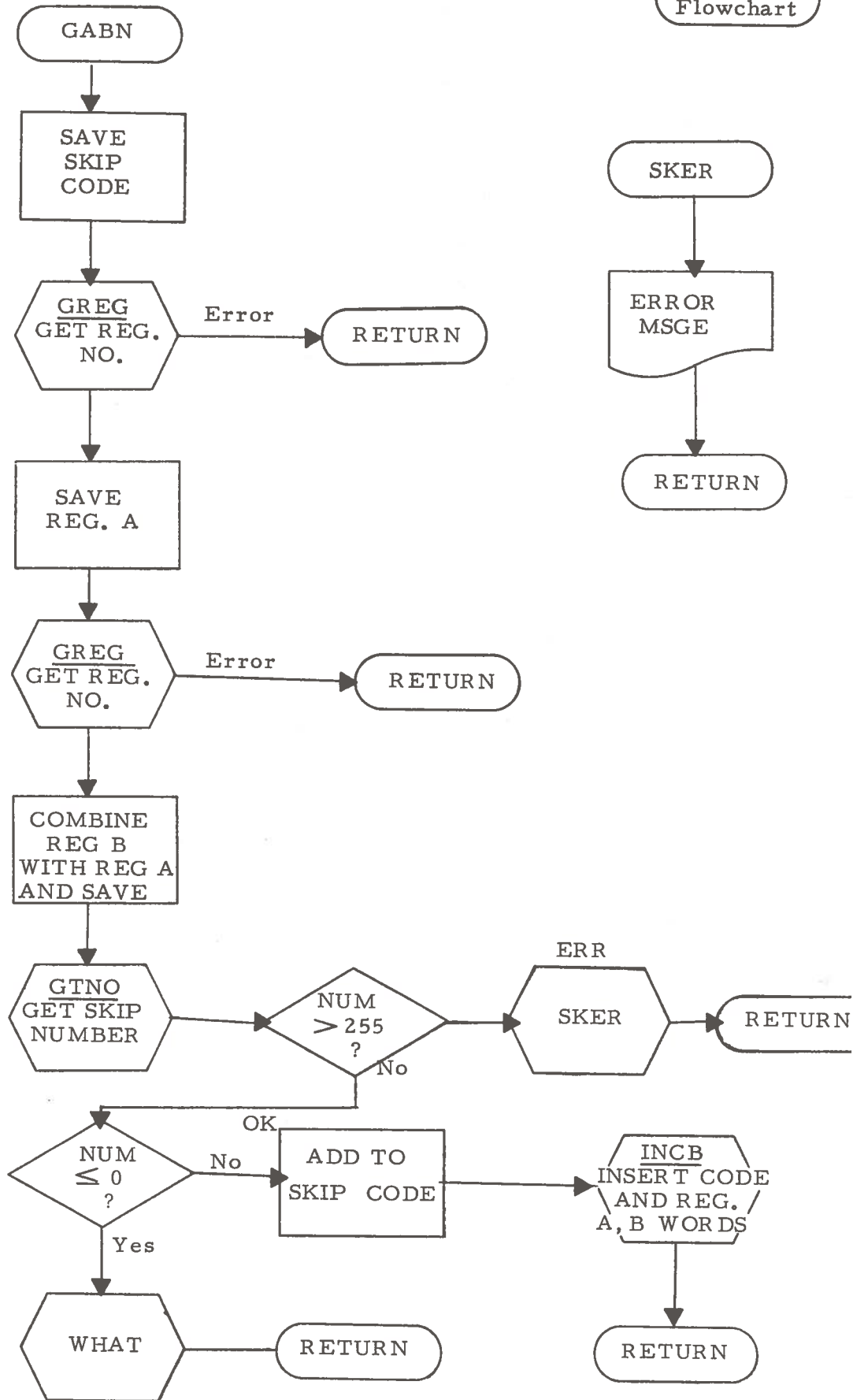
- b) Output error message.

6. EXTERNAL REFERENCES:

GREG  
GTNO  
WHAT  
INCB  
TAO\$

7. CORE USED:

GABN Flowchart



## GENT

1. PURPOSE:

Get address of next entity on indicator ring. Jump out when ring completed.

2. CALLING SEQUENCE:

SSM/SSP		/initialize or next
CALL	GENT	
JMP	OUT	/ring completed

Returns here with entity addr  
in A-Reg.

3. INPUT:

If bit 1 of A-Reg is set (SSM) get the address of the first entity of ring. If bit 1 of A-Reg is zero the address of the next entity of the ring.

4. OUTPUT:

If no next entity (end of ring) return to calling location + 1.  
Otherwise return to calling location + 2 with address of first word of entity block in A-Reg.

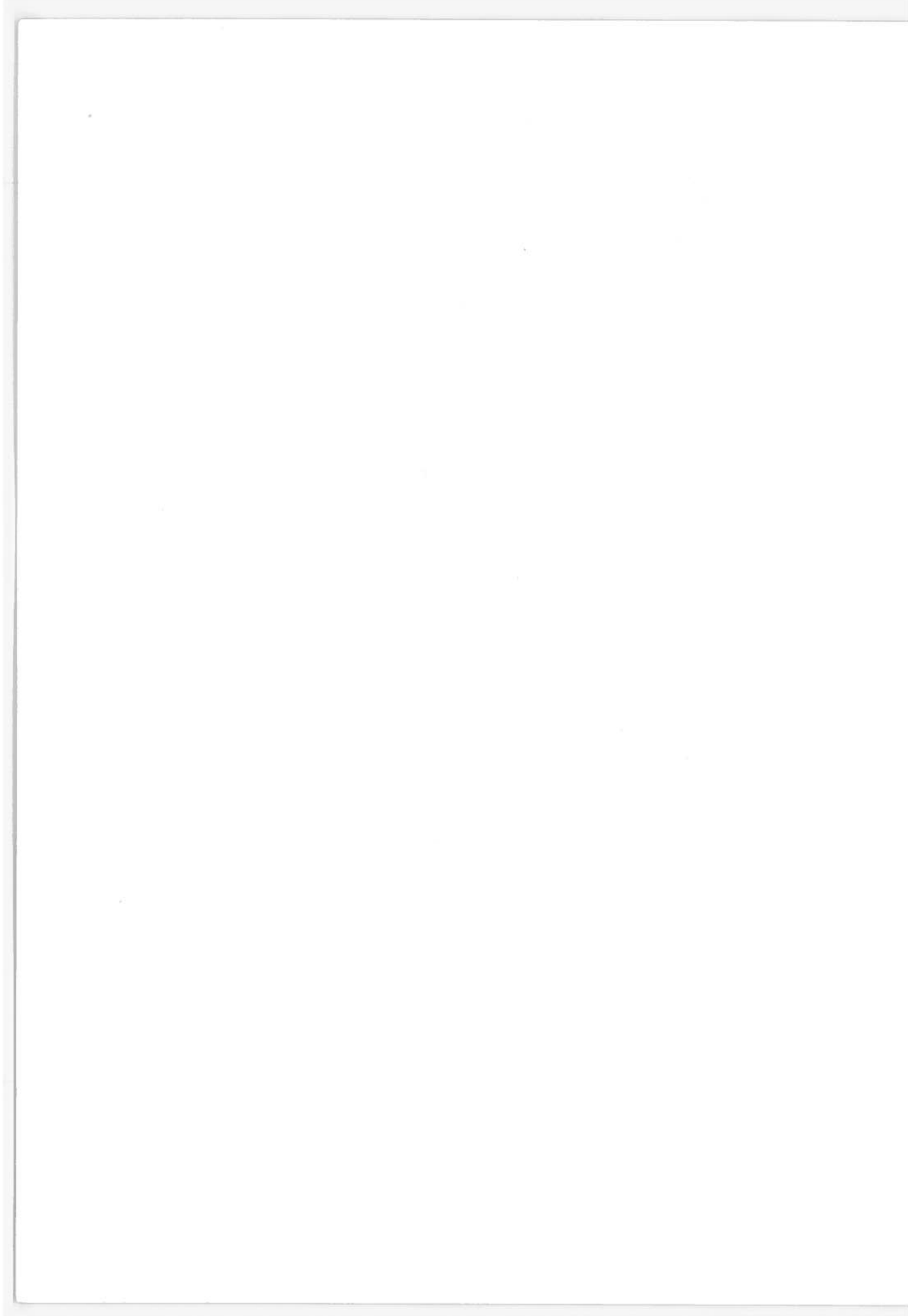
5. EXTERNAL REFERENCES:

INAD - INDICATOR ADDRESS

6. CORE USED:

45<sub>8</sub>





SUBROUTINE:

GETF

1. PURPOSE:

Get blocks of storage from the free ring.

2. CALLING SEQUENCE:

Call GETF

3. INPUT:

A = block size required, or

A =  $\emptyset$  if largest available block is desired

4. OUTPUT:

A = starting address of block, or

A =  $\emptyset$  if out of storage

B = last address of block

5. ACTION:

If no size is specified then the ring is searched for the largest block. The block is detached from the ring and the appropriate output is returned.

If size is specified then the ring is searched for a block of the specified length or the smallest block greater in length if only a larger block is found then the unneeded portion is retained on the ring.

6. EXTERNAL REFERENCES:

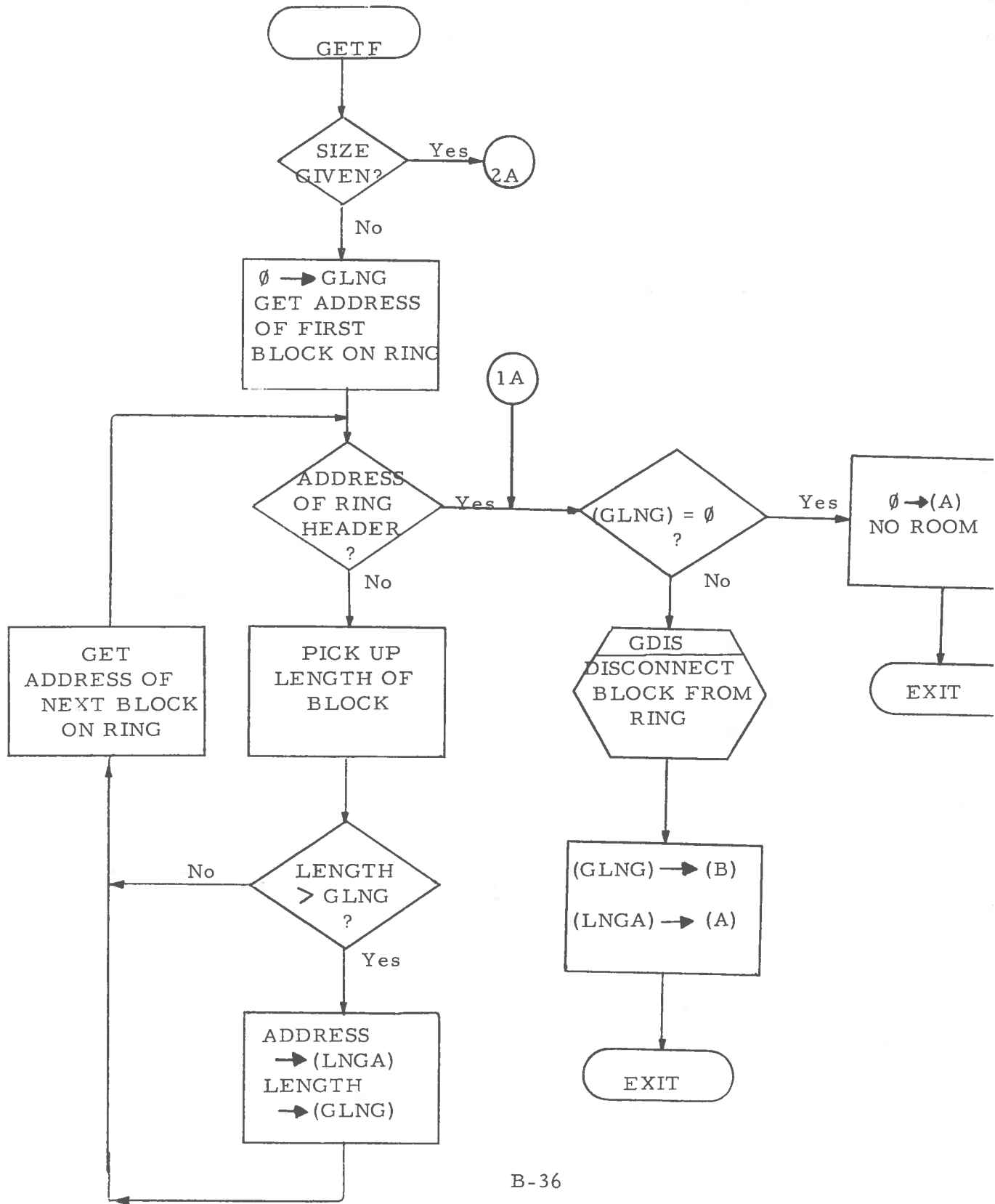
FREE, COLN

7. CORE USED:

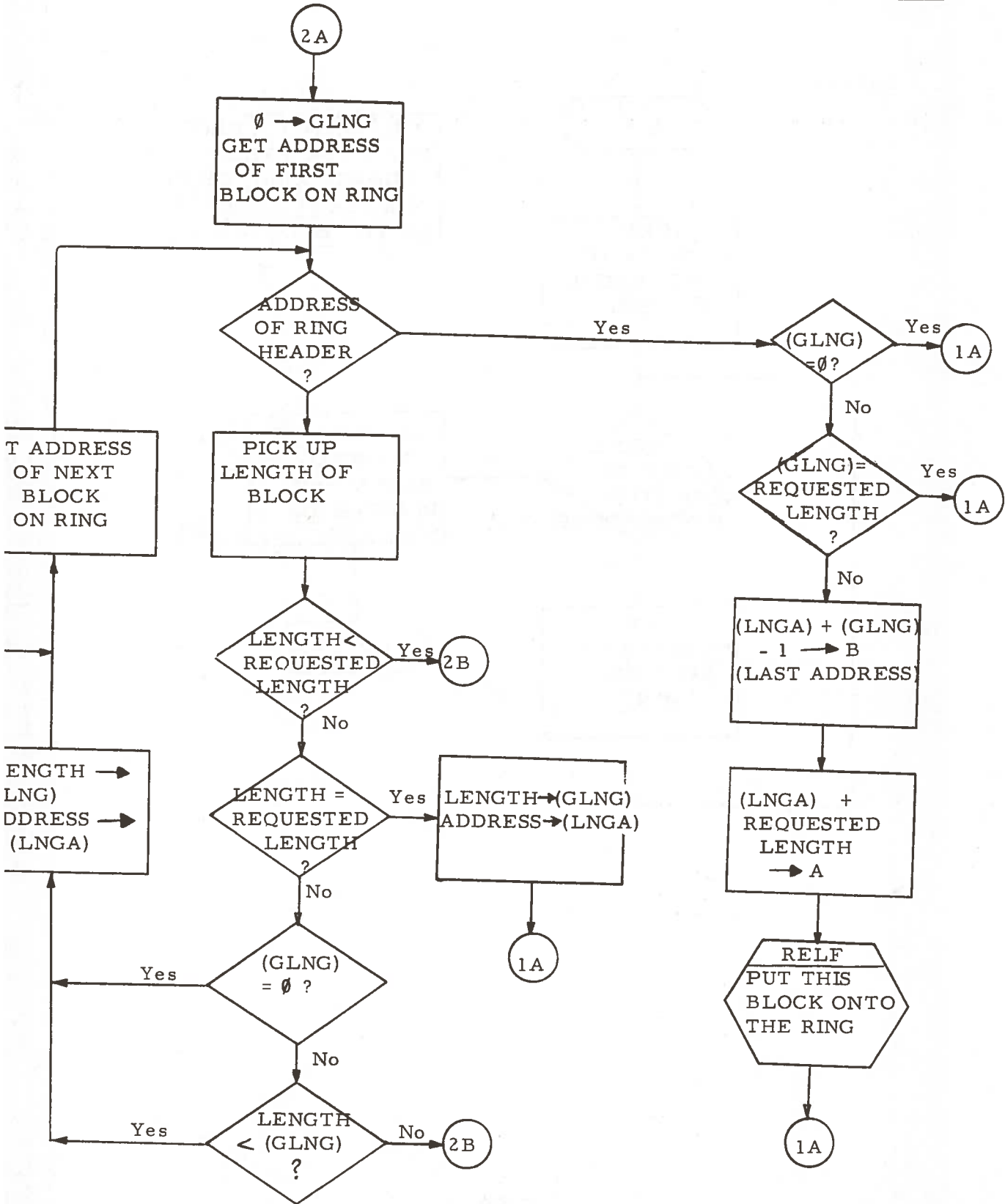
154<sub>8</sub>

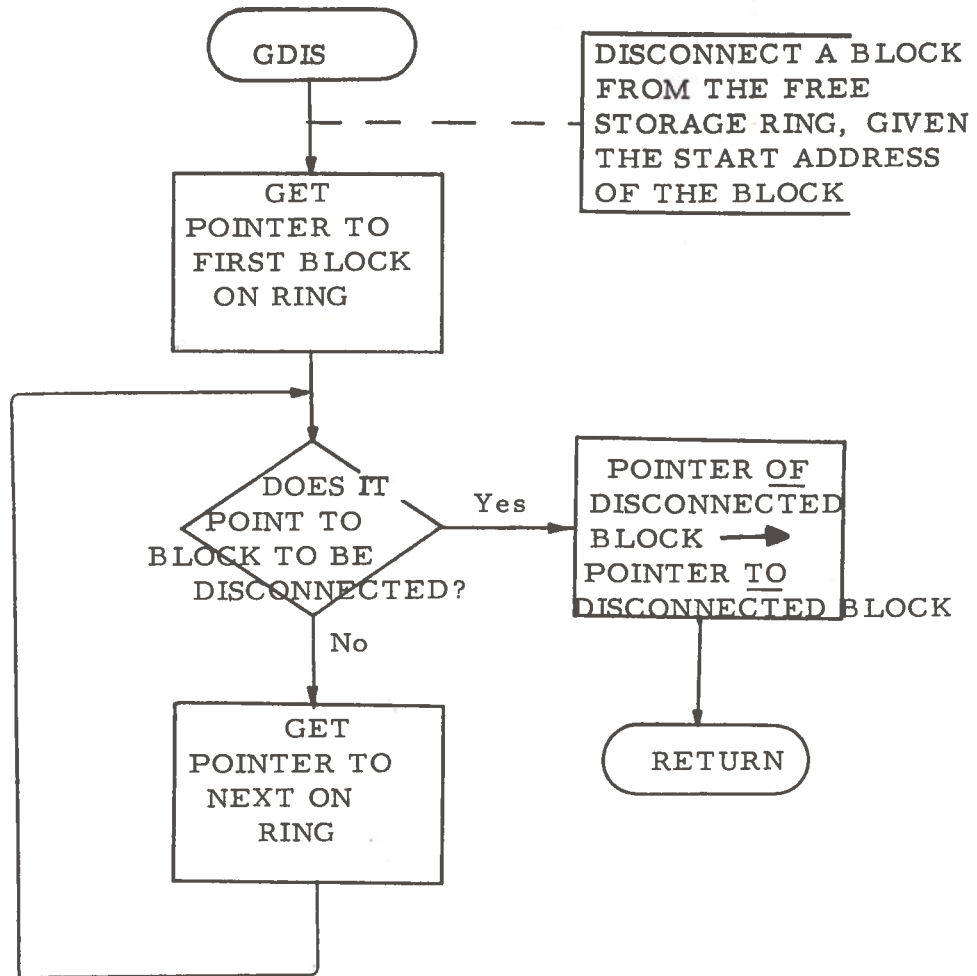
GETF: Get Free Storage Blocks, given size of block.  
 If size not given, get largest.

GETF



GETF





## MEXP

1. PURPOSE:

Move characters into conditioning block

2. CALLING SEQUENCE:

CALL MEXP

3. INPUT:

Character string from input buffer

4. OUTPUT:

Character string in conditioning block;

A-Reg = 1 if null word added; = 0 no null word added

5. ACTION:

Retrieves chars. and packs, spaces and tabs are ignored. Chars. are inserted into conditioning block, two at a time. Move is terminated when a semicolon or a carriage return is encountered.

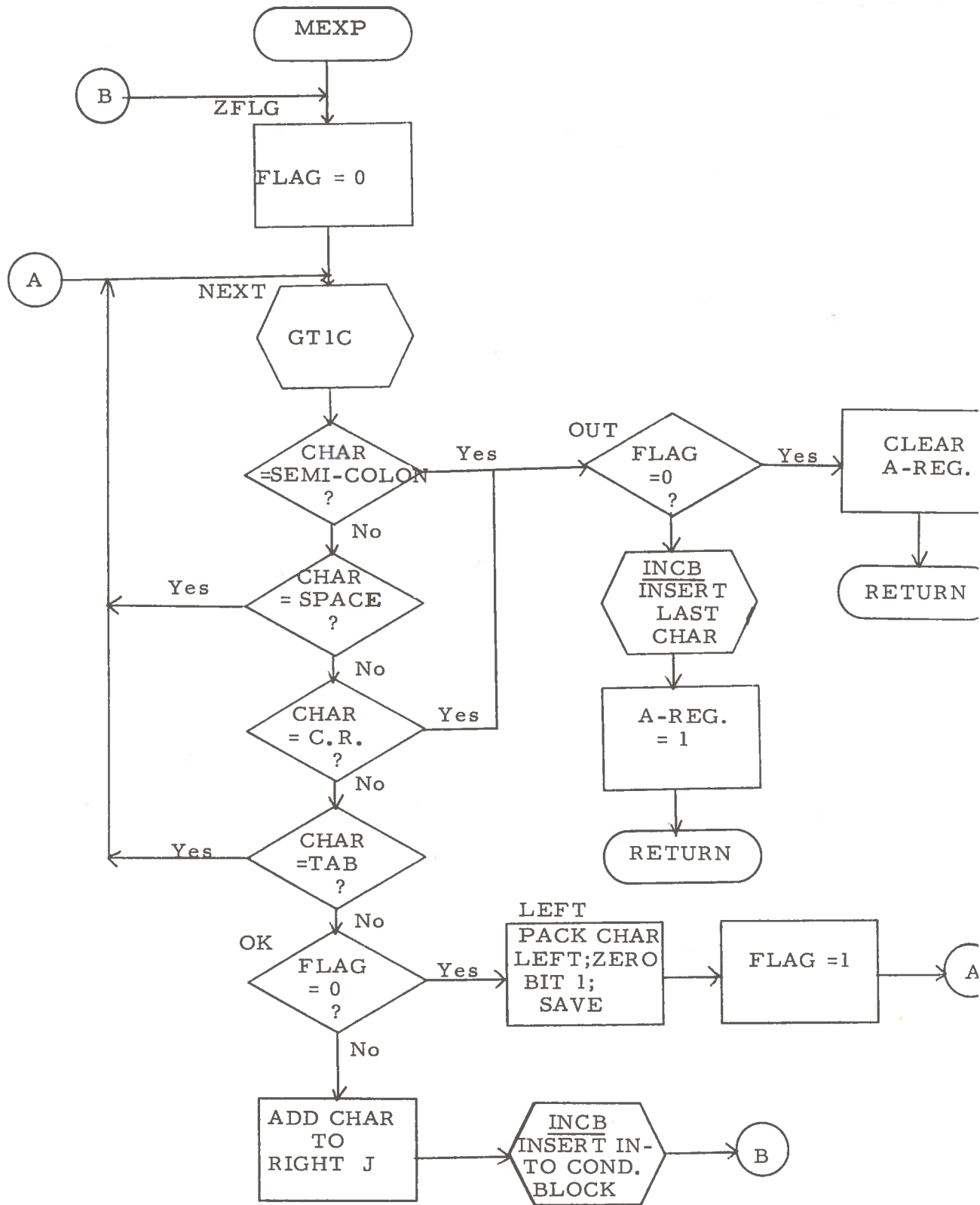
6. EXTERNAL REFERENCES:

GT1C

INCB

7. CORE USED:

548



SUBROUTINE

OCBK, CCBK, NCBK, RCBK

1. PURPOSE

Open, close, create (continue) and reopen component blocks.

2. CALLING SEQUENCE:

- a. Call OCBK
- b. Call CCBK
- c. Call NCBK
- d. Call RCBK

3. INPUT

Free storage blocks

4. OUTPUT

Entity component blocks

5. ACTION

a) The component blocks of the current entity will be traced to find the end of the components. (location with return to entity). This (location) will be saved. A free block will be retrieved from free storage. The address of this new block will be saved by the program and also placed in the system variable ACWD. The number of words in the new block minus four will be two's complemented and saved in system variable NCWD.

b) CCBK - The component block of the current entity will be closed in the following manner. If ACWD contains the address +2 as the saved address of the new block - SAB, then it is assumed that no components were inserted. Then the new block is returned to free storage, and the CCBK exits to the calling routine.

If the addresses are different, then a component has been inserted. The return command is inserted into the next available location of the new block. Any portion of the new block still unused is returned to free storage, and CCBK exits to the calling routine.



c) NCBK - NCBK inserts the return to entity command in the last location of the new block and updates the word saving the return to entity command location. Next a block from free storage is retrieved, and the address to its first available word is saved by NCBK (same word as in OCBK) in ACWD. The number of words in the new block minus four is two's complemented and saved in NCWD. The new block is filled with noops and linked to both the previous component block and the entity block. Then NCBK exists to the calling routine.

d) RCBK - RCBK reopens a component block for a previously existing entity. The address of the last word of the previous block is saved and control transfers to NCB4 in subroutine NCBK.

#### 6. EXTERNALS REFERENCED

ACWD - Address of next available component word

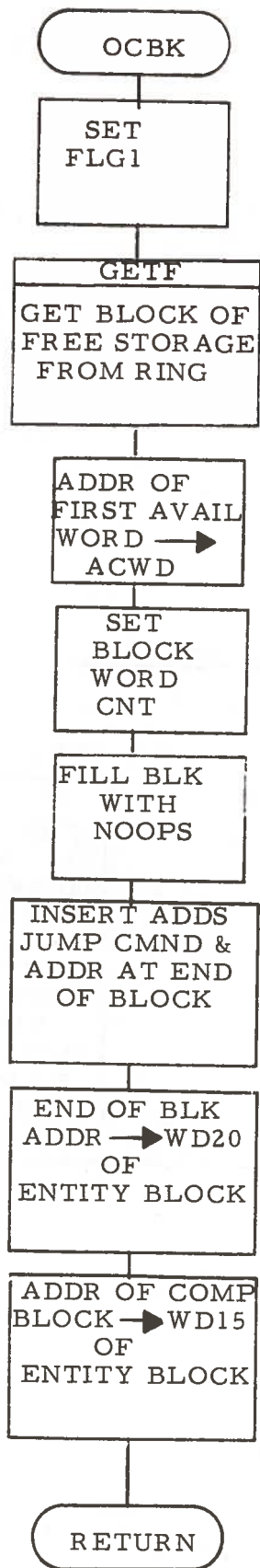
NCWD - Counter of words left in new component block

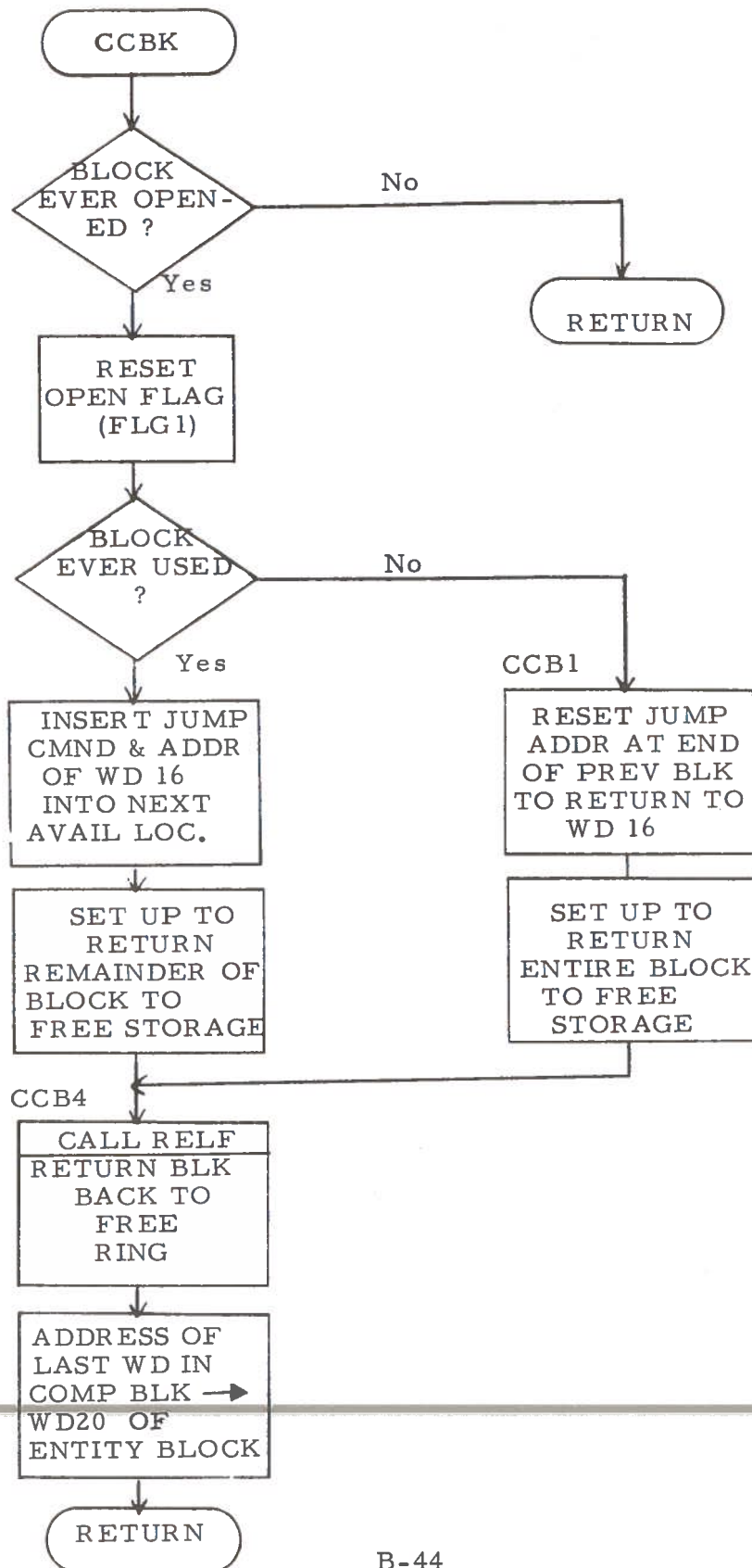
ENAD - Entity address

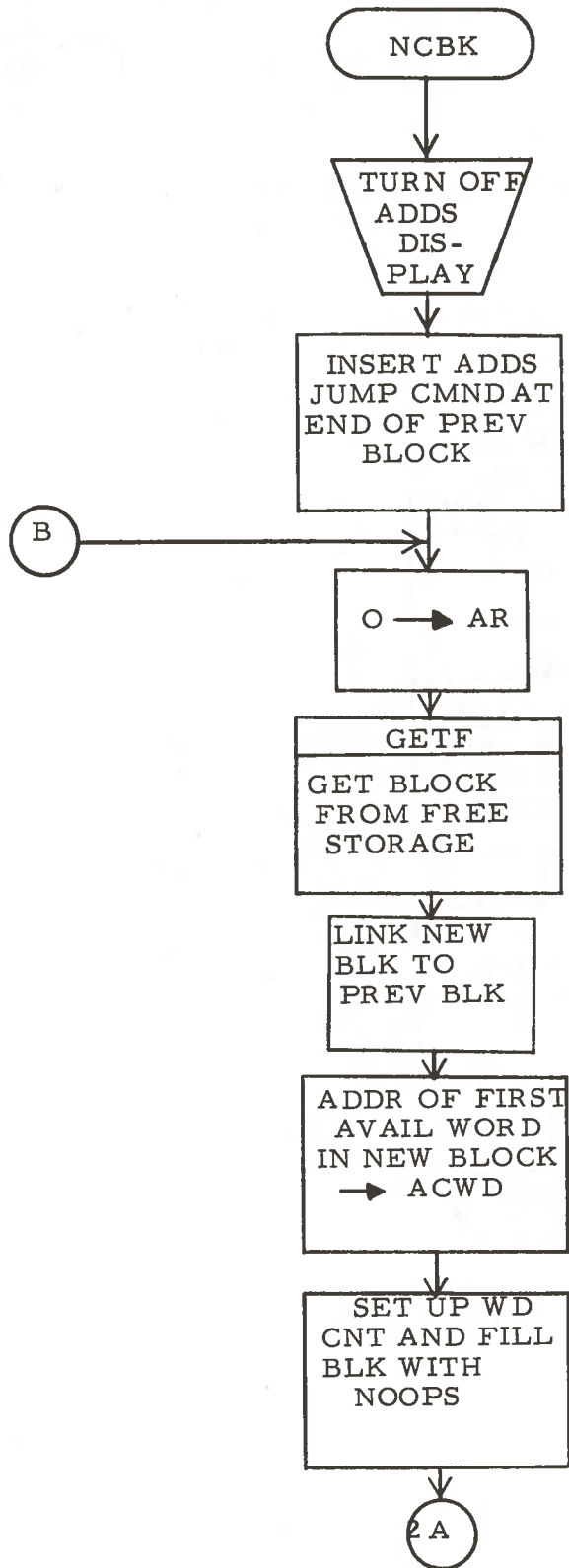
FLG1 - Component block flag (set if open)

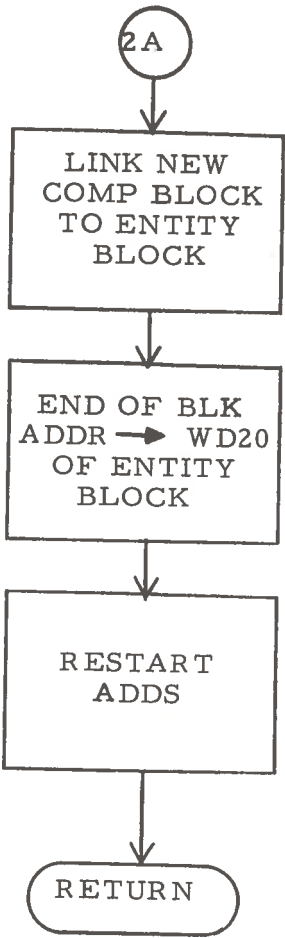
#### 7. CORE USED

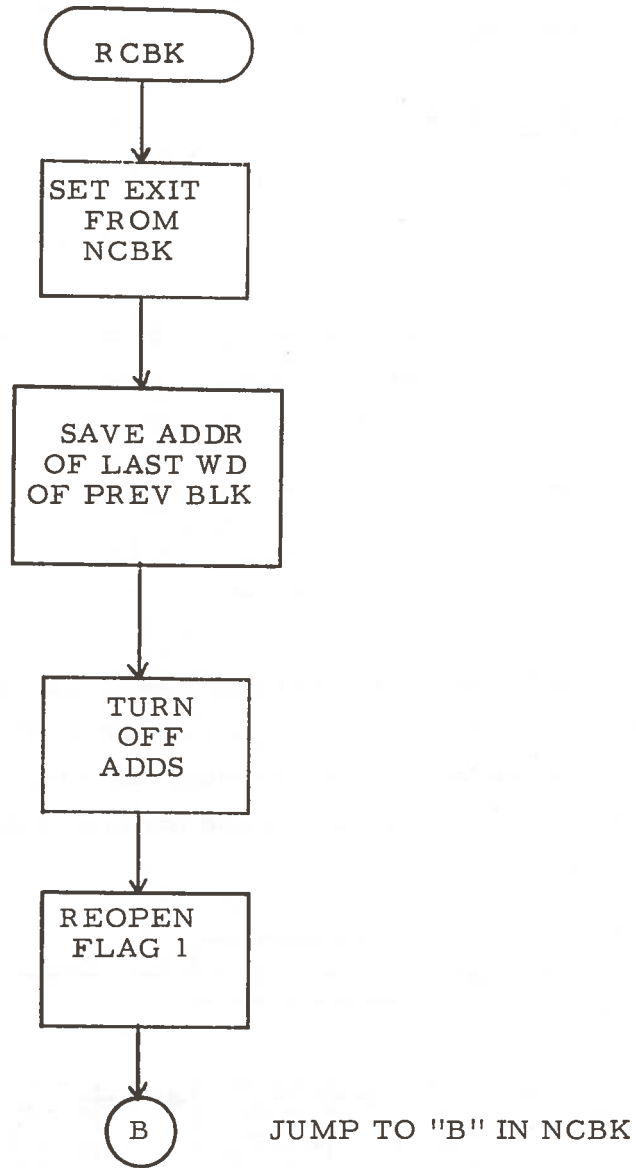
230<sub>8</sub>











SUBROUTINE:

REL F

1. PURPOSE:

Release blocks of storage no longer needed back to the free ring.

2. CALLING SEQUENCE:

Call REL F

3. INPUT:

A = first address of block to be released

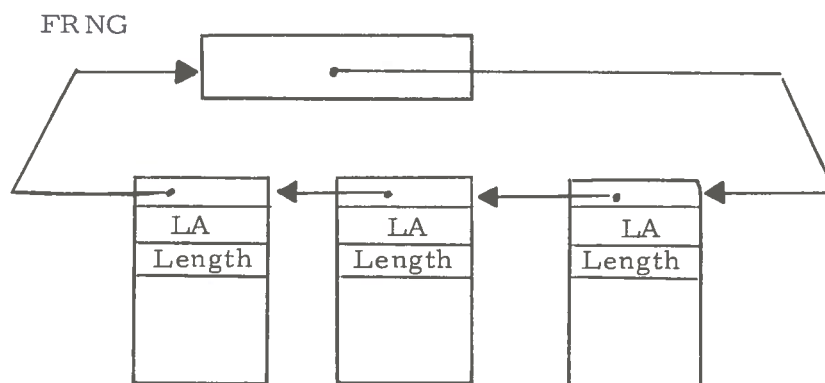
B = last address of block to be released

4. OUTPUT:

None

5. ACTION:

The released block is attached to the beginning of the free ring. The contents of the first word of the block contain the pointer to the next on the ring; the second word contains the last address of the block and the third word the length of the block.



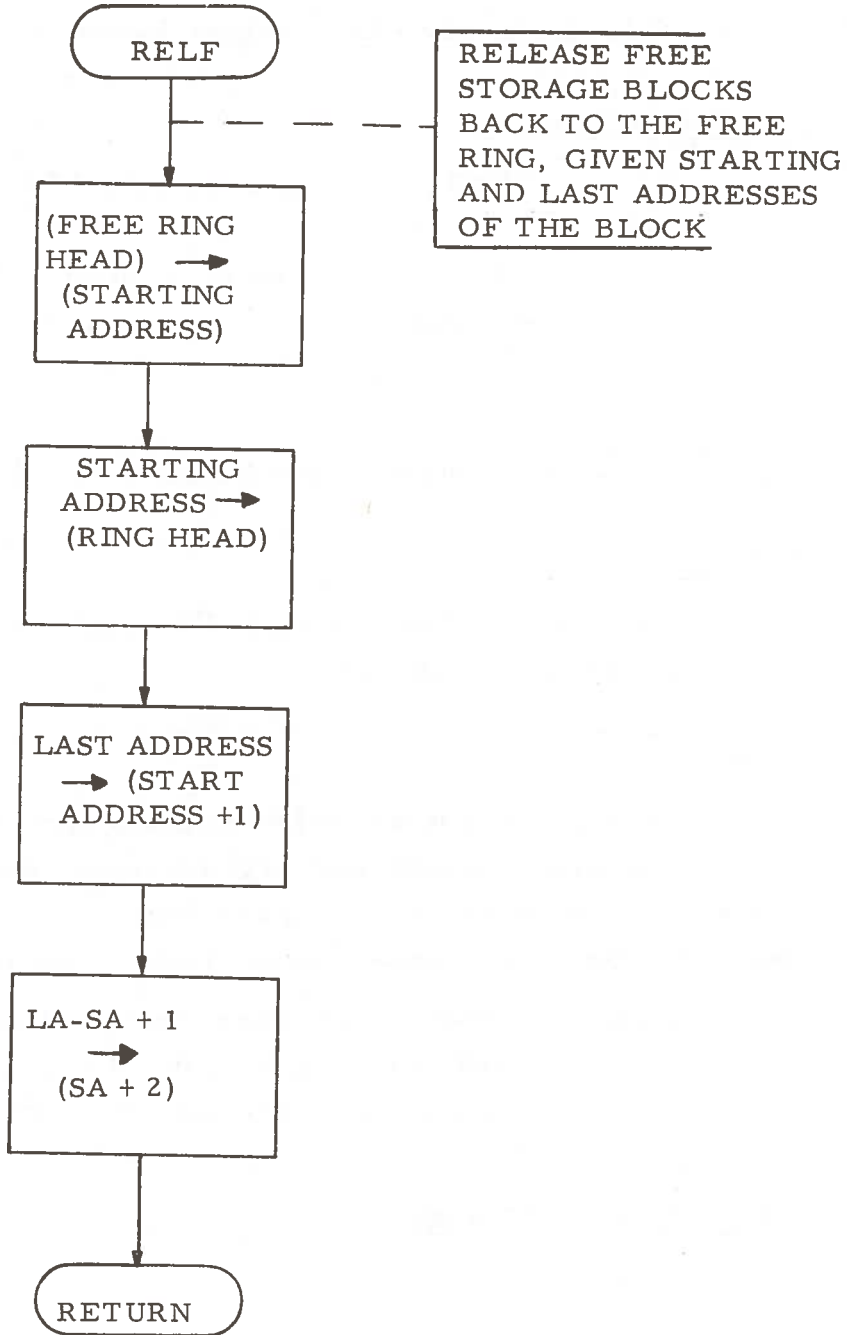
6. EXTERNAL REFERENCES:

None

7. CORE USED:

B-48

REL F





SUBROUTINE:                   SQRJAM

1.    PURPOSE:

        Calculate square root of integer double precision

2.    CALLING SEQUENCE:

        LDA     LARG            /Load least SIGNIF Bits  
        IAB  
        LDA     MARG           /Load most SIGNIF Bits  
        CALL    SQRJAM

3.    INPUT:

        DDP-516 double precision integer in A and B-Registers

4.    OUTPUT:

        Square root of input rounded off to single precision  
        integer (in A-Register)

5.    ACTION:

        Converts input to a positive integer if necessary. Zeros  
        root, then squares root and checks against input. Root is incremented,  
        squared, and compared until its square either exceeds input or equals  
        input. If square of root equals inputs, root is returned.

        If square of root exceeds input, root is added to the square  
        of the previous root and compared to input. If this sum is greater  
        than the input, root minus one is returned. Otherwise, root is  
        returned.

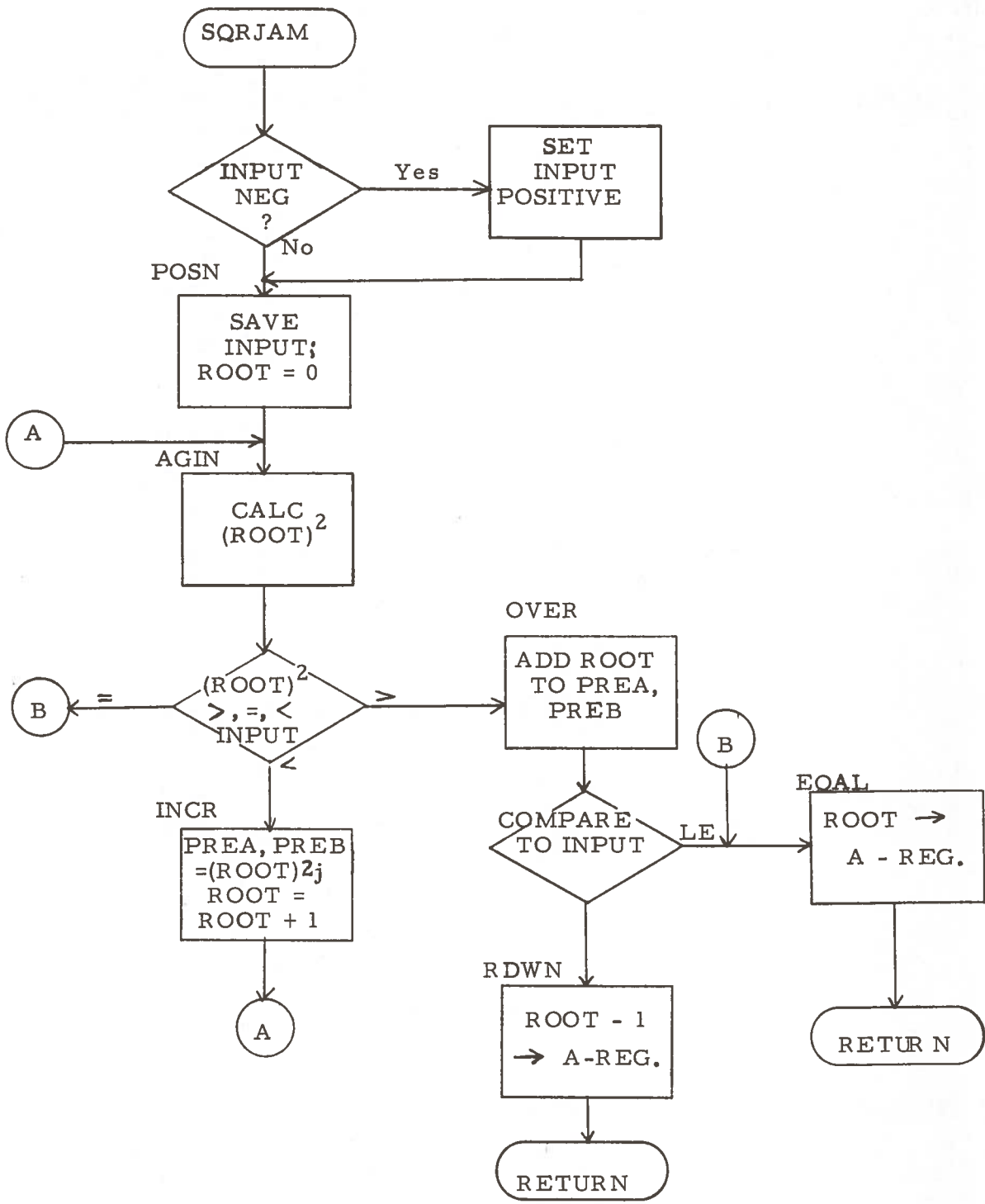
6.    EXTERNAL REFERENCES:

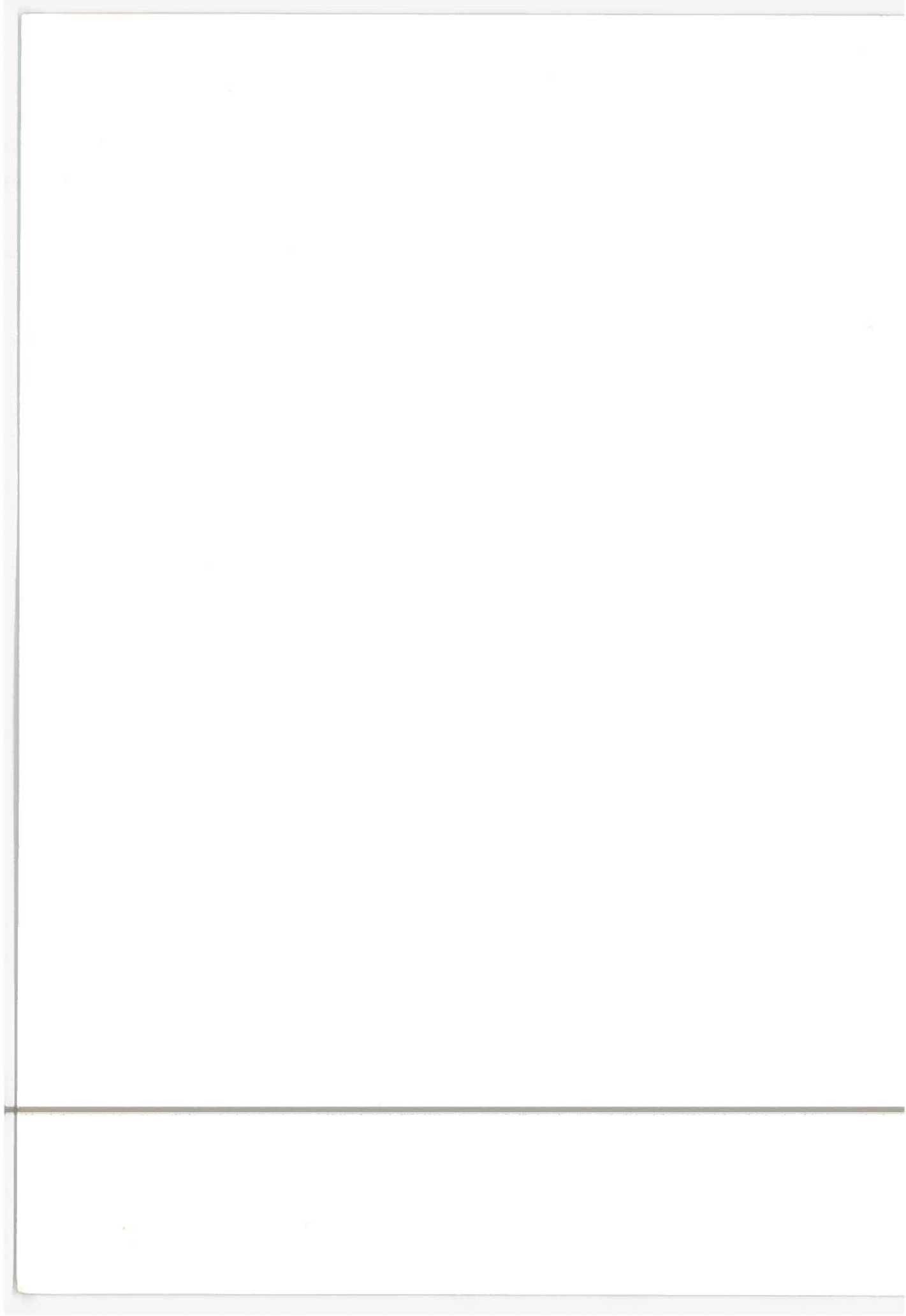
        None

7.    CORE USED:

        64<sub>8</sub>

SQRJAM





SUBROUTINE:

**ALPA**

1. PURPOSE:

Sets up character mode and size commands. Reads text from input buffer and inserts appropriate display commands into component block.

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	ALPA

3. INPUT:

Size (1-4) from input buffer. Smallest=1; largest=4.  
Text from input buffer.

4. OUTPUT:

Display commands in component file.

5. ACTION:

Checks for entity working level. Gets size from input buffer and sets up size command. Checks terminating character of size number for comma, space, or tab. Retrieves text from input buffer. Packs and inserts characters into component block. Returns to calling program when a carriage return is encountered.

6. EXTERNAL REFERENCES:

CMOD

GTNO

WHAT

SBLK

COP

CIN

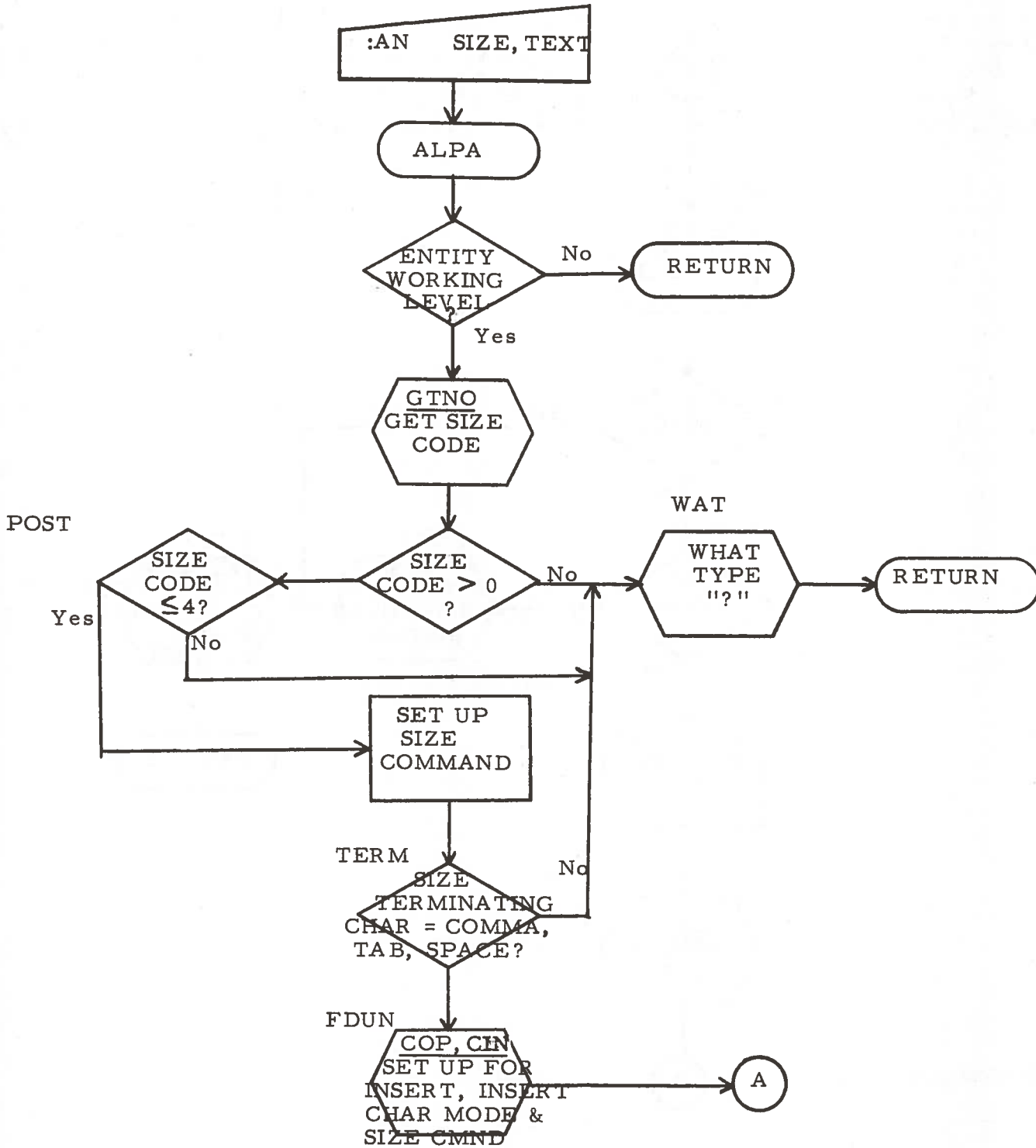
GTCH

CLO

7. CORE USED:

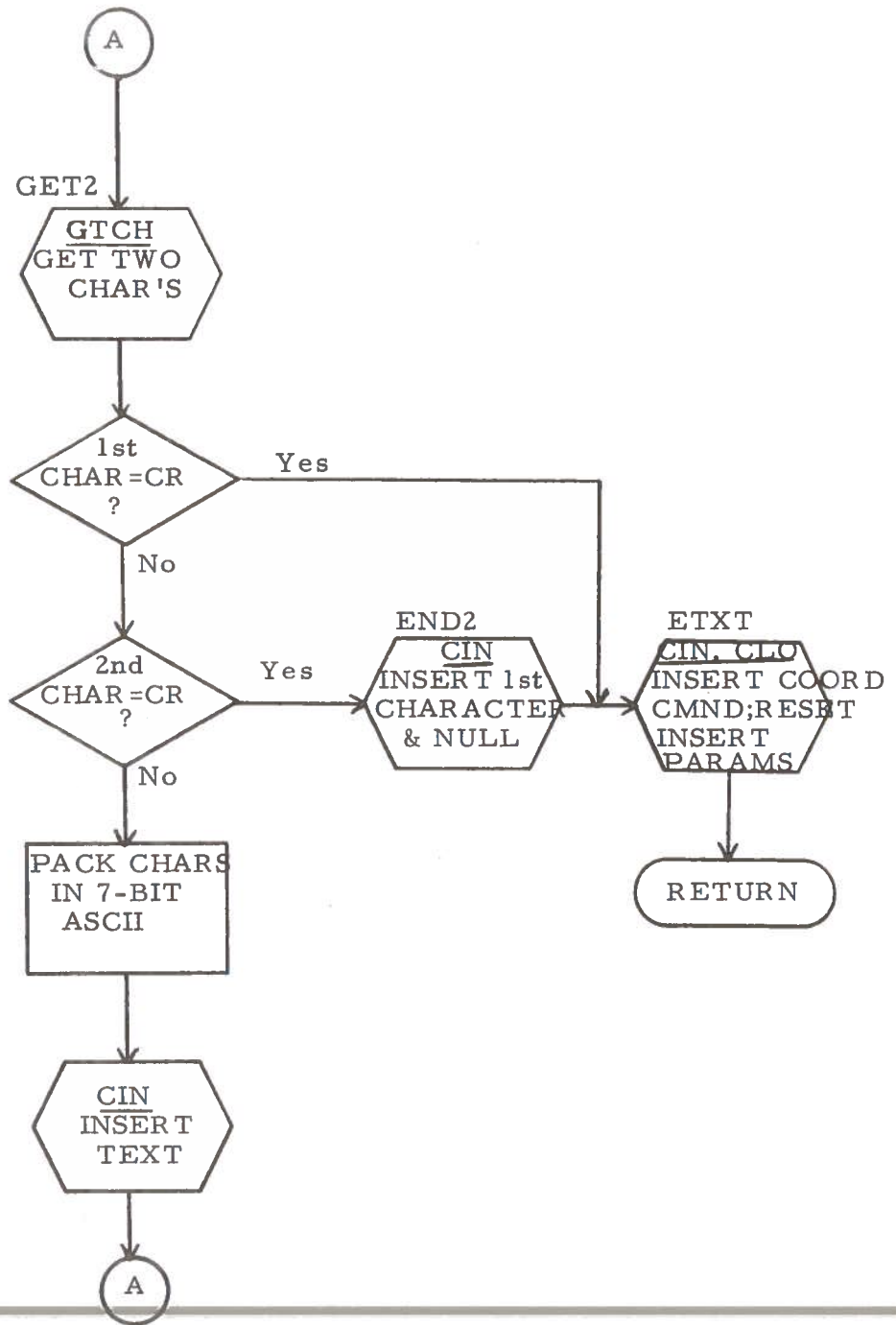
114<sub>8</sub>

DRAW ALPHANUMERIC



ALPA  
FLOWCHART

P. 2



SUBROUTINE:

ARC

1. PURPOSE:

Set up component commands to draw arc given relative start point and radius,  $(\Delta x, \Delta y)$  and angle of arc,  $\Theta$ .

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	ARC

3. INPUT:

From the input buffers:

- a)  $\Delta x$ , x-component of start point, radius vector
- b)  $\Delta y$ , y-component of start point, radius vector
- c)  $\Theta$ , angle of arc

NOTE: If  $\Delta y, \Theta$  not input,  $\Delta x$  is assumed to be the radius and a full circle is drawn.

4. OUTPUT:

Display commands are inserted into the current component block.

5. ACTION:

Input will be checked to determine size and angle of arc. Arcs will be drawn clockwise starting at the point  $(\Delta x, \Delta y)$  relative to initial position (center of arc).



An Arc will be defined as a portion of a 60-sided regular polygon, one side for each  $6^\circ$ . Arcs will be drawn to the nearest  $6^\circ$  so that each arc will be an integral number of sides. \*  $\Delta x$ ,  $\Delta y$  will specify the radius of the polygon's circumscribed circle. Figures below show the relationship of the portions of the arc to each other.

\* No arc will be drawn for  $2^\circ$  or less.

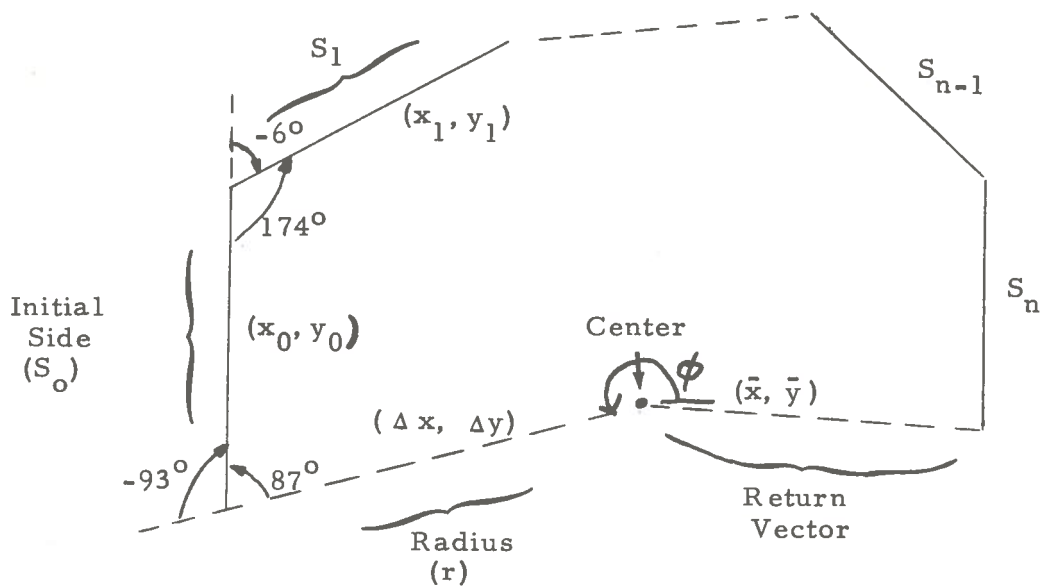
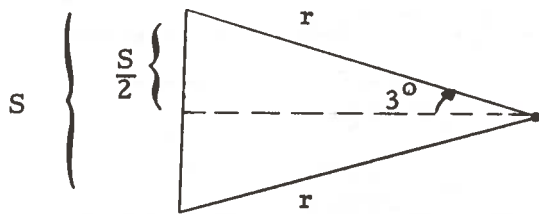


FIGURE 1.

Angular Relationship of Sides and Given Radius



$$\frac{S}{2} = r \sin 3^\circ$$

$$S = 2r \sin 3^\circ$$

FIGURE 2.

Side, Radius Length Relationship

COMPUTATION OF (X, Y)<sub>15n+1</sub> (n = 0, 1, 2, 3)

XI, YI are computed for the first chord of each quarter-circle as functions of the radius components, DELX, DELY, in raster units\*16.

$$XI = \left[ \text{DELX} * \sin(-3^\circ) + \text{DELY} * \cos 3^\circ * 2 \sin 3^\circ \right] \quad (1a)$$

$$YI = \left[ \text{DELY} * \sin(-3^\circ) - \text{DELX} * \cos 3^\circ * 2 \sin 3^\circ \right] \quad (1b)$$

PROGRAM:

$$XI = \frac{-1715 * \text{DELX} + 32723 * \text{DELY}}{32768} * 3430 + 16384$$

$$YI = \frac{-32723 * \text{DELX} - 1715 * \text{DELY}}{32768} * 3430 + 16384$$

Where 16384 is the round-off summand for the double-word numerators.

COMPUTATION OF (X, Y)<sub>15n+j</sub> (n = 0, ..., 3; j = 2, ..., 14)

For the second through the fourteenth chords:

$$XI' = XI * \cos 6^\circ + YI * \sin 6^\circ \quad (2a)$$

$$YI' = XI * \sin(-6^\circ) + YI * \cos 6^\circ \quad (2b)$$

PROGRAM:

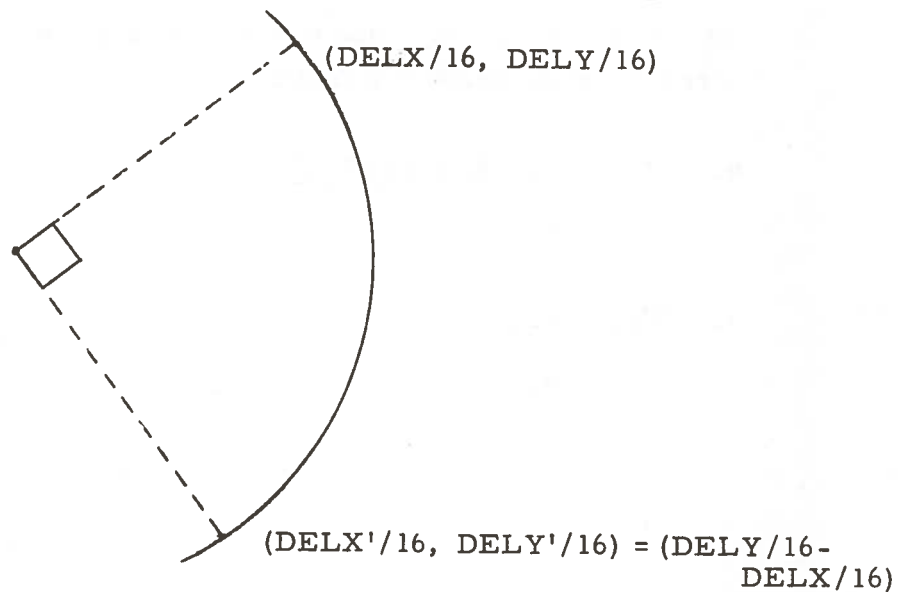
$$XI' = \frac{32588 * XI + 3425 * YI + 16384}{32768}$$

$$YI' = \frac{-3425 * XI + 32588 * YI + 16384}{32768}$$

Where XI', YI' replace XI, YI for subsequent computation.

COMPUTATION OF  $(X, Y)_{15n}$  ( $n = 1, 2, 3, 4$ )

The last chord for a quarter-circle is computed to draw a vector from the end of the previous (14th, 29th, 44th or 59th) chord to the end of the radius vector resulting from a  $90^\circ$  clockwise transformation of the radius to the first point on the quarter-circle.



X, Y components of beam movement are summed as XBAR, YBAR, hence, we wish to compute  $(X, Y)_{15n} = XI, YI$  such that

$$16 \text{ XBAR}_{14} + XI_{15} = \text{DELY}, \text{ and}$$

$$16 \text{ YBAR}_{14} + YI_{15} = -\text{DELX}$$

So, we set:

$$XI_{15} = \text{DELY} - 16 \text{ XBAR}, \text{ and} \quad (3a)$$

$$YI_{15} = -\text{DELX} - 16 \text{ YBAR} \quad (3b)$$

The transformation is applied to DELX, DELY for the next quadrant:

$$\text{DELX}' = \text{DELY}, \text{ DELY}' = -\text{DELX}$$

We note that the updated values of XBAR and YBAR are computed properly:

$$\text{XBAR}_{15} = \text{XBAR}_{14} + (\text{DELY} - 16 \text{XBAR}_{14}) / 16 = \text{DELY}/16$$

$$\text{YBAR}_{15} = \text{YBAR}_{14} + (-\text{DELX} - 16 \text{YBAR}_{14}) / 16 = \text{DELX}/16$$

Last we note that the first chord of each quarter-circle cannot be computed by (2a, b) due to the propagated errors of precision - the errors which (3a, b) eliminate.

6. EXTERNAL REFERENCES

None

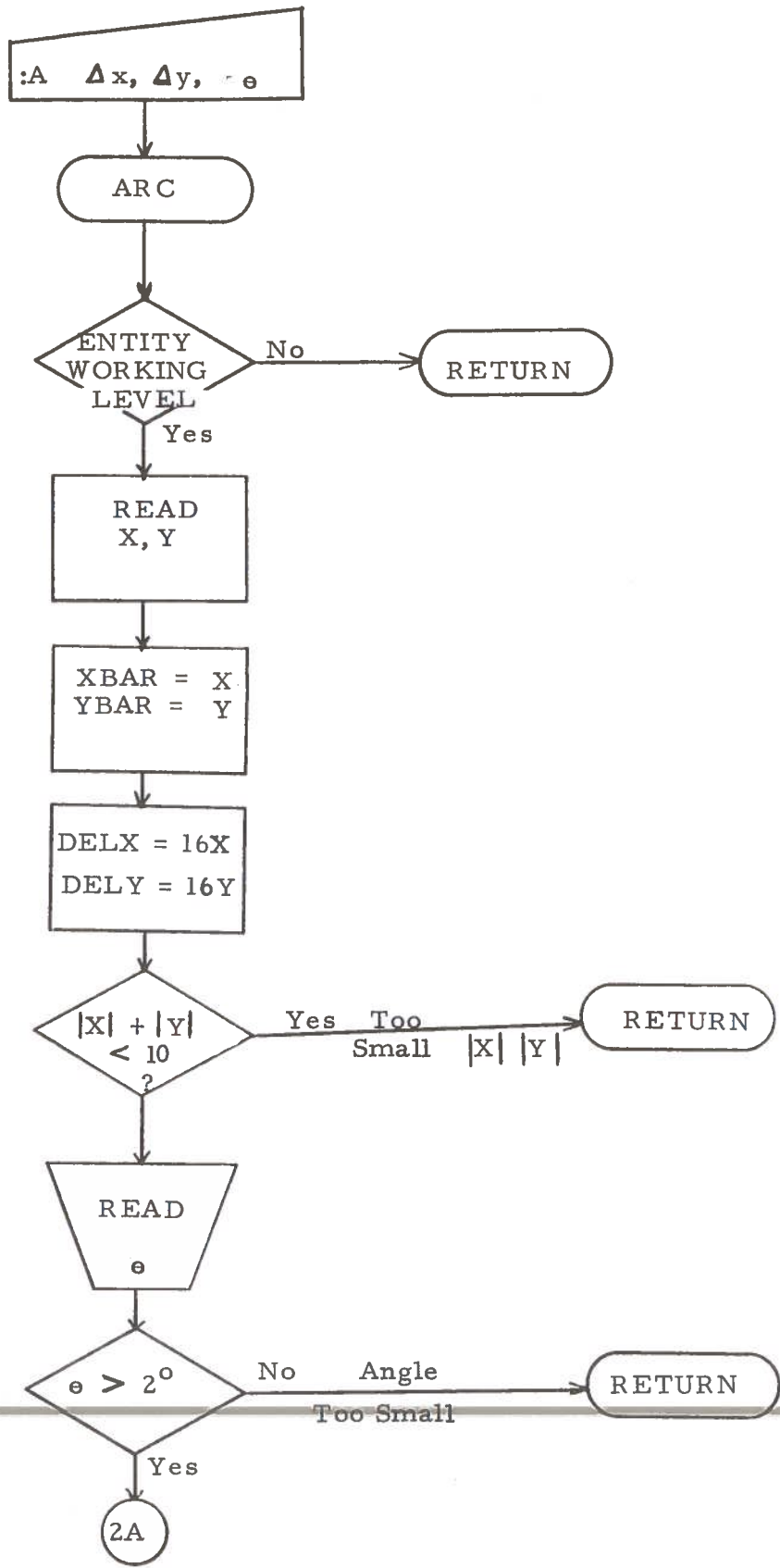
7. CORE USED

316<sub>8</sub>

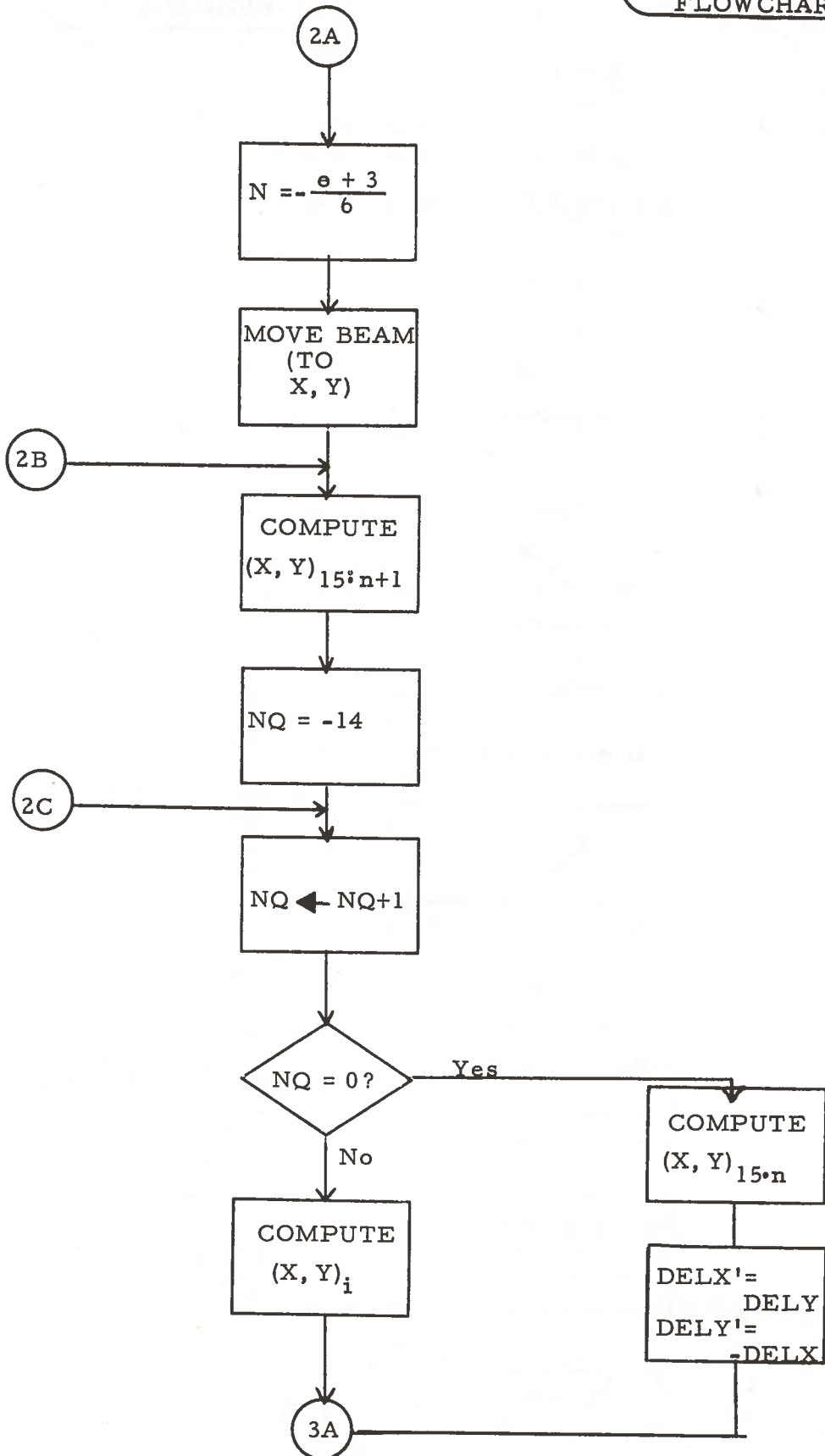
## VARIABLES

- YBAR: Cumulative Y-axis (ordinate) beam travel; final value is 2's complemented for return of beam to center of arc.
- XBAR: Similarly for X-axis (abscissa) beam travel.
- DELY:  $16*Y$  - component of radius relative to the center of the arc; 90-degree clockwise transformation is applied for each quarter-circle component of the arc drawn.
- DELX:  $16*X$  - component of radius, as for DELY.
- YI:  $16*Y$  - component of chord drawn; this is the internal (to the program) computed value of the relative Y beam travel for the chord drawn.
- XI:  $16*X$ -component of chord.
- YD:  $YI/16$  (rounded); the relative Y beam travel for a chord drawn; a summand for YBAR.
- XD: Similarly.
- N: The counter for the number of chords with subtended angle =  $6^\circ$ , to be drawn; an additional ( $6^\circ$ ) chord is drawn for input angles modulo 6 equal to  $3^\circ$  or more.
- NQ: Internal counter for quarter-circle chords drawn; used to sense the last chord of a quarter-circle or the first chord of the subsequent quarter-circle is to be drawn.
- LMXY: Minimum -1 of sum of absolute values of radius X and Y components; error message typed is "TOO SMALL |X| |Y| ".

ARC  
FLOWCHART

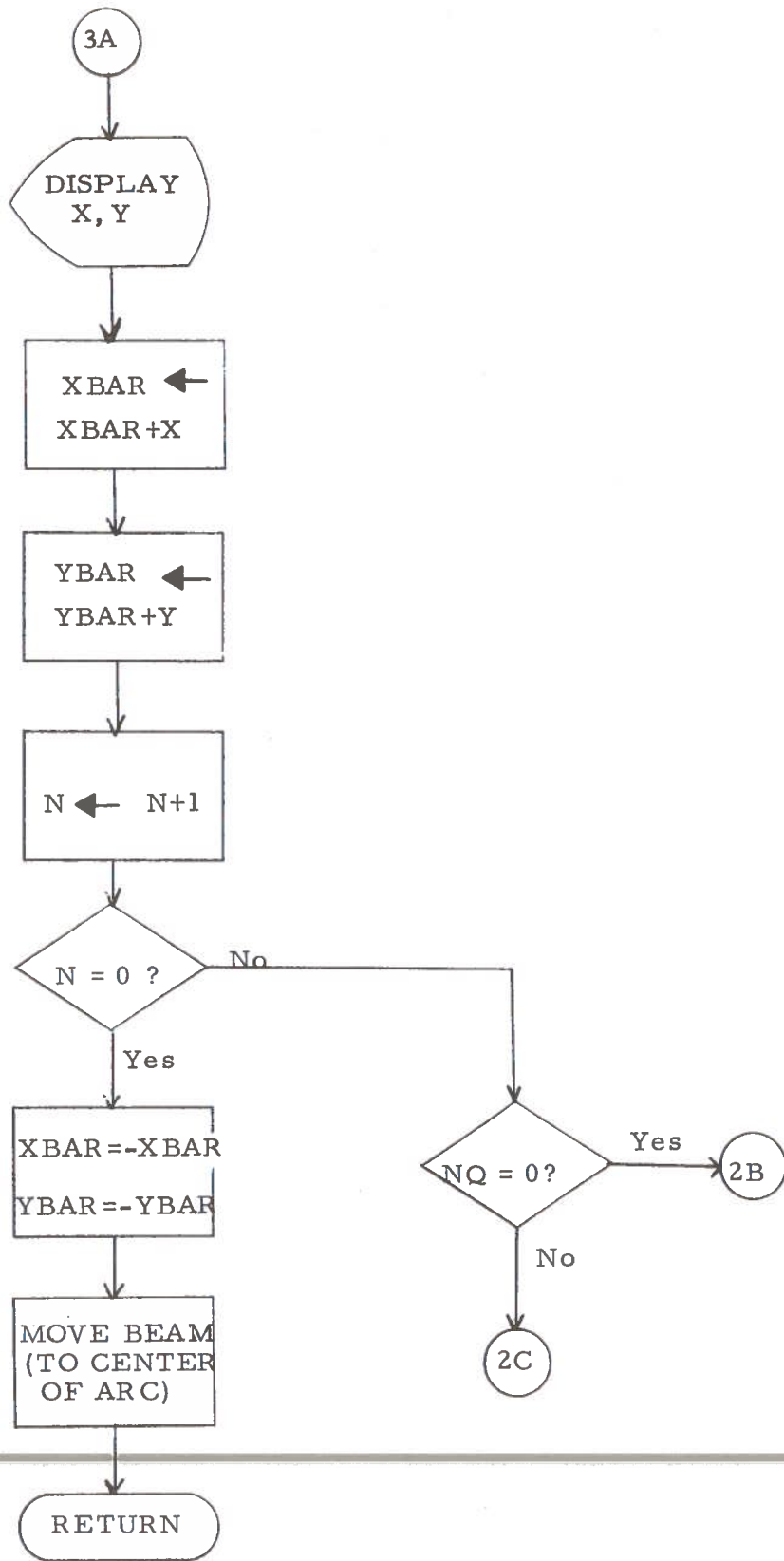


ARC  
FLOWCHART





ARC  
FLOWCHART



BLIK, INTY, NEWF, OFF\$, ON\$\$, UBLI

1. PURPOSE:

- a) BLIK      set blink command in conditioning block for indicator or entity
- b) INTY      set intensity command in conditioning block
- c) NEWF      set new frame command in conditioning block
- d) OFF\$      set command to turn off indicator or entity in conditioning block
- e) ON\$\$      set command to turn on indicator or entity in conditioning block
- f) UBLI      set unblink command in conditioning block

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	Prog

where      Prog = BLIK, INTY, NEWF, OFF\$, ON\$\$, UBLI

3. INPUT:

- a) None
- b) Intensity from teletype buffer
- c) New frame ID from teletype buffer
- d) None
- e) None
- f) None

4. OUTPUT:

- a) Blink command in conditioning block
- b) Intensity command in conditioning block
- c) New frame command in conditioning block
- d) Command to turn off indicator or entity in conditioning block
- e) Command to turn on indicator or entity in conditioning block

5. ACTION:

Each routine checks for proper working level and:

- a) inserts blink command into conditioning block
- b) gets intensity, checks if in range 1 through 7. If not, calls WHAT and returns. Otherwise inserts intensity into conditioning block.
- c) calls IDER to get end check input ID for range. If OK, inserts new frame command into conditioning block.
- d) inserts command to turn off indicator or entity into conditioning block
- e) inserts command to turn-on indicator or entity into conditioning block
- f) inserts unblink command into conditioning block

6. EXTERNAL REFERENCES:

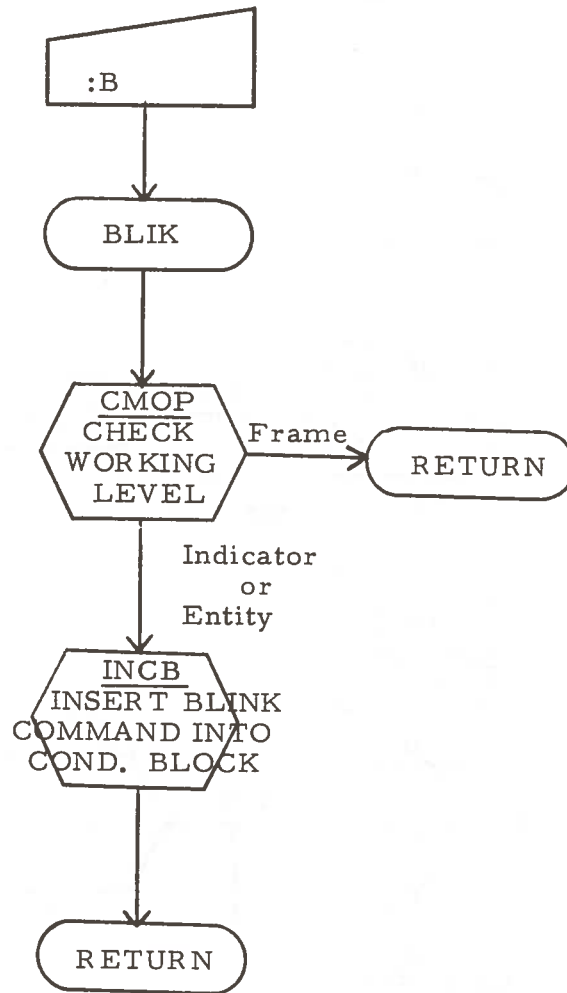
CMOD, INCB, GTNO, WHAT, IDER

7. CORE USED:

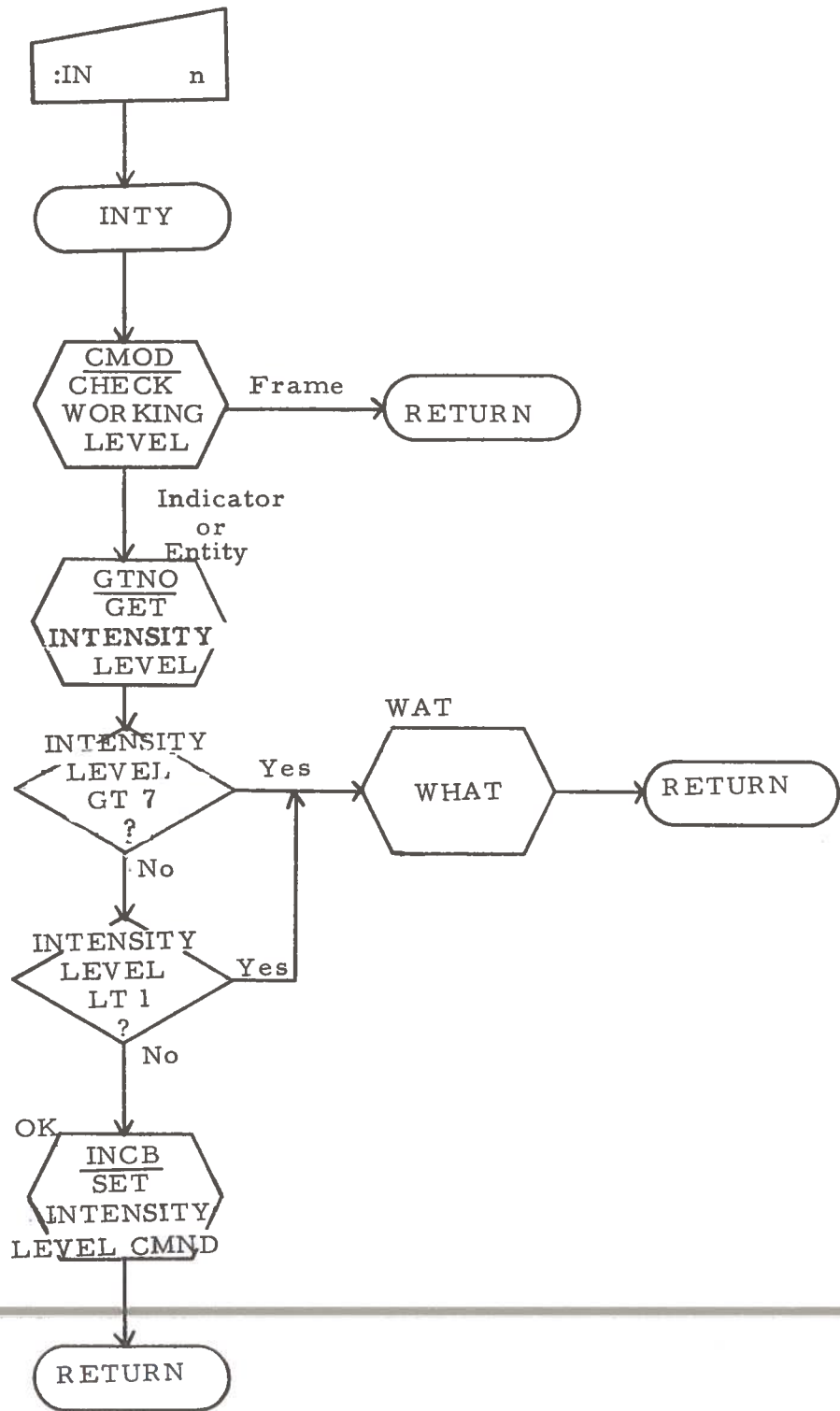
114<sub>8</sub>

BLINK

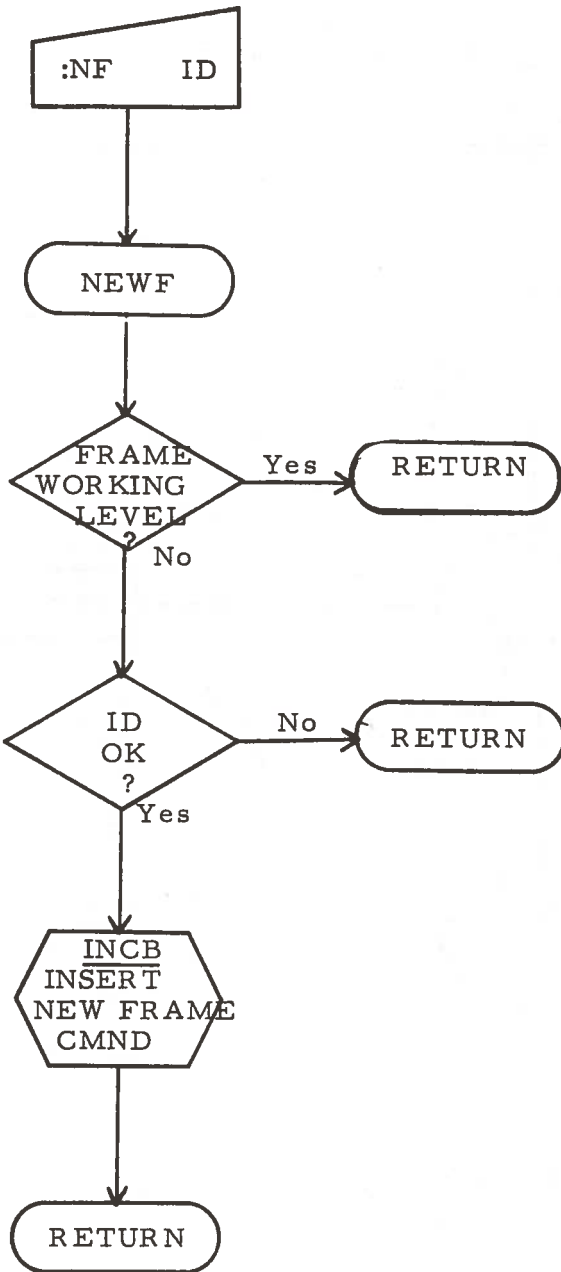
BLIK  
FLOWCHART



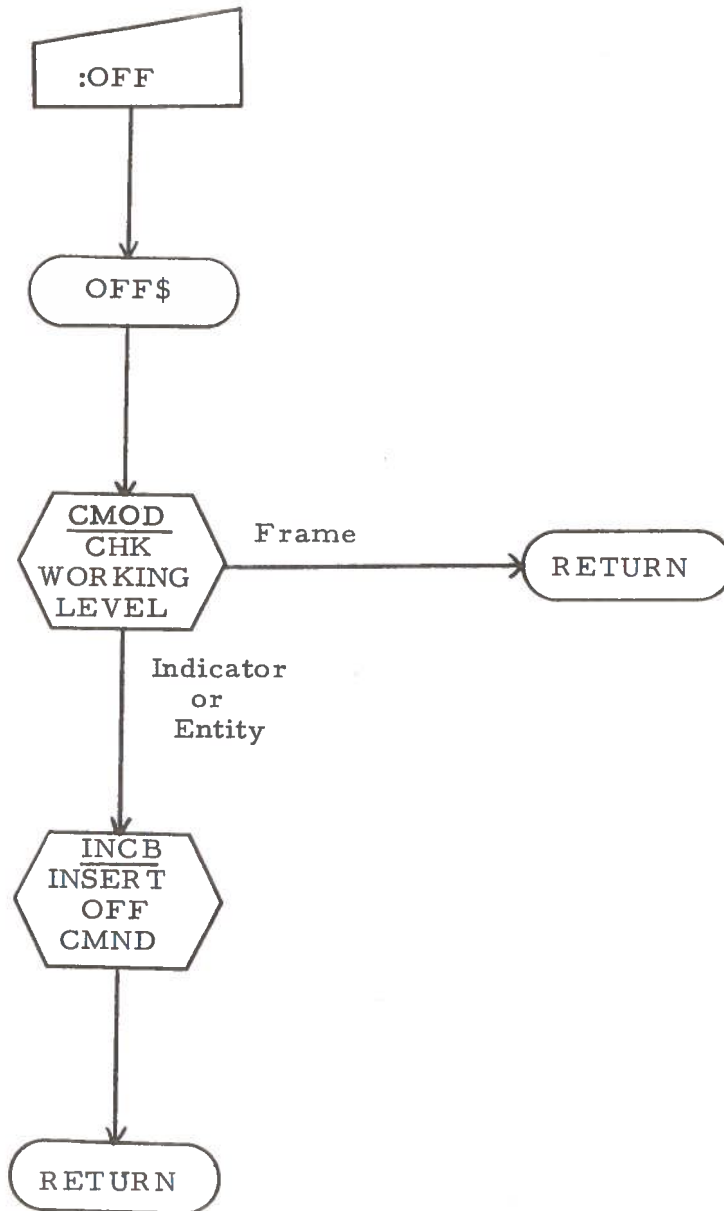
INTY  
FLOWCHART



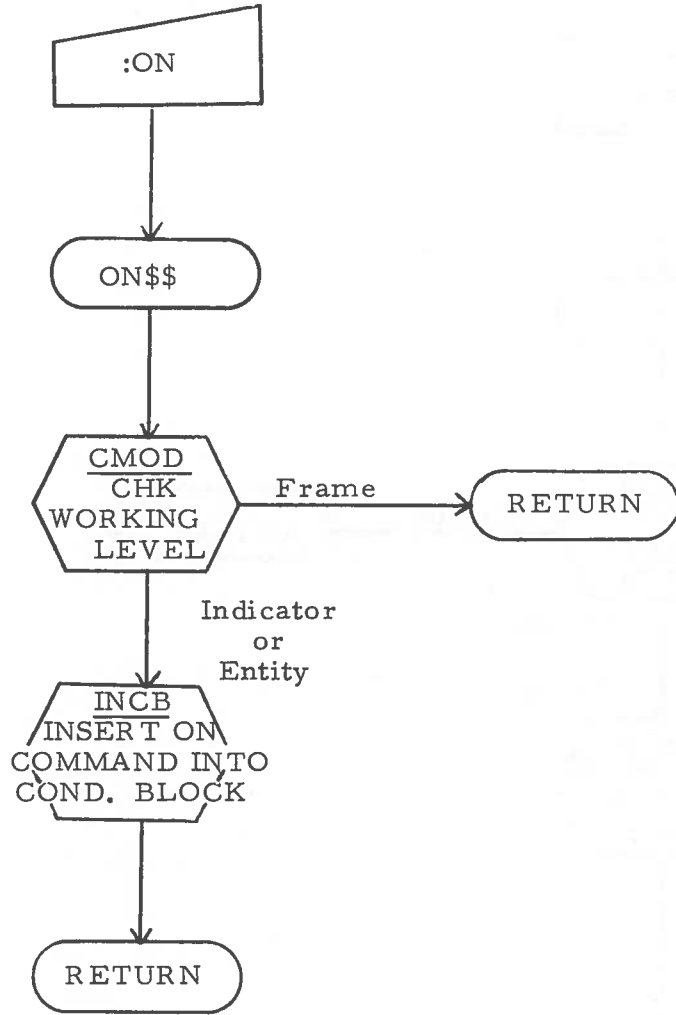
NEWF  
FLOW CHART



OFF\$  
FLOWCHART



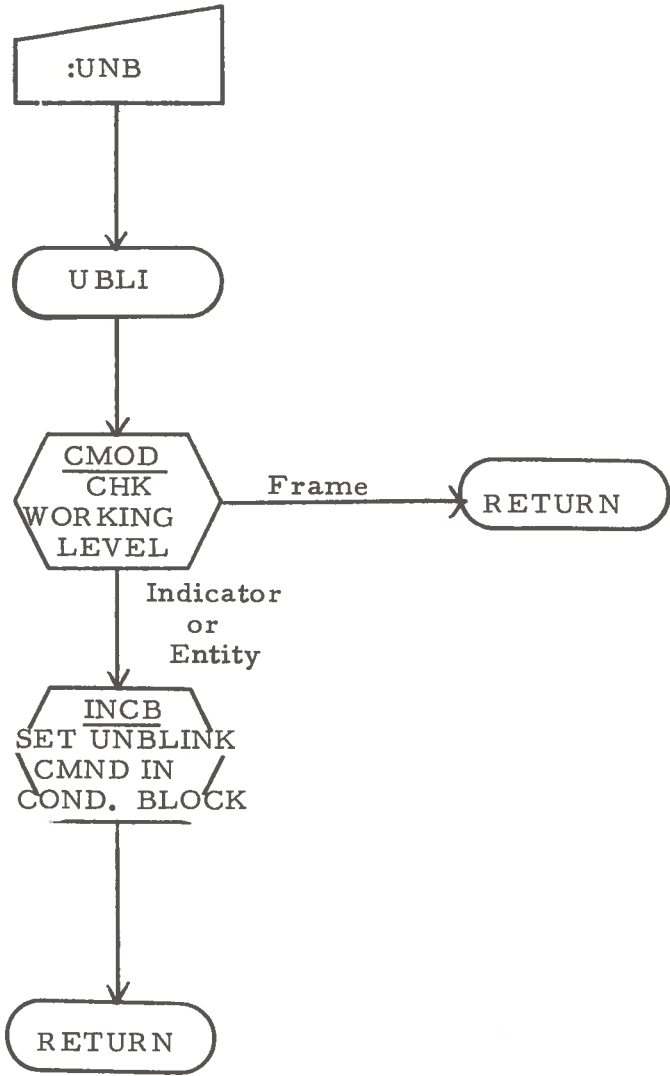
ON\$\$  
FLOWCHART





UBLI  
FLOWCHART

UNBLINK



## BLIN, LINE, PINT

### 1. PURPOSE:

- a) BLIN        Draw blank line
- b) LINE       Draw line
- c) PINT       Display point

### 2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	Prog

where:        Prog = BLIN, LINE, PINT

### 3. INPUT:

$\Delta x$ ,  $\Delta y$  from input buffer. Also dynamic expression if any from input buffer.

### 4. OUTPUT:

Appropriate display commands in component block and conditioning information in conditioning block if any.

### 5. ACTION:

Sets up return address and parameter depending upon entry point. Checks working level for entity. Gets component block address for x command and saves.

Gets  $\Delta x$  and inserts "LOAD  $\Delta X$  REL" command into component block. Checks for number sign indicating a dynamic expression is present. If not, jumps to input  $\Delta y$ .

If so, the component code, a pointer to the component, and the expression are moved into the conditioning block in the following format:

1	0010010		ignored	component code
0	Pointer to Component			
0	7-bit ASCII CHAR	X	7-bit ASCII CHAR	dynamic expression
0	⋮	X	⋮	
0	⋮	X	⋮	

$\Delta y$  is processed similar to  $\Delta x$ . The "Draw" or "Load" command, a "draw x rel" command is given where  $x=0$ .

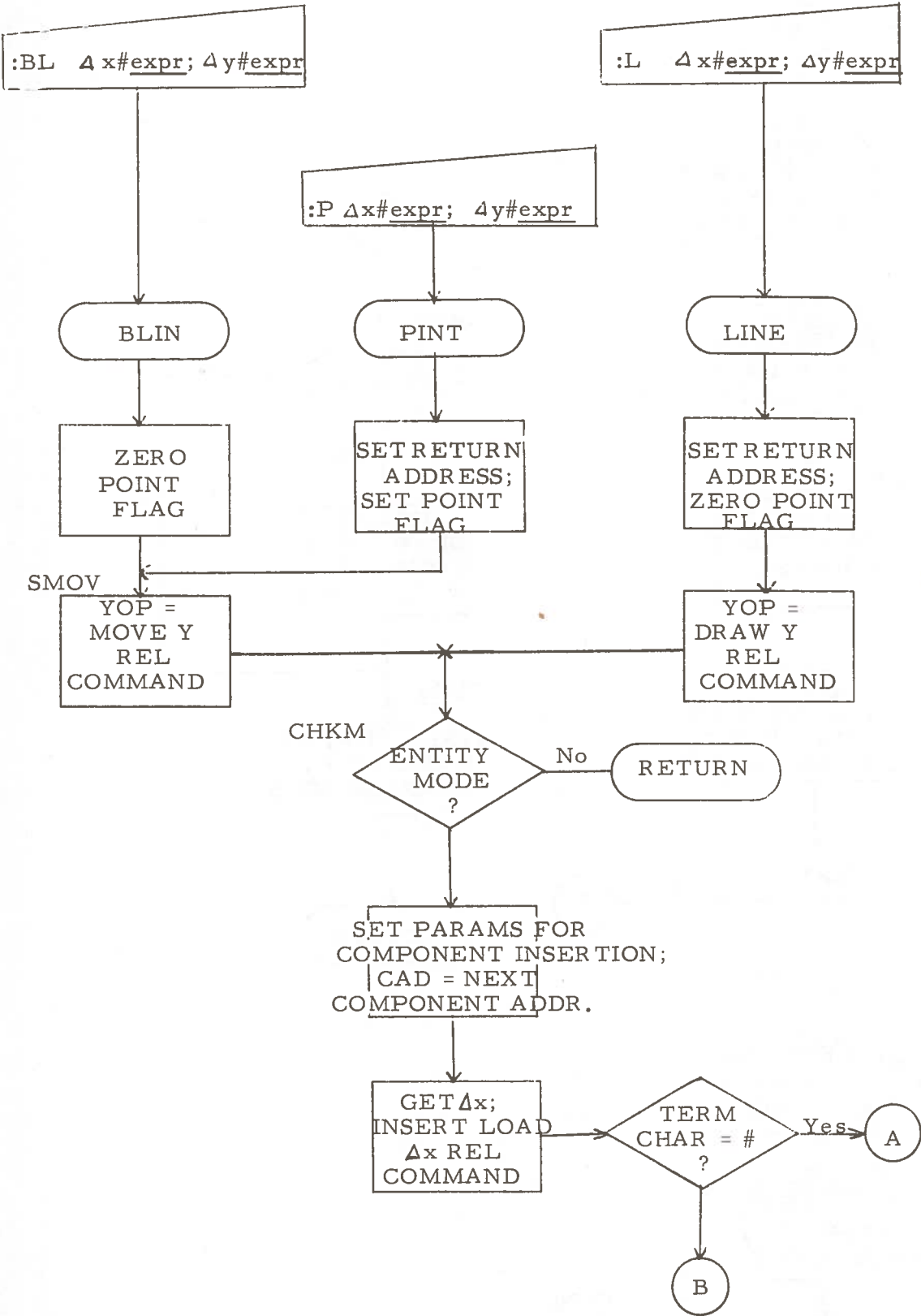
6. EXTERNAL REFERENCES:

- CMOD
- GTNO
- COP
- CIN
- CLO
- INCB
- CPAD

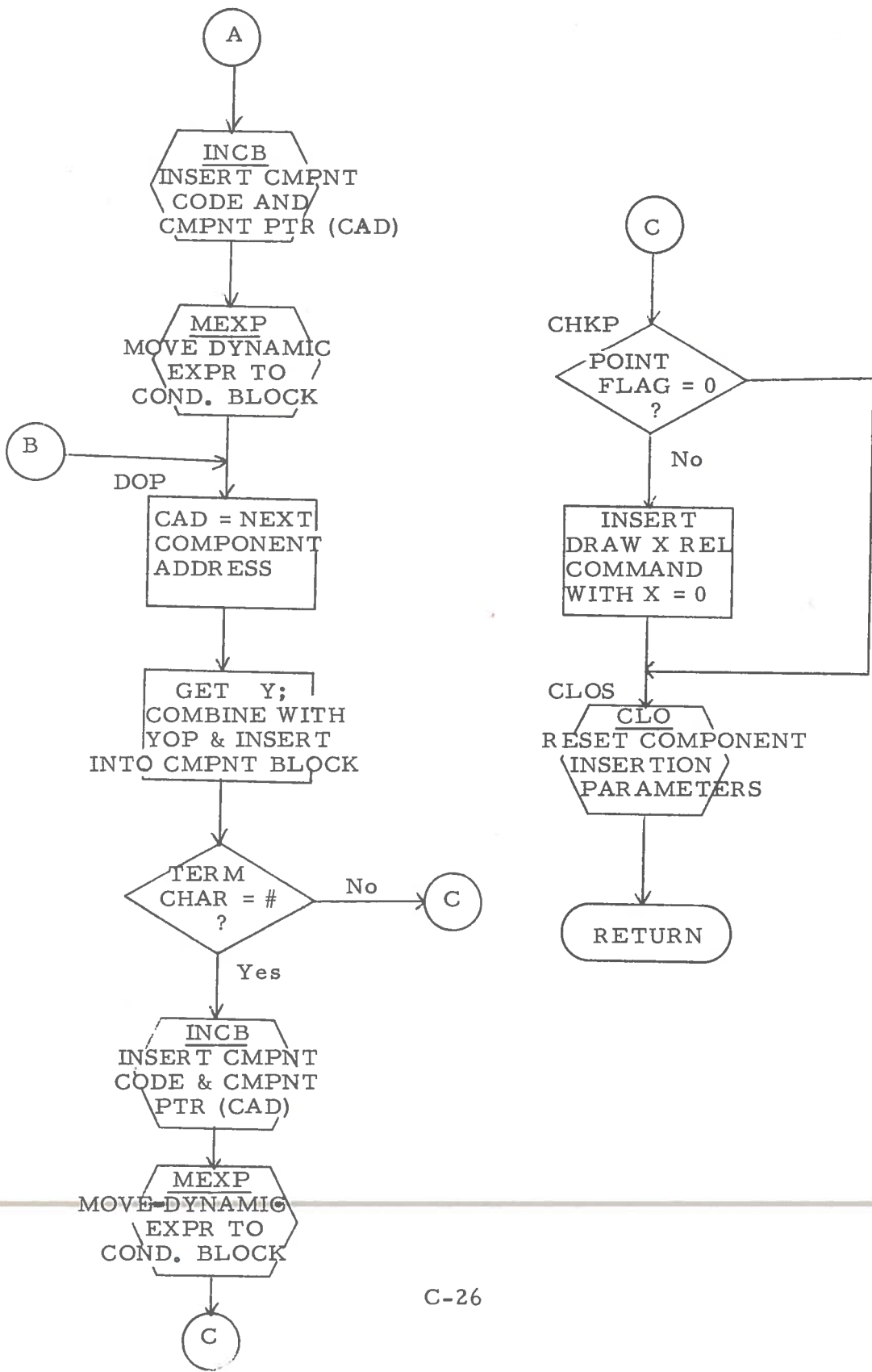
7. CORE USED:

125<sub>8</sub>

Draw Blank Line, Draw Line, or Draw Point



BLIN, LINE, PINT  
FLOWCHART



## DIRE

1. PURPOSE:

Set up display commands for Digital Readout

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	DIRE

3. INPUT:

Dynamic Expression, number of digits to display, and character size from input buffer

4. OUTPUT:

Character setup initialized to zero in component block.  
Digital Readout expression set up in conditional block.

5. ACTION:

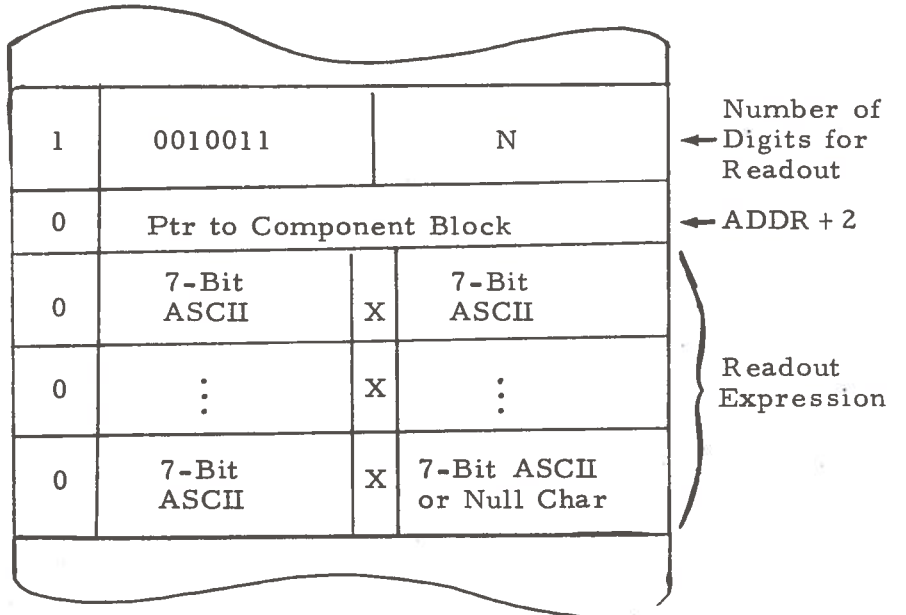
Checks for entity working level. Exits on frame and indicator level. Bypasses expression. Gets number of digits to display. Inserts "enter char mode" command into component block. Gets size and sets command into component block. Inserts Digital Readout command, component block pointer and expression in conditional block. Sets characters in component block to zero. Sets component block to return to coordinate mode and returns.

6. EXTERNAL REFERENCES:

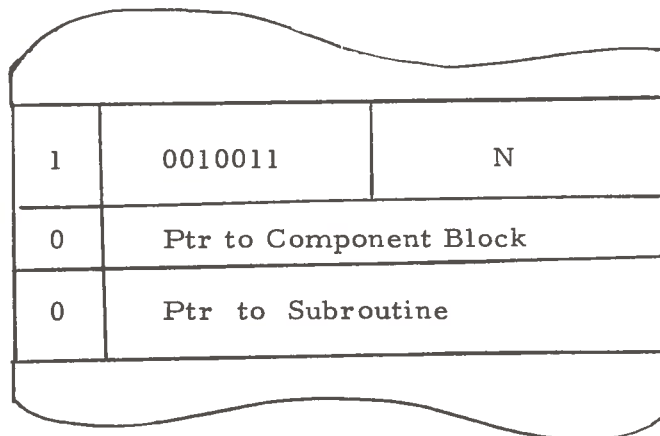
CMOD, SAVP, GIIC, WHAT, GTNO, TAO\$, COP, CIN,  
CLO, CPAD, INCB, RESP, MEXP

7. CORE SETUP OF DISPLAY FILE

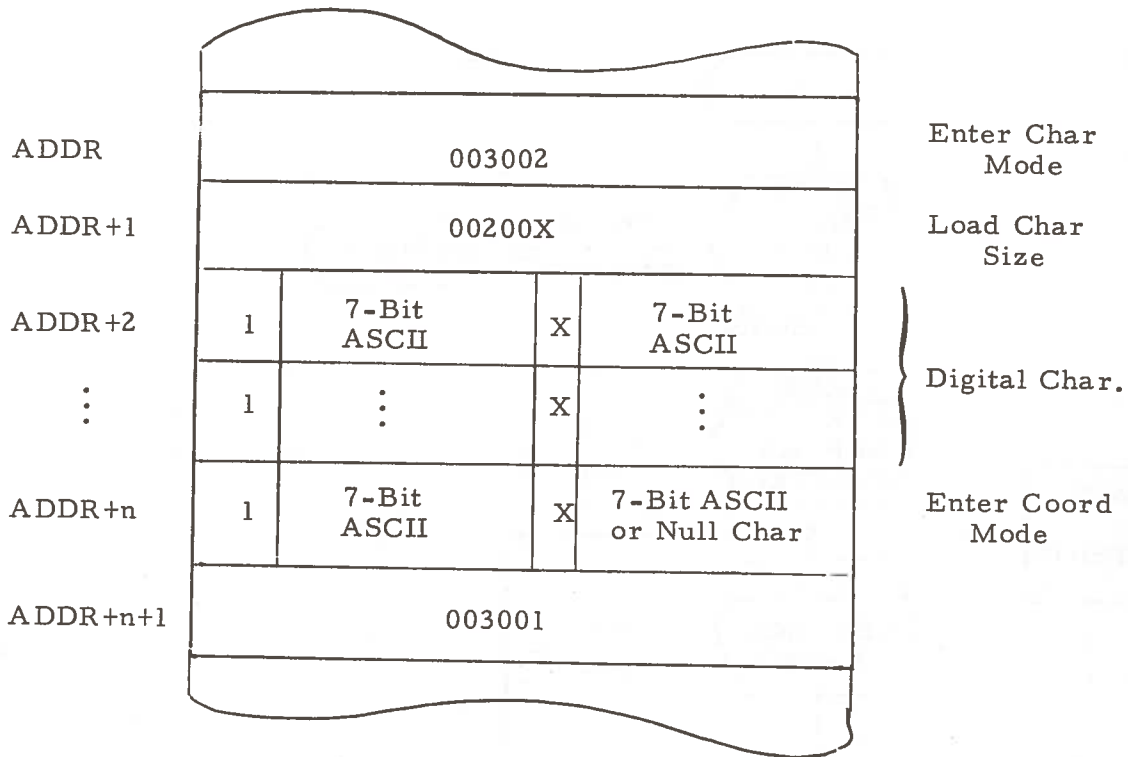
Cond. Block



Phase II  
Conversion



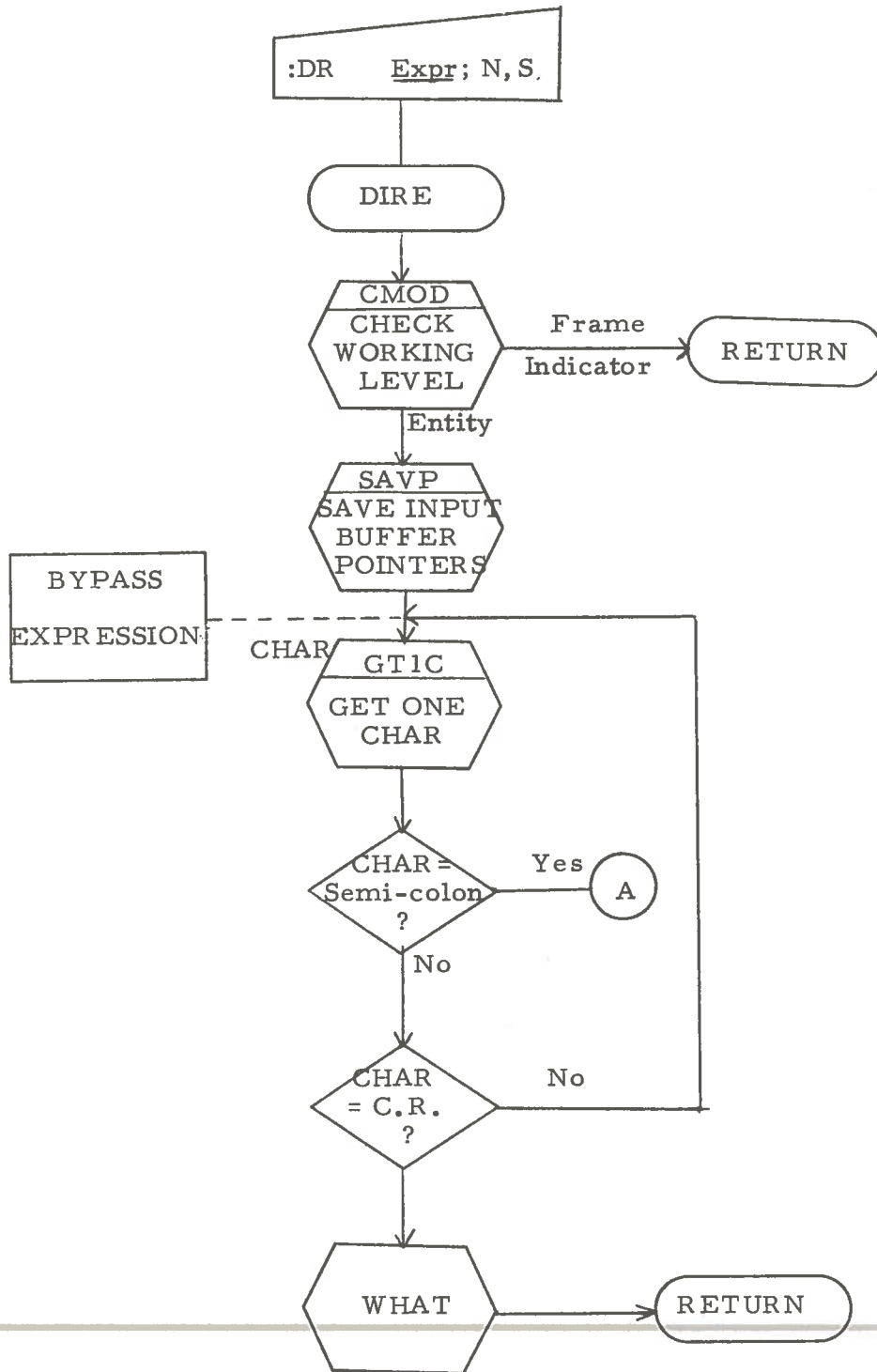
Component Block



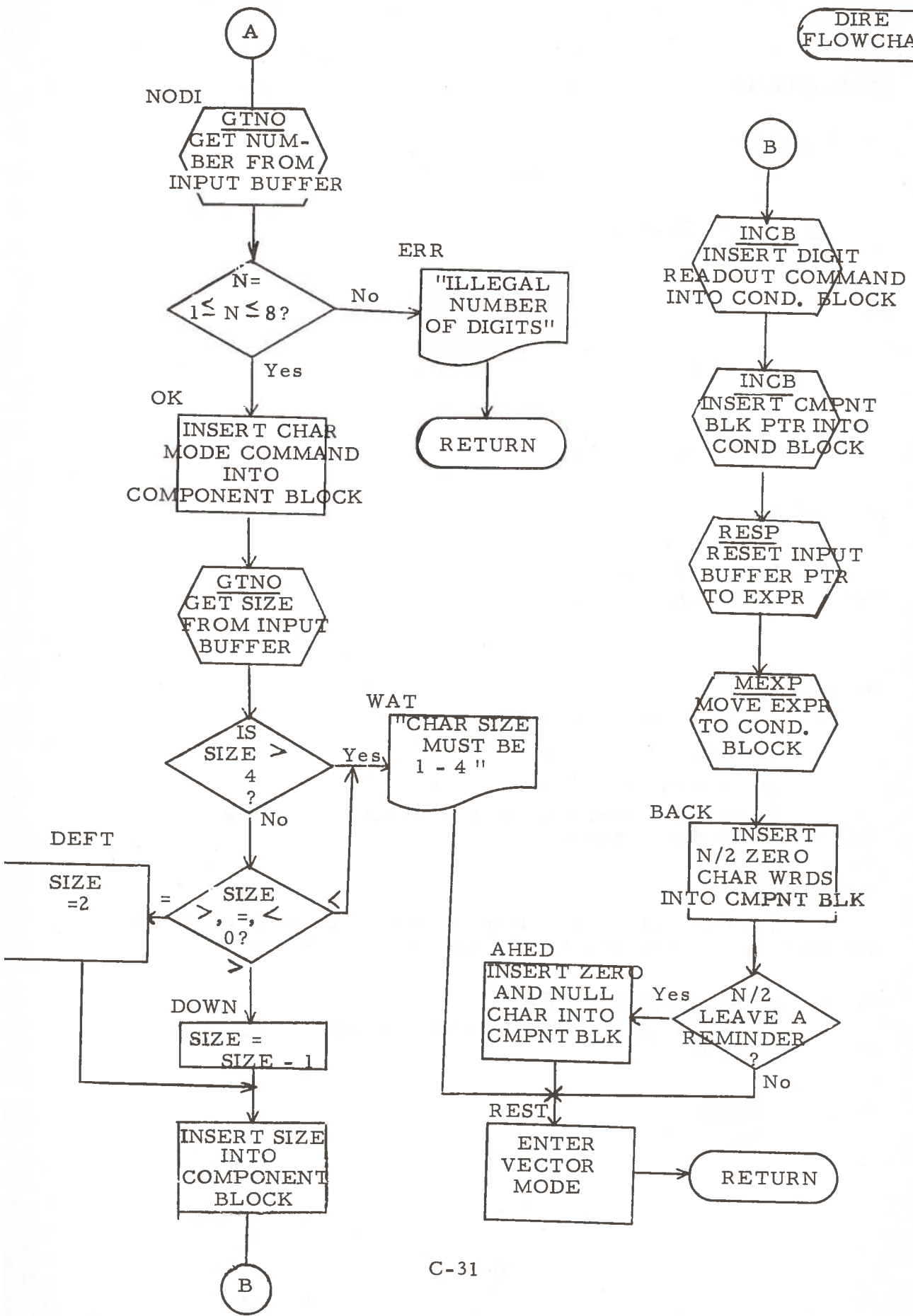


DIGITAL READOUT

DIRE  
FLOWCHART



DIRE  
FLOWCHART



SUBROUTINE            DLET

1. PURPOSE

Delete Frames, Indicators, and Entities.

2. CALLING SEQUENCE:

	JST	PTR
	.	
	.	
	.	
PTR	DAC	COML+N
	.	
	.	
COML+N	XAC	DLT

3. INPUT

- a) D F ID, I ID, E ID
- b) D F ID, I ID
- c) D F ID
- d) D

4. OUTPUT

- a) Delete specified entity.
- b) Delete specified indicator.
- c) Delete specified frame.
- d) Delete current entity if at entity level; otherwise type 'ILLEGAL WORKING LEVEL'.

5. ACTION

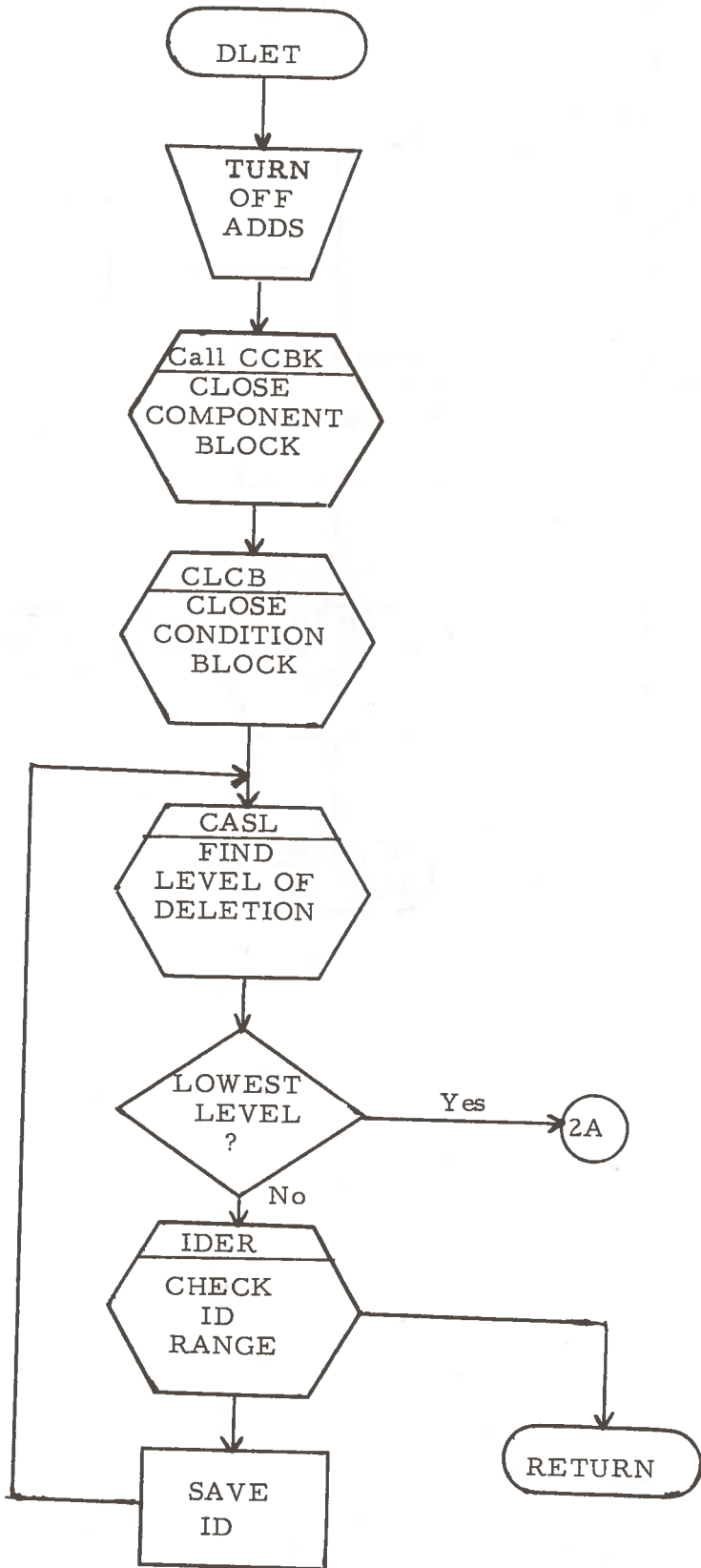
The highest level block to be deleted and all lower levels are removed from the display list and returned to free storage.

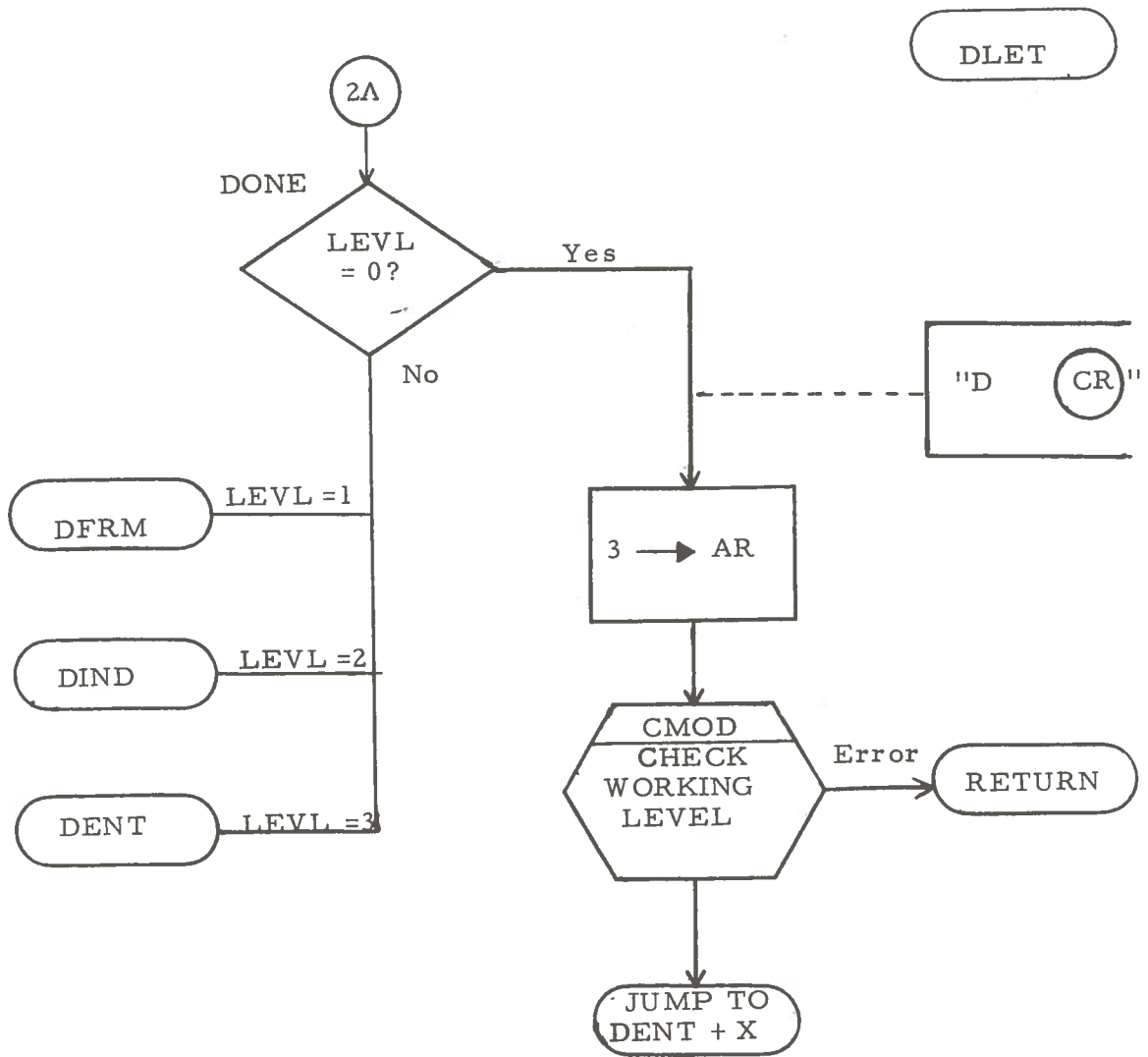
6. EXTERNALS REFERENCED

FRID, FRAD, INID, ENID, ENAD, LFAD, LIAD, LEAD, FRHD, and CLVL.

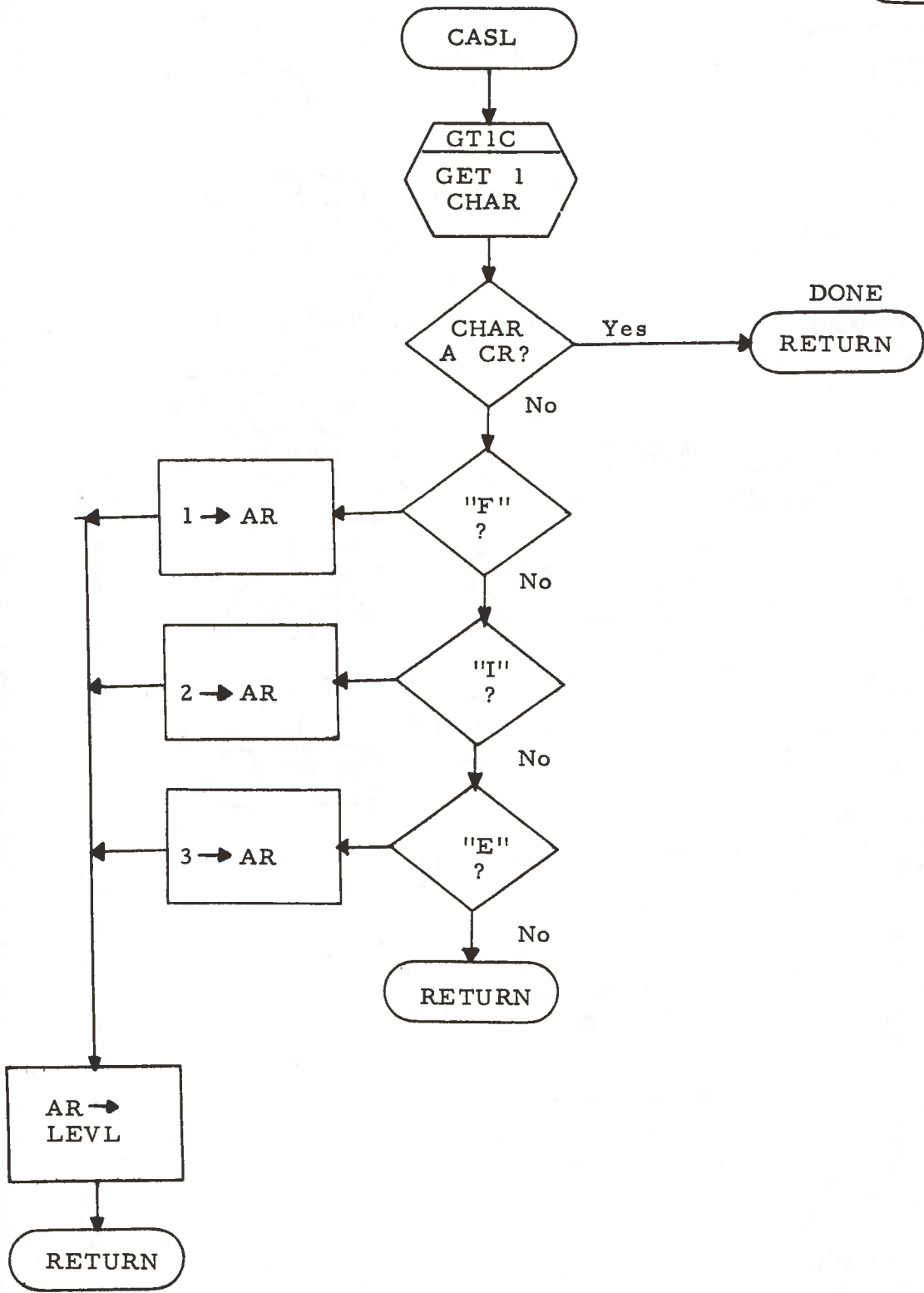
7. CORE USED

446<sub>8</sub>



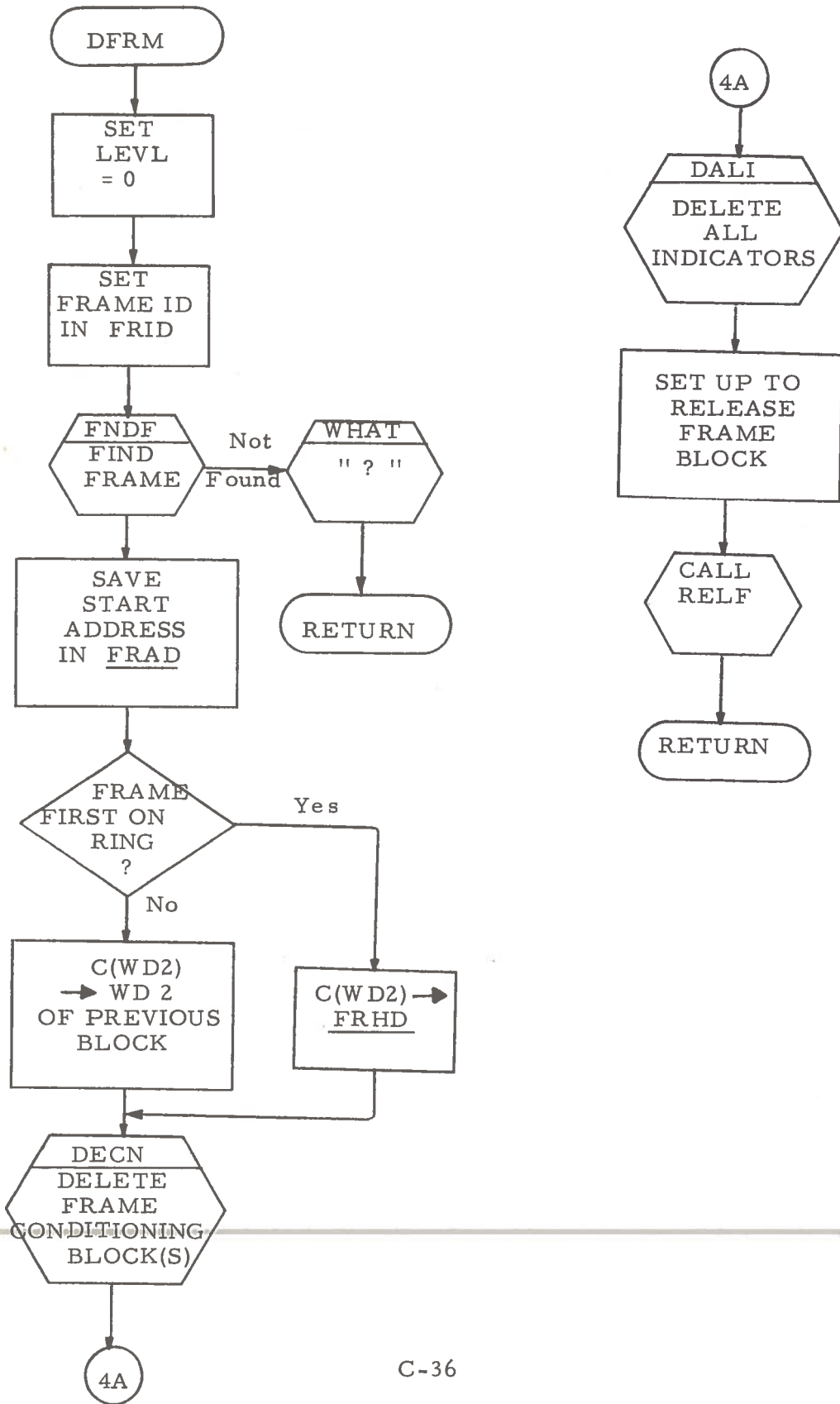


DLET



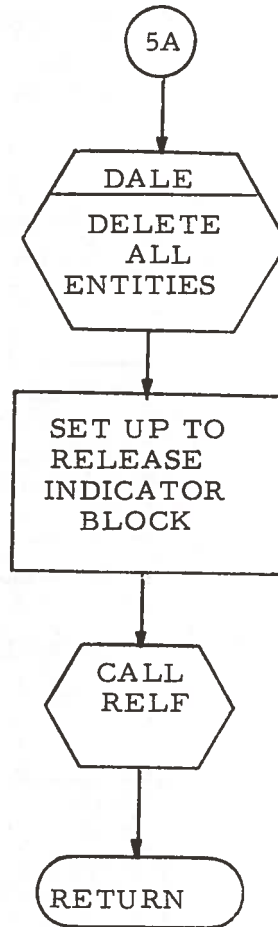
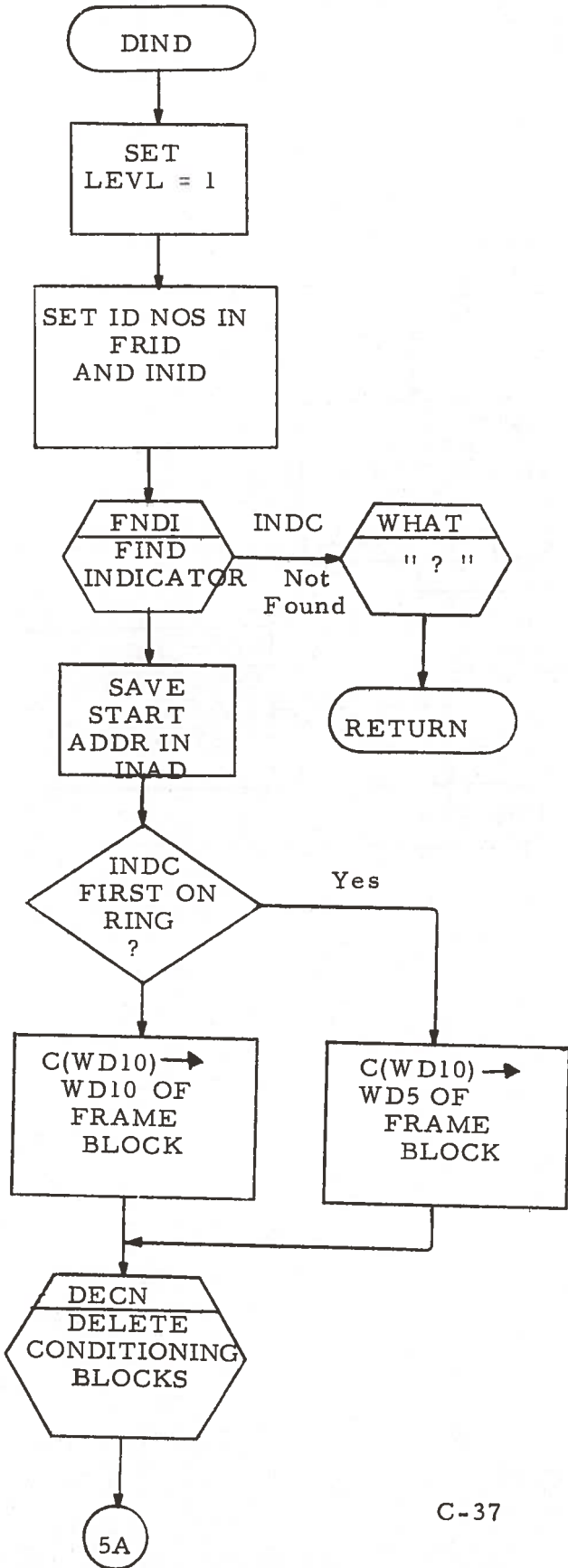
DFRM - Delete Single Frame

DLET



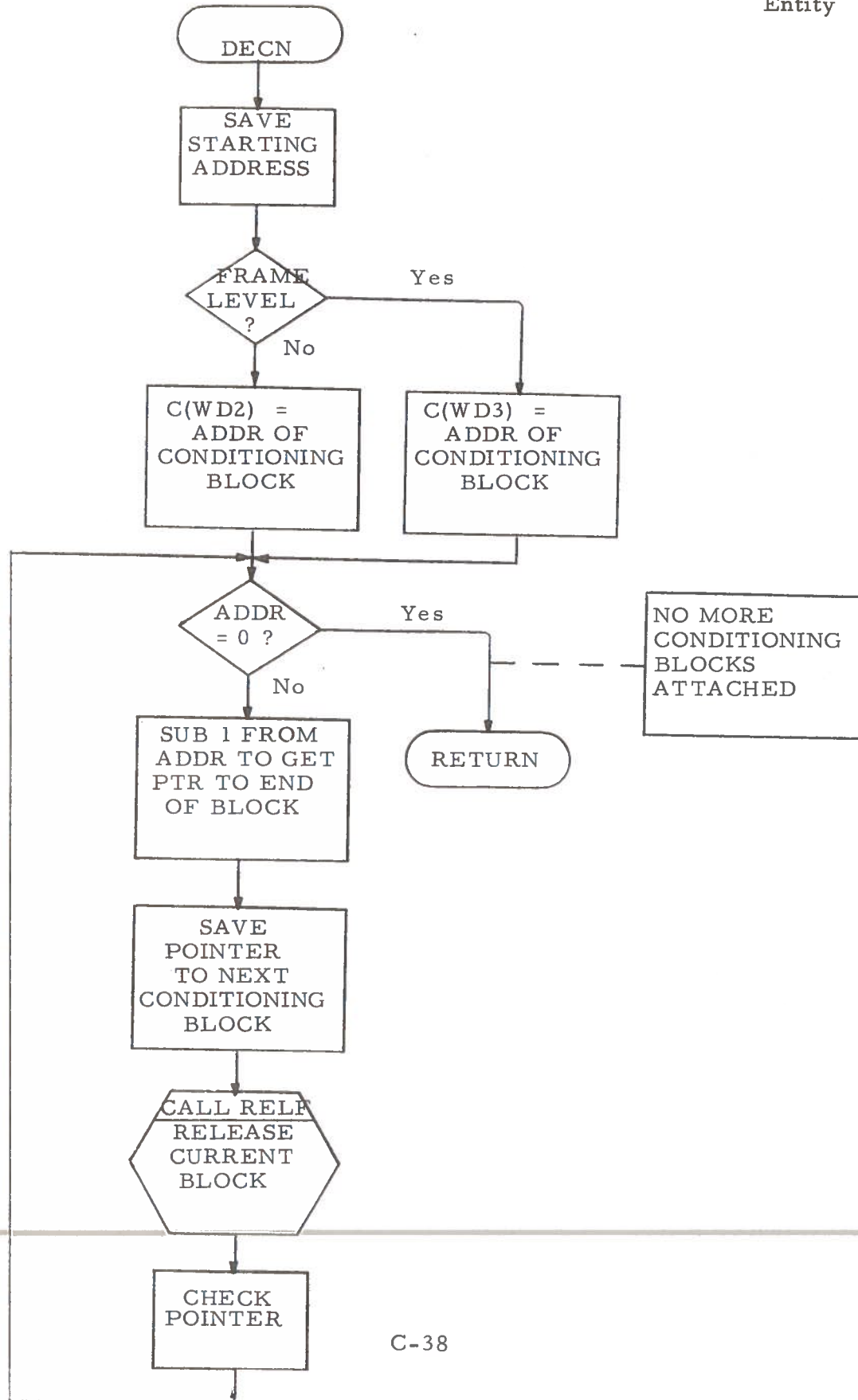
DIND - Delete Single Indicator

DLET



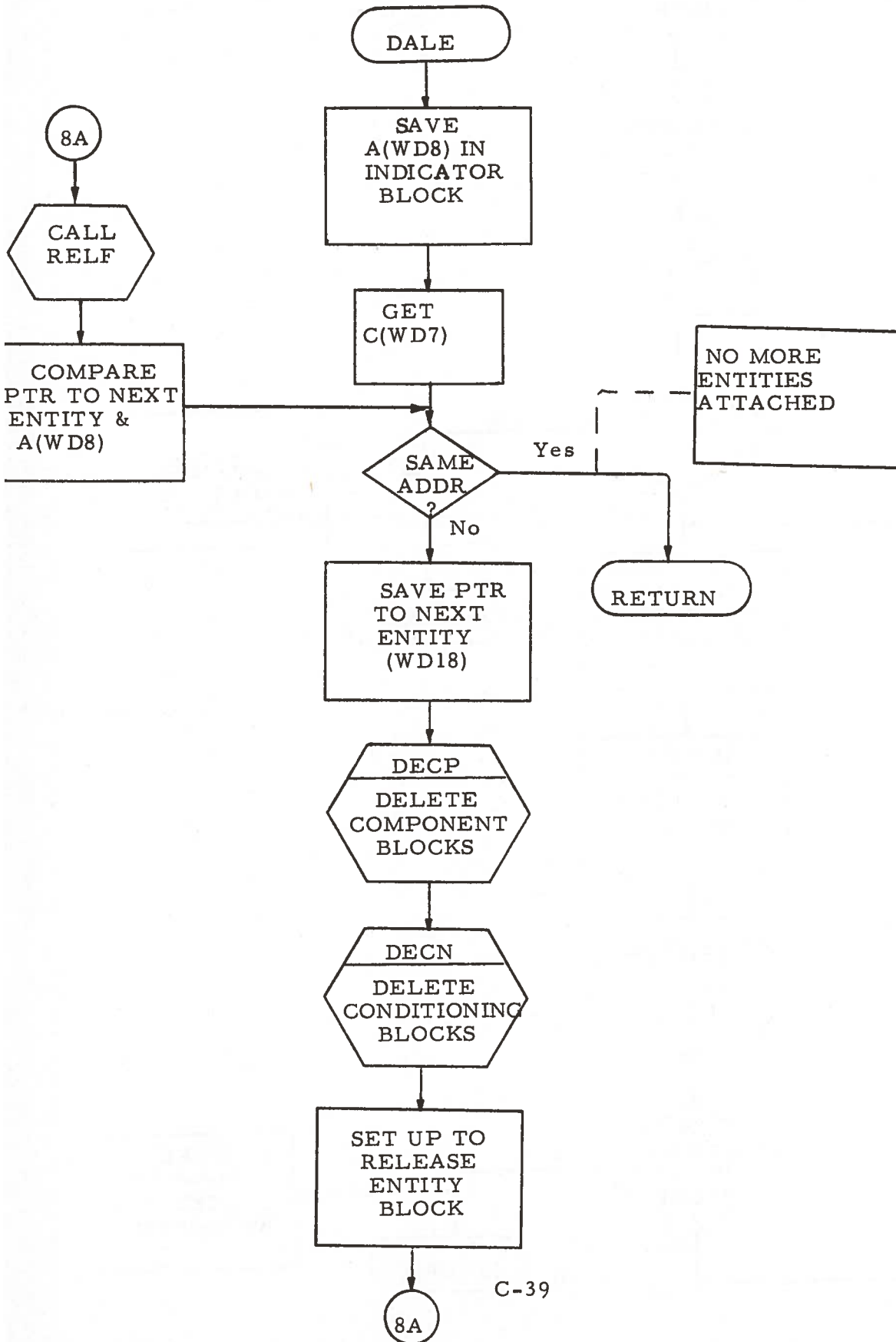


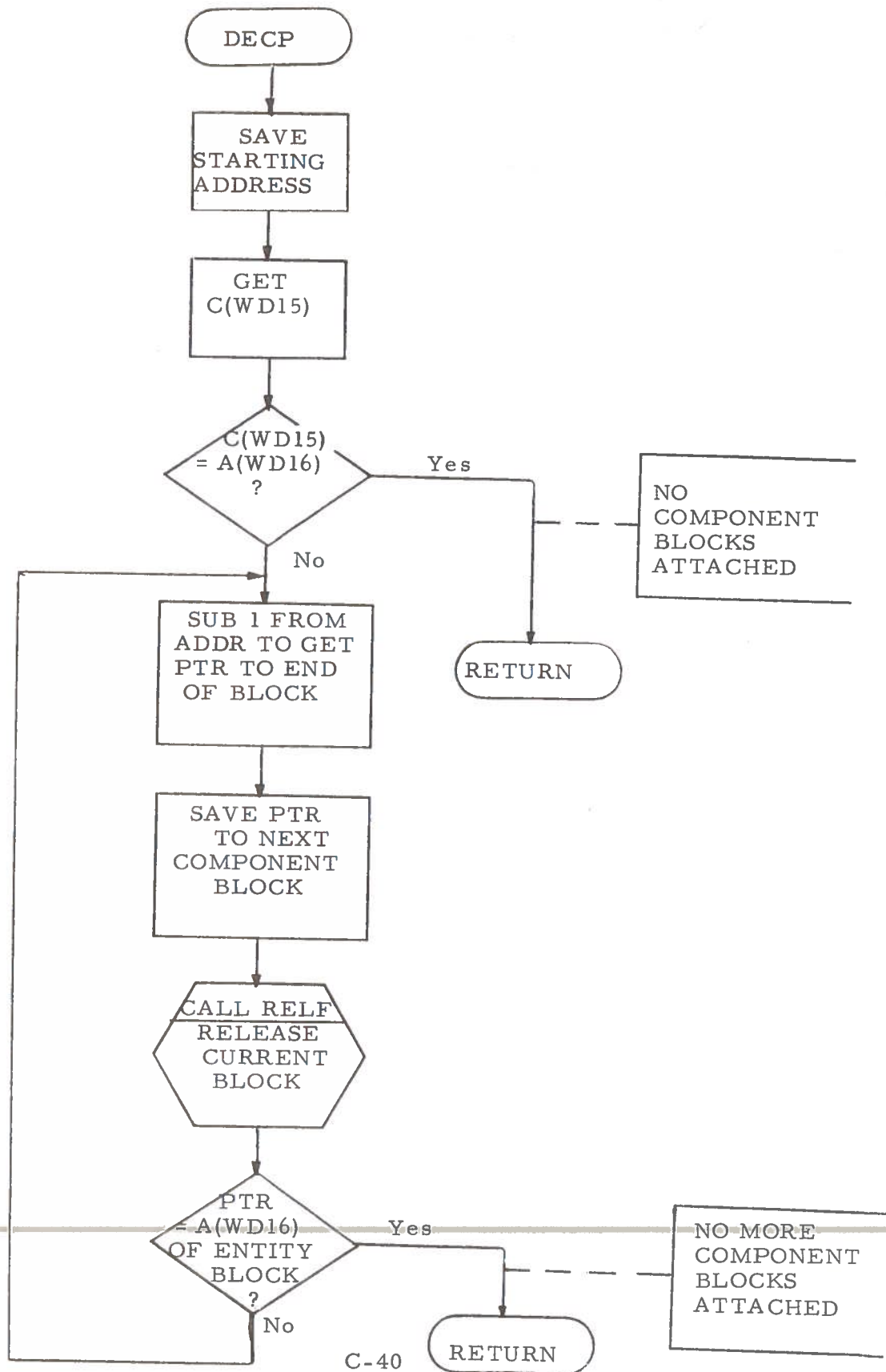
DECN - Delete Conditioning Blocks on Entry A = Addr of Frame, Indicator or Entity



DALE - Delete all Entities  
Input - Start Address of Indicator in INAD

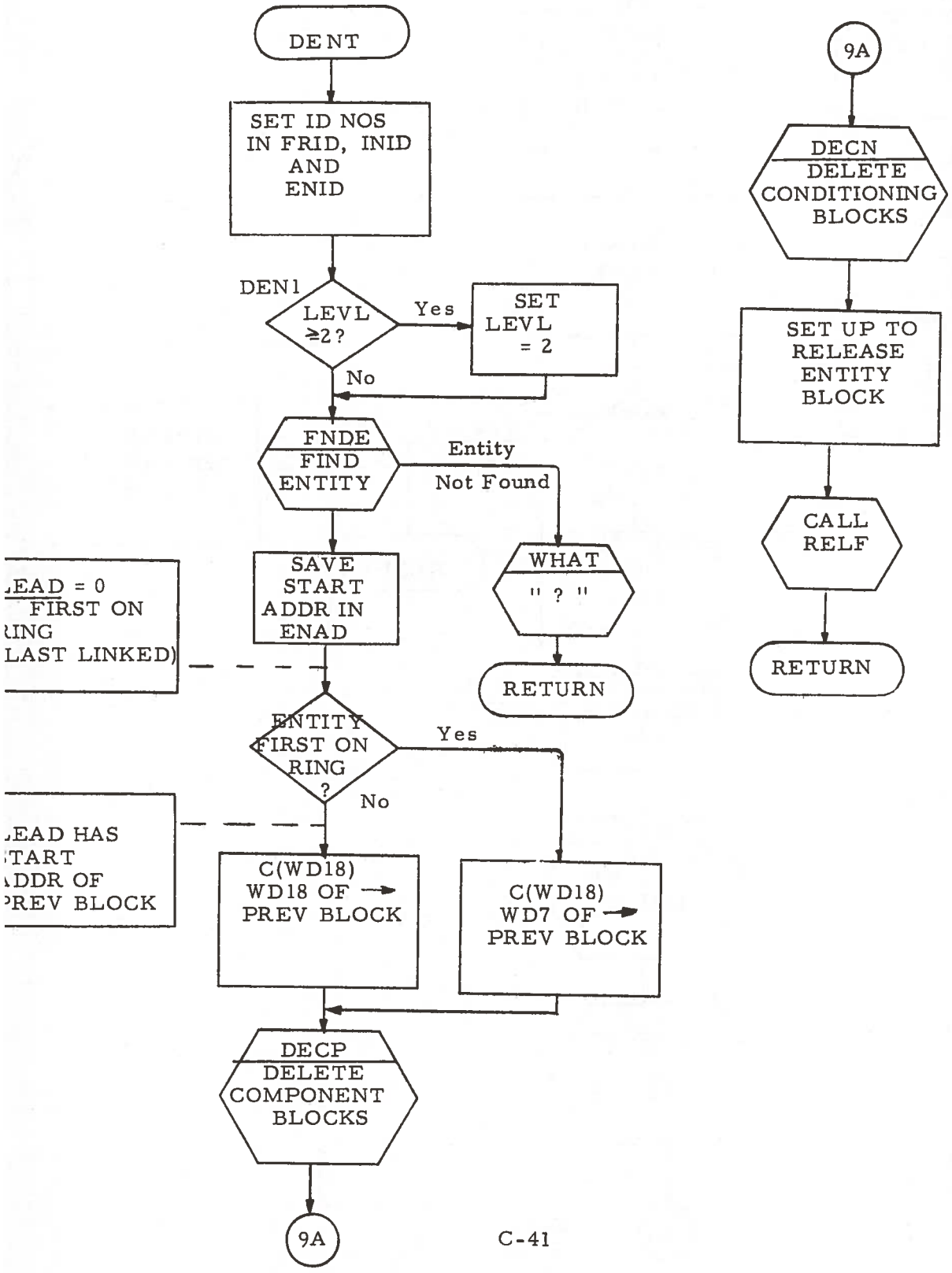
DLET



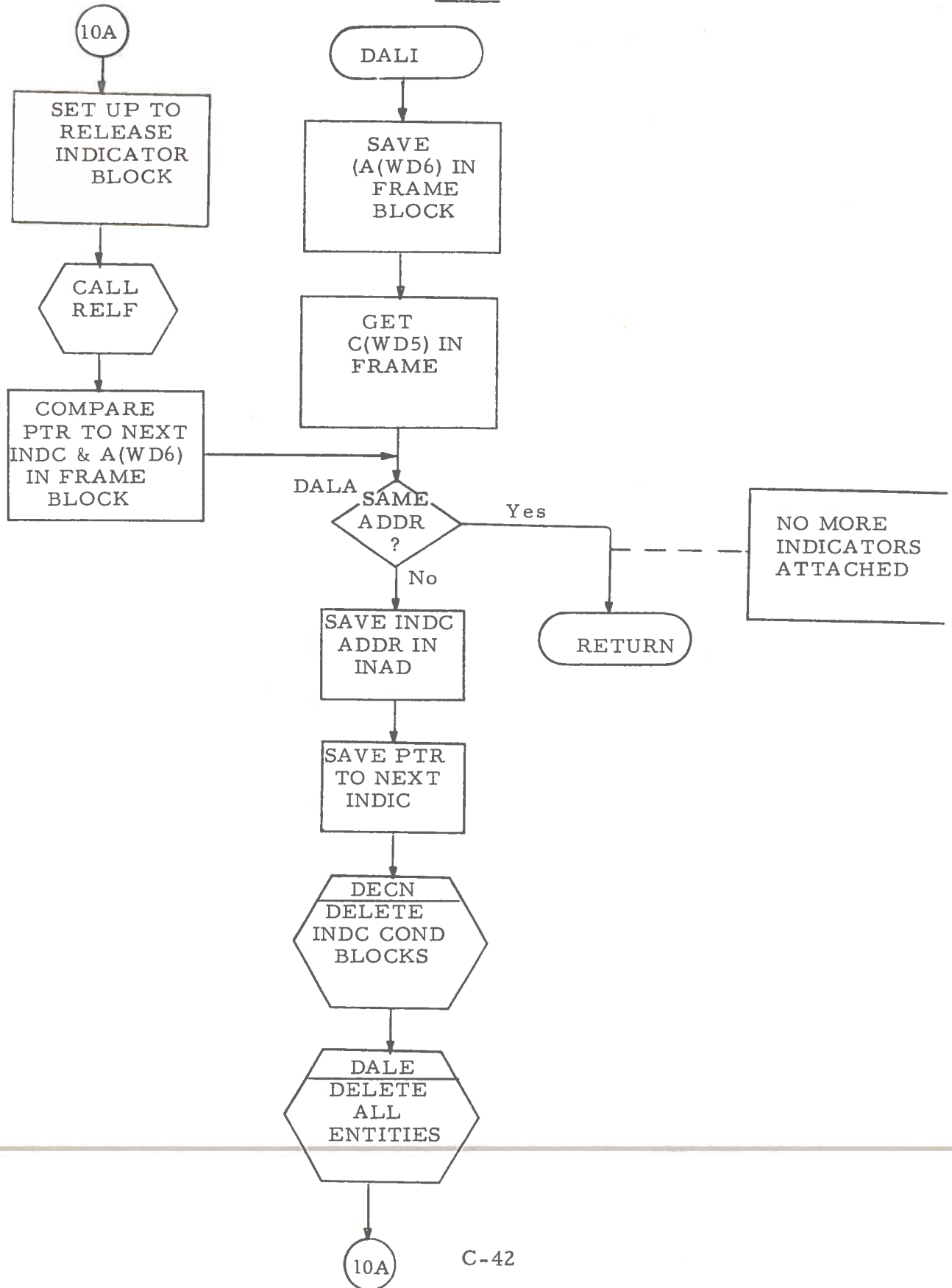


DENT - Delete Single Entity

DLET



DALI - Delete all Indicators in given frame  
 Input - Start Address of Frame in FRAD



SUBROUTINE:                   DOS

1.    PURPOSE:

      Return control to Disc Operating System.

2.    CALLING SEQUENCE:

	JST*	PTR
PTR	DAC	COML+N
	.	
	.	
	.	
COML+N	XAC	DOS

3.    INPUT:

      None

4.    OUTPUT:

      None

5.    ACTION:

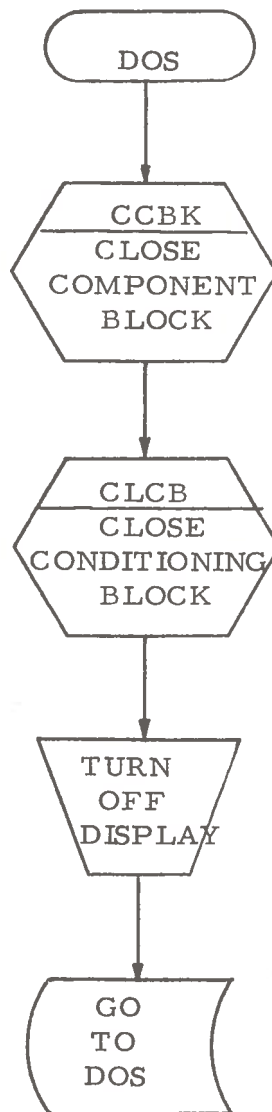
      The working level is set to zero, and the component and conditioning blocks are closed. The display is shut off and control passes to the Disc Operating System.

6.    EXTERNAL REFERENCES:

      CLVL - Current Working Level.

7.    CORE USED:

      14<sub>8</sub>



SUBROUTINE

DREG

1. PURPOSE:

Set up Register Definition input for later analysis

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	DREG

3. INPUT:

- a) Register to be defined in form  $R_i$   
where  $i=9, \dots, 99$
- b) An expression defining the given register

4. OUTPUT:

The Register Definition is inserted into the Register Definition Block in the following format:

1	0	Register Number	
0	7-Bit ASCII CHAR	X	7-Bit ASCII CHAR
0	⋮	X	⋮
0	7-Bit ASCII CHAR	X	7-Bit ASCII CHAR or 0



5. ACTION:

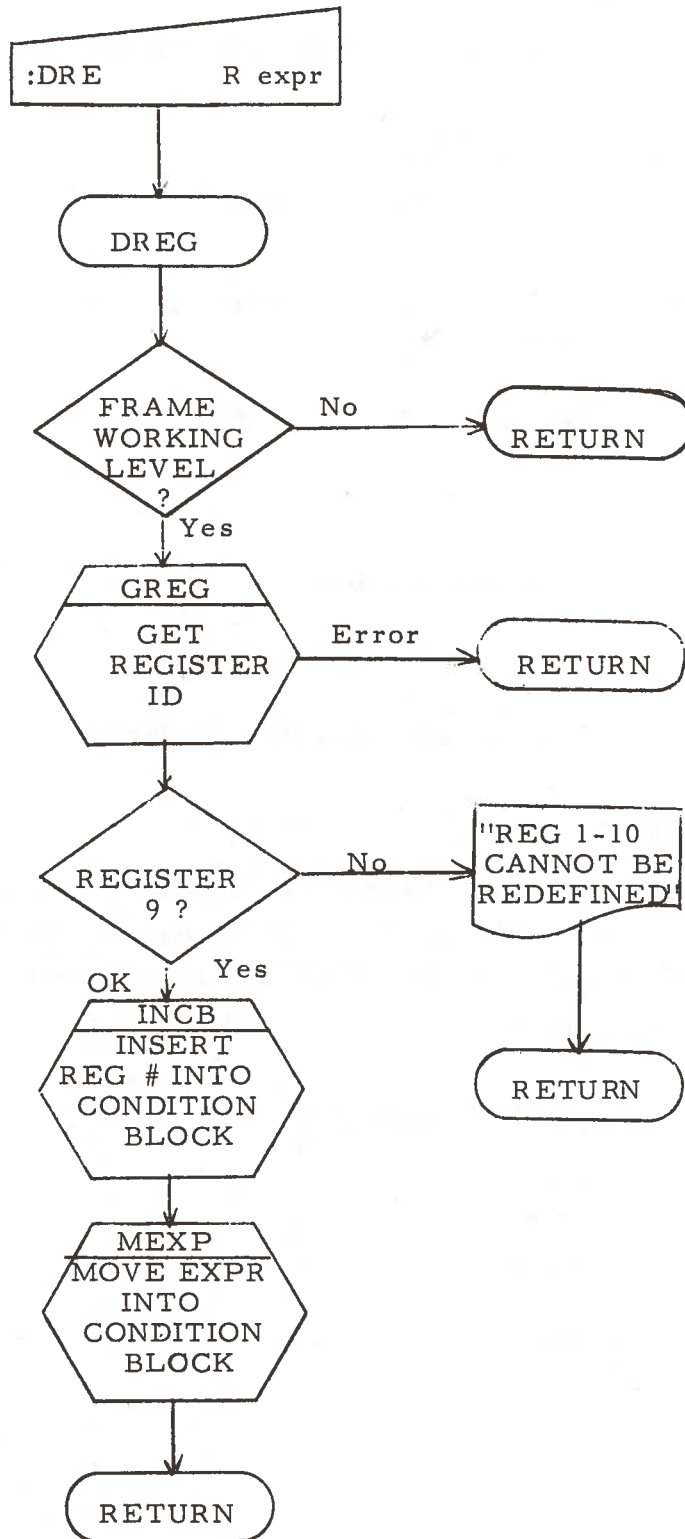
Checks for frame working level. Checks register to be defined. Ignores comma immediately following defined register, and spaces and tabs leading to expression. Moves expression, checking only for carriage return into Register Definition Block.

6. EXTERNAL REFERENCES:

MEXP  
CMOD  
GTIC  
GTNO  
WHAT  
INCB

7. CORE USED:

42<sub>8</sub>



SUBROUTINE

ENTY

1. PURPOSE:

Get or create entity; set working level to entity

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	ENTY

3. INPUT:

ID from input buffer

4. OUTPUT:

Sets current entity ID and address

5. ACTION:

Retrieves ID from input buffer. Retrieves or creates entity. If retrieved, the entity display is intensified. The current entity ID and address are set and control returns to the calling program.

6. EXTERNAL REFERENCES:

CLVL  
ENAD  
ENID

7. CORE USED:

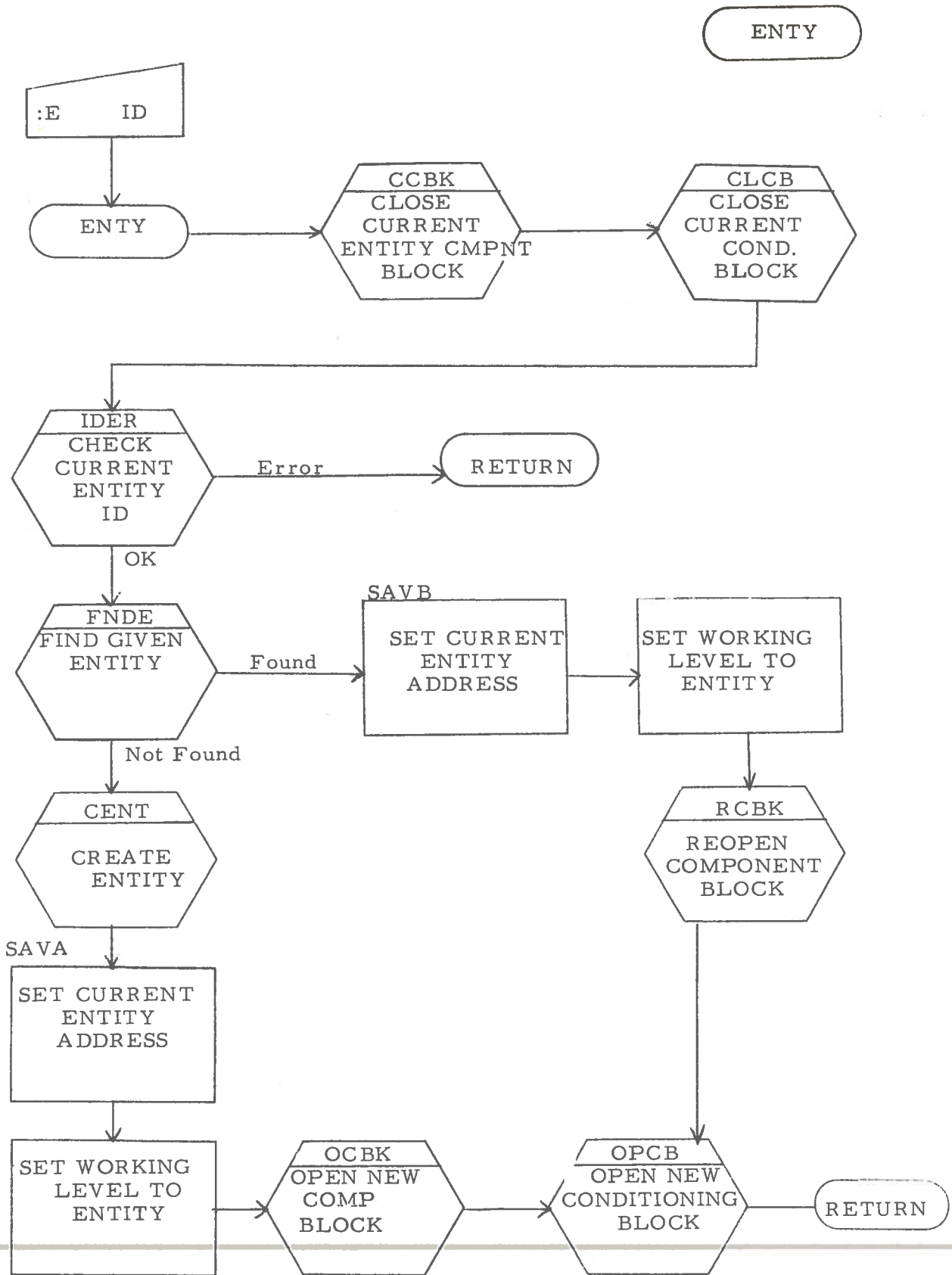
34<sub>8</sub>

6. EXTERNAL REFERENCES:

IDER  
FNDE  
CENT  
STEX  
INEN  
CLVL  
ENID  
ENAD  
CCBK  
OCBK  
CLCB  
OPCB

7. CORE USED:

32<sub>8</sub>



SUBROUTINE:                      FRAM

1.     PURPOSE:

        Find frame or create new frame with given ID

2.     CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	FRAM

3.     INPUT:

        ID number retrieved from input buffer

4.     OUTPUT:

        Frame address set

5.     ACTION:

        ID number is retrieved. An attempt is made to locate a frame with the given ID. If successful, the frame address is saved; frame is displayed; and working level set to frame.

        If unsuccessful, a new frame is created before returning. If storage is not available for the creation of a new frame, an appropriate message is typed before returning.

6. EXTERNAL REFERENCES:

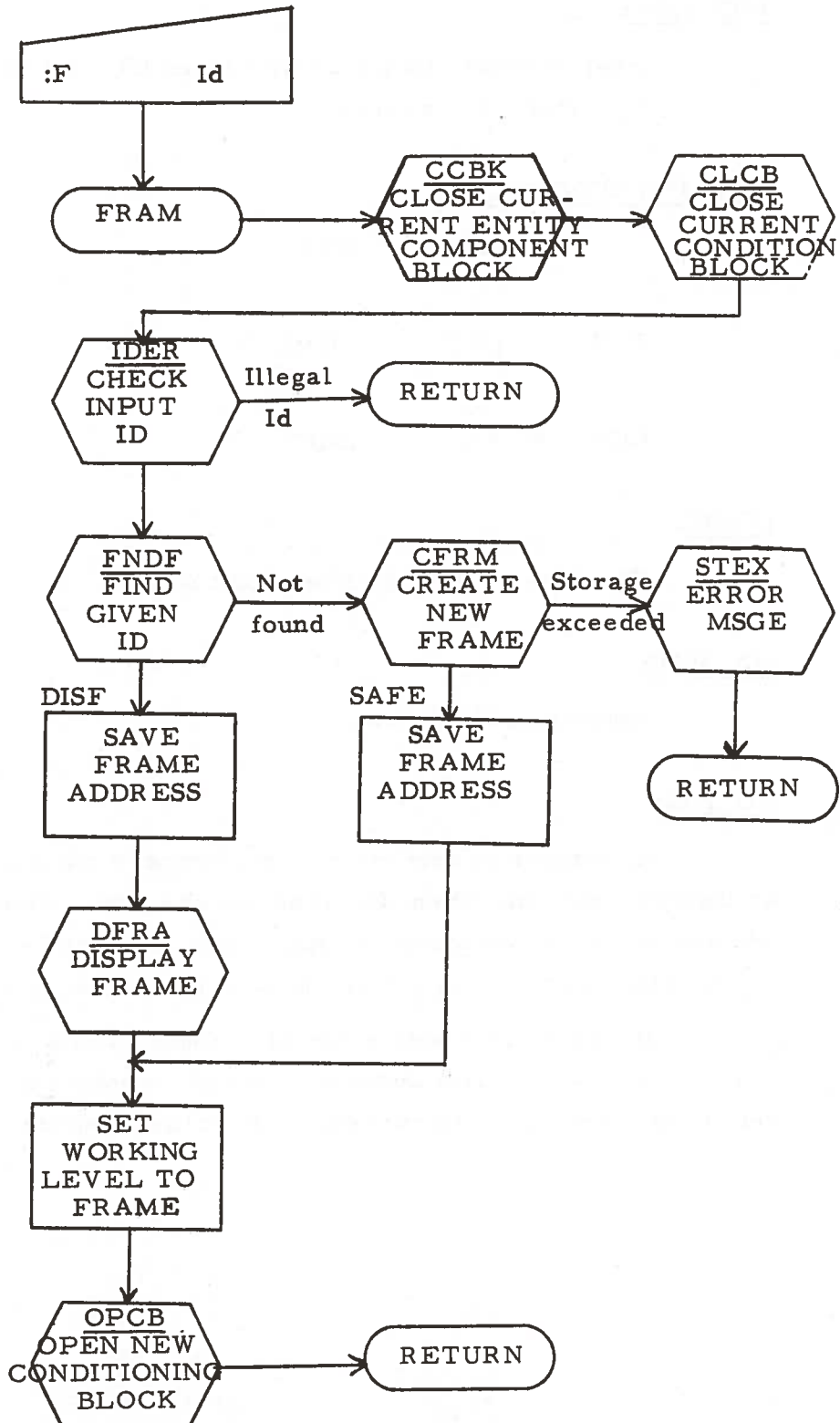
IDER  
FNDF  
CFRM  
STEX  
DFRA  
FRAD  
FRID  
CCBK  
CLVL  
OPCB  
CLCB

7. CORE USED:

$30_8$

FRAME

FRAM





## SUBROUTINE

## INDC

1. PURPOSE:

Find or create indicator with given ID. Set to indicator working level.

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	INDC

3. INPUT:

ID number retrieved from input buffer

4. OUTPUT:

Indicator address set

5. ACTION:

ID number is retrieved. An attempt is made to locate an indicator with the given ID. If successful, the indicator address is saved; indicator is intensified; working level is set to indicator, and control returns to calling routine.

If no indicator with given ID is found, a new indicator is created; the indicator address is saved; working level is set to indicator, and control returns to calling routine.

6. EXTERNAL REFERENCES:

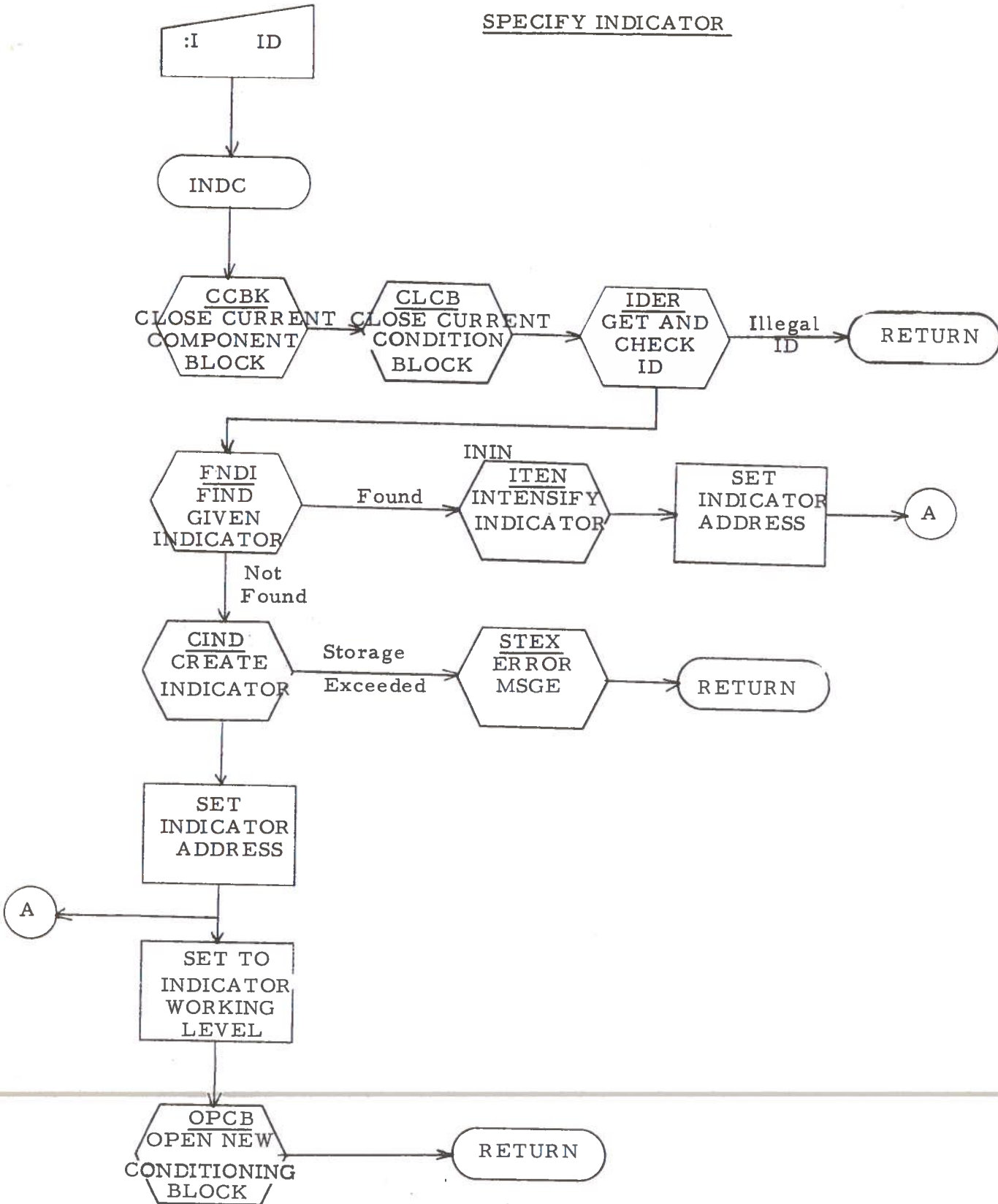
INAD  
INID  
CLVL

7. CORE USED:

27<sub>8</sub>

INDC  
FLOWCHART

SPECIFY INDICATOR



## ROTT

1. PURPOSE:

Set up dynamic expressions for entity ~~rotation~~

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	ROTT

3. INPUT:

Dynamic expressions for  $x$ ,  $y$ , and  $\Theta$  from  
input buffer

4. OUTPUT:

Expression packed two ASCII characters per word  
in conditioning block for current entity

5. ACTION:

Moves expressions into conditioning block. Inserts  
null word if no null characters packed by move routine,  
MEXP. Set up of expressions in conditioning block is  $\Delta x$ .  
Then  $\Delta y$  followed by  $\Theta$ , as below

Null  
Word  
Separates  
Expressions  
Following  
Even Number  
of Characters

1	000 00010		Ignored
0	7-Bit ASCII Char	X	7-Bit ASCII Char
		⋮	
0	7-Bit ASCII Char	X	0
0	7-Bit ASCII Char	X	7-Bit ASCII Char
		⋮	
0	0	X	0
0	7-Bit ASCII Char	X	7-Bit ASCII Char
		⋮	
0	7-Bit ASCII Char	X	0

Conditioning  
Block Rotate  
Command

X - Expression

Null Character  
Separates  
Expressions  
Following Odd Number  
Of Characters

Y - Expression

θ - Expression

Null Character  
Here terminates  
θ-expression.

6. EXTERNAL REFERENCES:

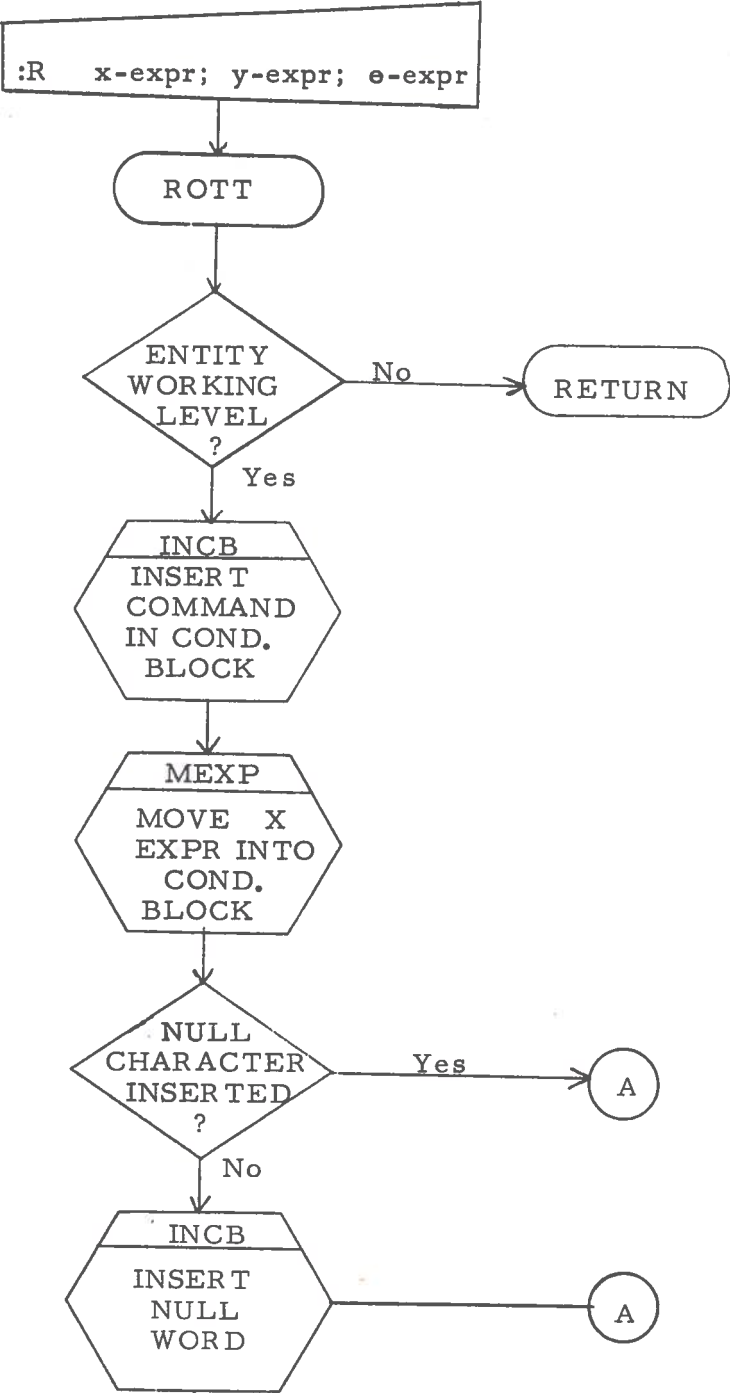
CMOD

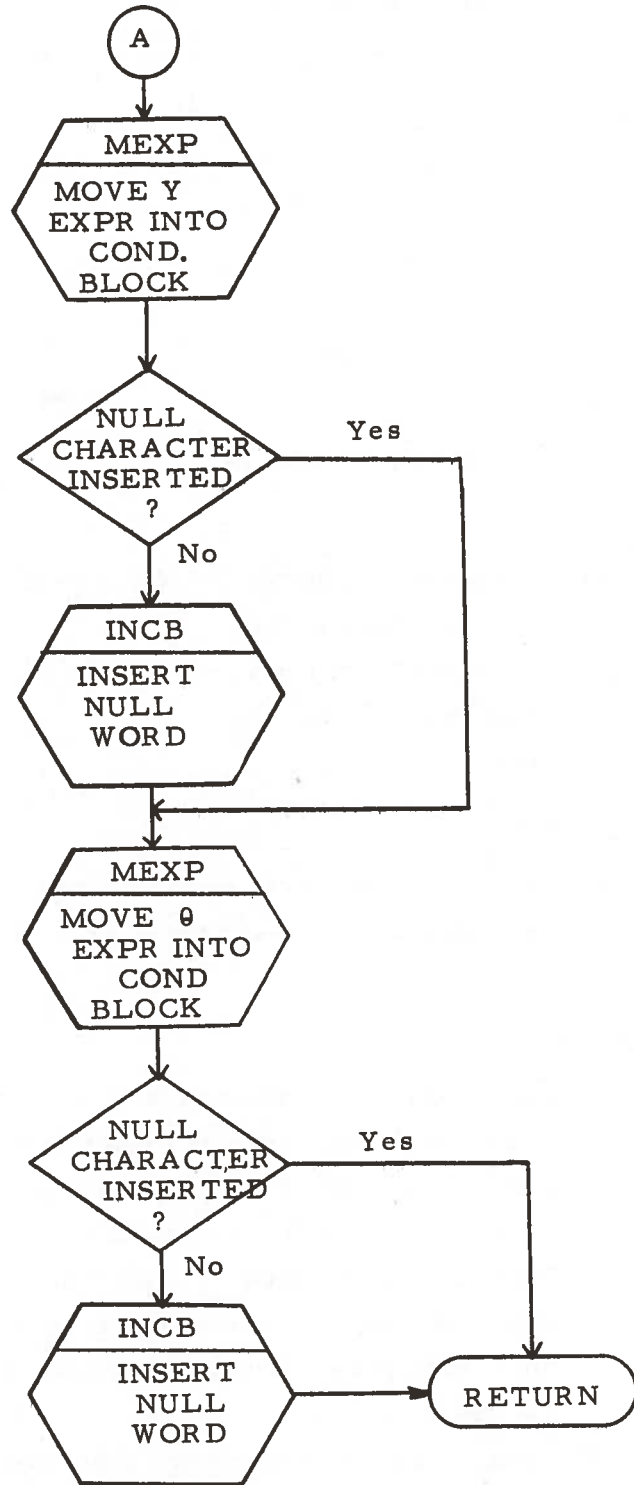
INCB

MEXP

7. CORE USED:

22<sub>8</sub>







SUBROUTINE:

SAVE, RLOD, INIT

1. PURPOSE:

- a) Save data structure on disc or paper tape
- b) Reload data structure from disc or paper tape
- c) Initialize data structure; ie, set up free storage ring

2. CALLING SEQUENCE:

	JST*	PTR
	.	
	.	
	.	
PTR	DAC	COML+N
COML+N	XAC	PROG

Where PROG = SAVE, RLOD, INIT

3. INPUT:

- a) File name, output device (disc or paper tape) and phase from the input buffer.
- b) File name, input device (disc or paper tape) and phase from the input buffer.
- c) None

4. OUTPUT:

- a) Data structure saved on disc or paper tape
- b) Data structure reloaded from disc or paper tape
- c) None

5. ACTION:

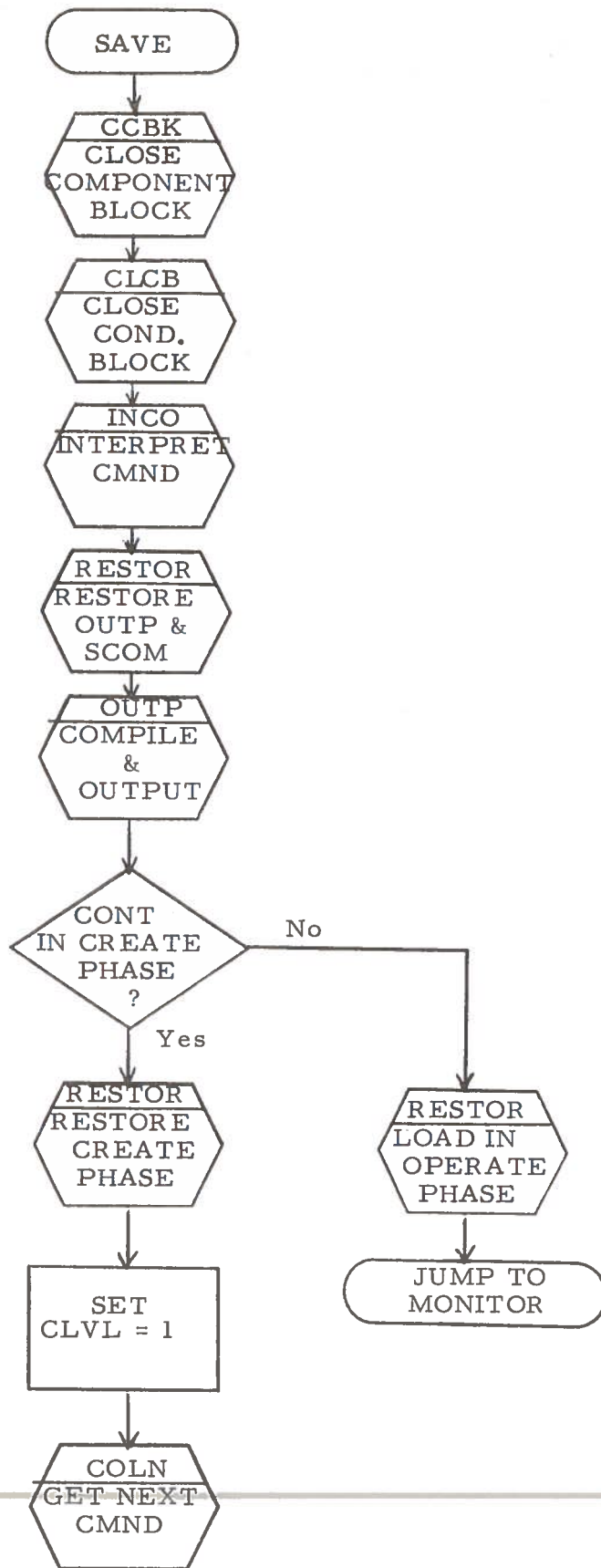
- a) The conditioning commands in the data structure are compiled and the data structure is output to the specified device. Control will pass to either the create phase or operate phase depending upon the "C" or "O" parameter.
- b) Free storage is initialized and the data structure is read in from either the disc or paper tape reader. Control will pass to either the create phase or operate phase, depending upon the "C" or "O" parameter.
- c) Pointers are initialized and free storage is attached to the free storage ring using  $100_8$  word blocks.

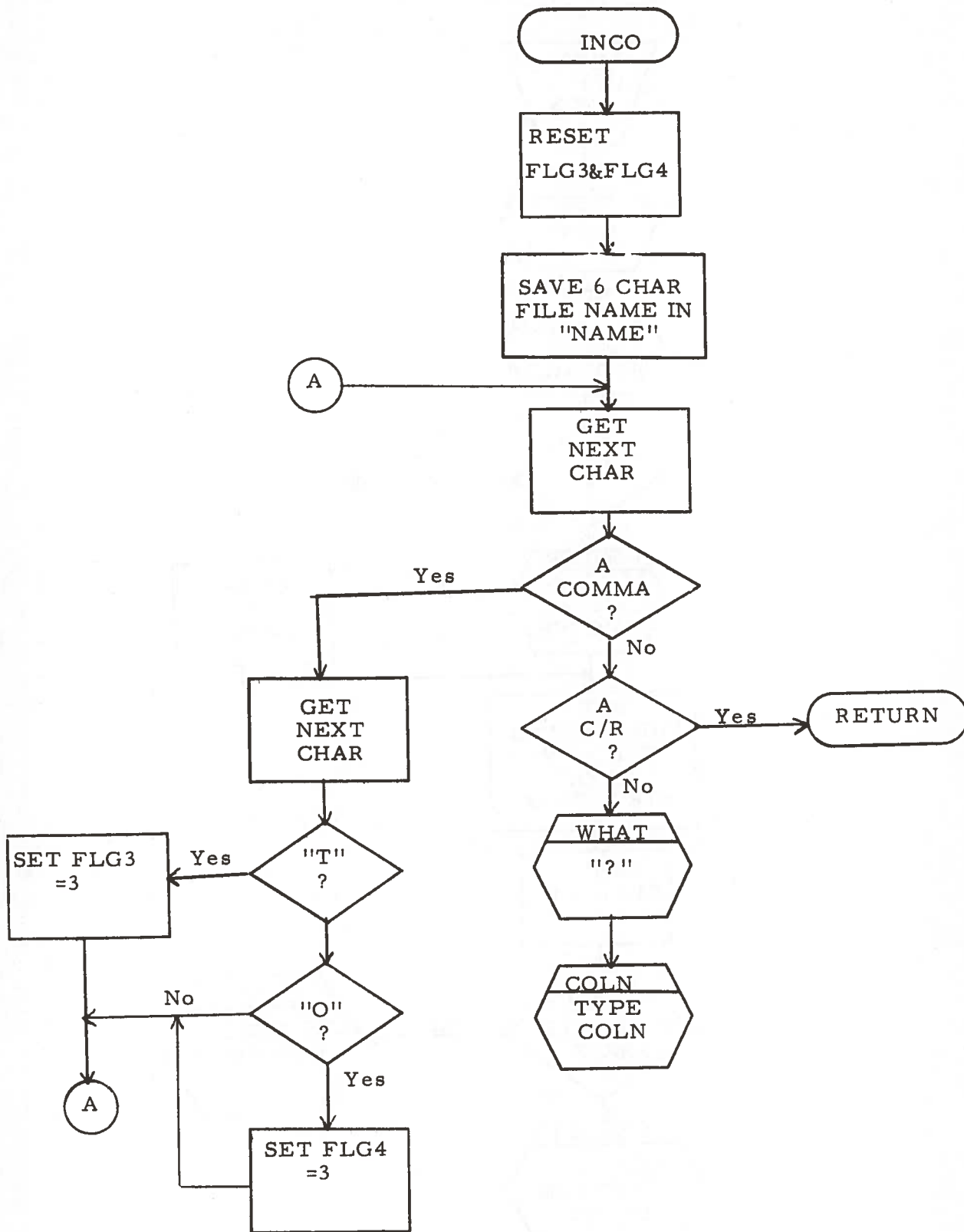
6. EXTERNAL REFERENCES:

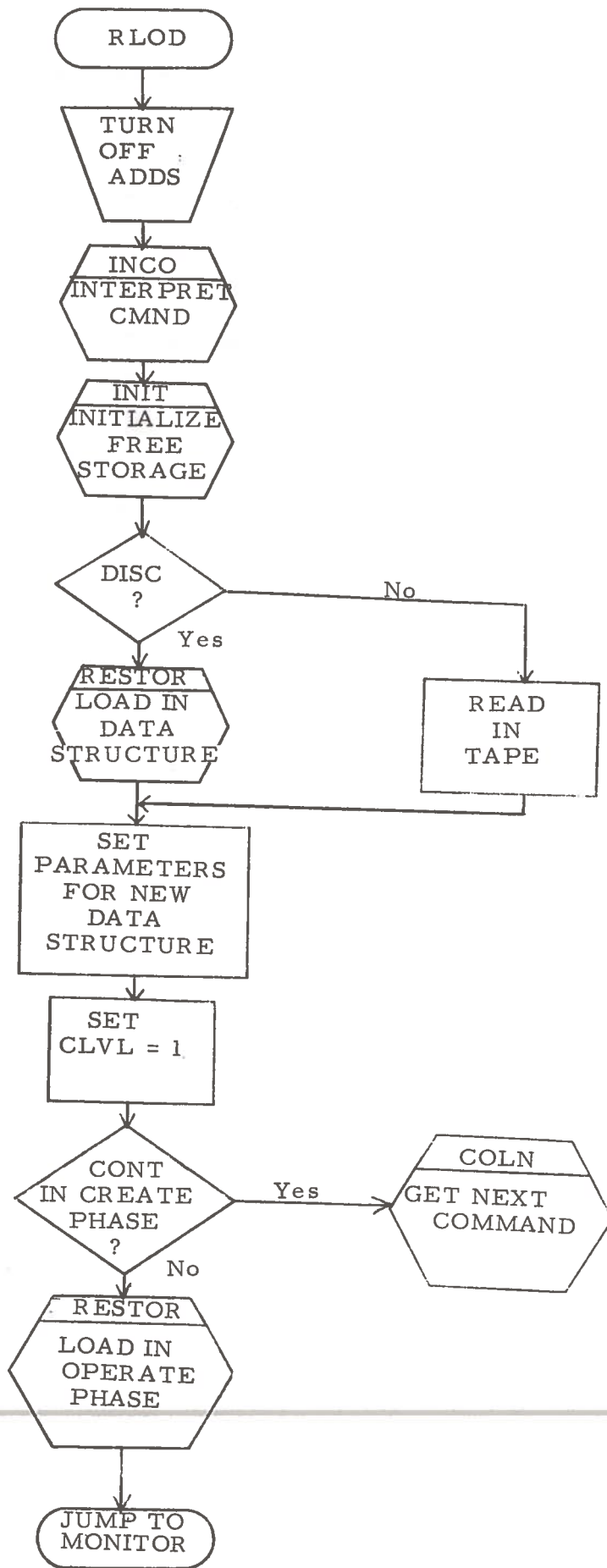
GETF

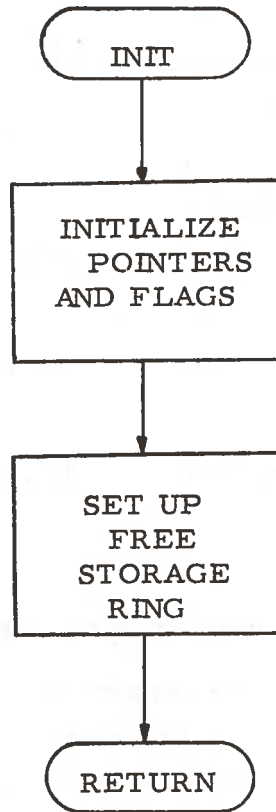
7. CORE USED:

413<sub>8</sub>









## SUBROUTINE

## SCAL

1. PURPOSE:

Insert commands to draw numeric labels (scales)  
for lines

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	SCAL

3. INPUT:

The following from teletype input buffer

- a)  $x_i$  x-component; Initial point relative
- b)  $y_i$  y-component; Initial point relative
- c)  $x_f$  x-component; Final point relative
- d)  $y_f$  y-component; Final point relative
- e) A Initial Value of Scale
- f) B Final Value of Scale
- g) N Number of labels
- h) SIZE Character size

4. OUTPUT:

Display commands are inserted into the current  
component block to draw and position scale labels.

5

ACTION:

Checks working level and inputs  $x_i, y_i, x_f, y_f, A, B, N,$  and  $SIZE$ . Returns after calling WHAT if early carriage return encountered. Calculates  $x, y$  position increments:

$$\Delta x = \frac{x_f - x_i}{N-1}, \quad \Delta y = \frac{y_f - y_i}{N-1}$$

and value increment:

$$\Delta S = \frac{B-A}{N-1}$$

Inserts size command and sets up spacing according to the following table:

<u>SIZE</u>	<u>SPACING</u> (raster units)
1	15
2	30
3	45
4	60

Calculates position of leftmost character relative to value center point and moves to that point. Set command to enter character mode, inserts characters, and returns to coordinate mode. Either the next scale value is then set up or a blank move to bring beam to  $(x_f, y_f)$  relative to original position is set up and control is returned to calling program.



6. EXTERNAL REFERENCES:

CMOD

GTNO

NONO

WHAT

COP

CIN

CHAD

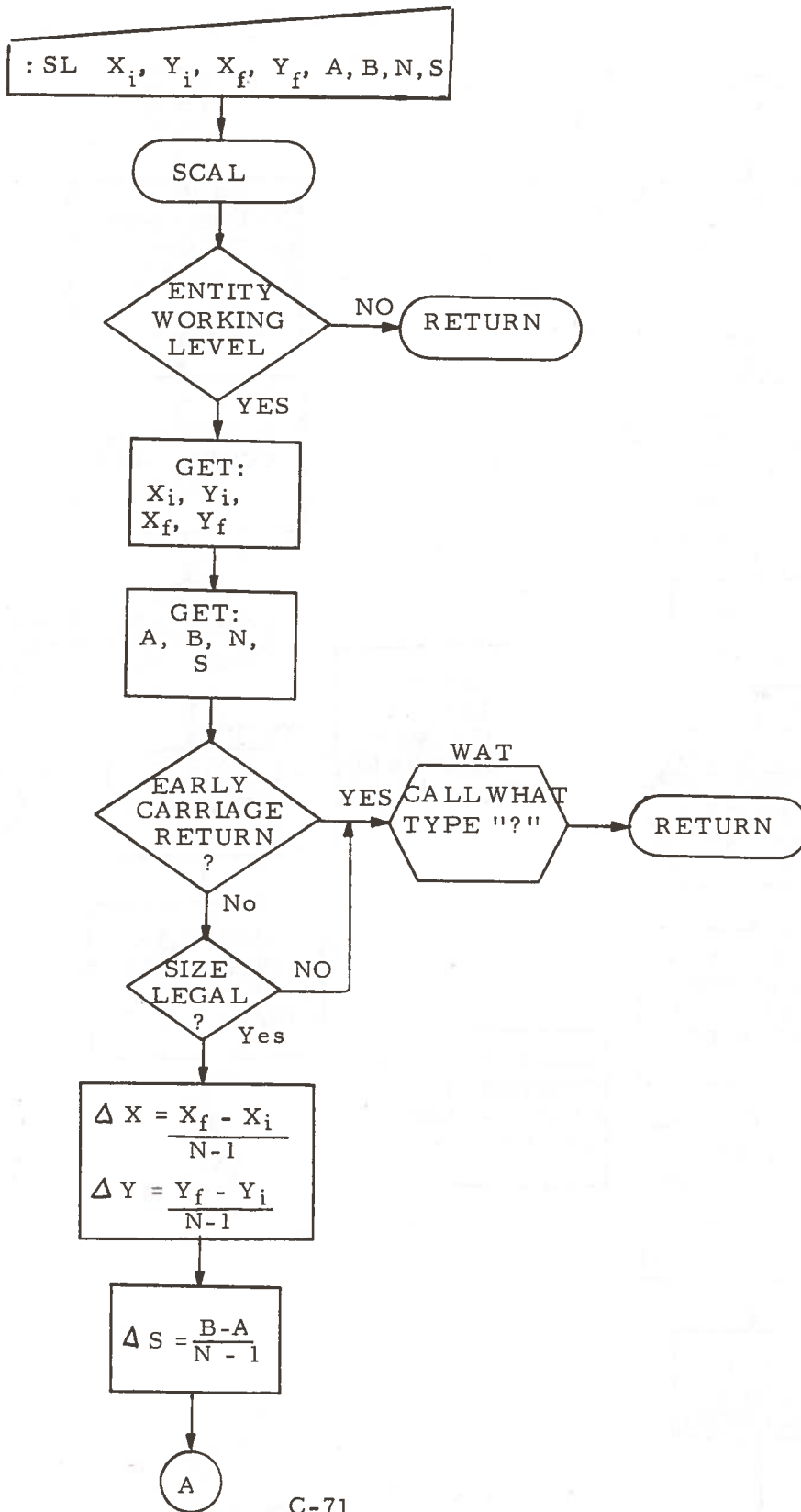
CLO

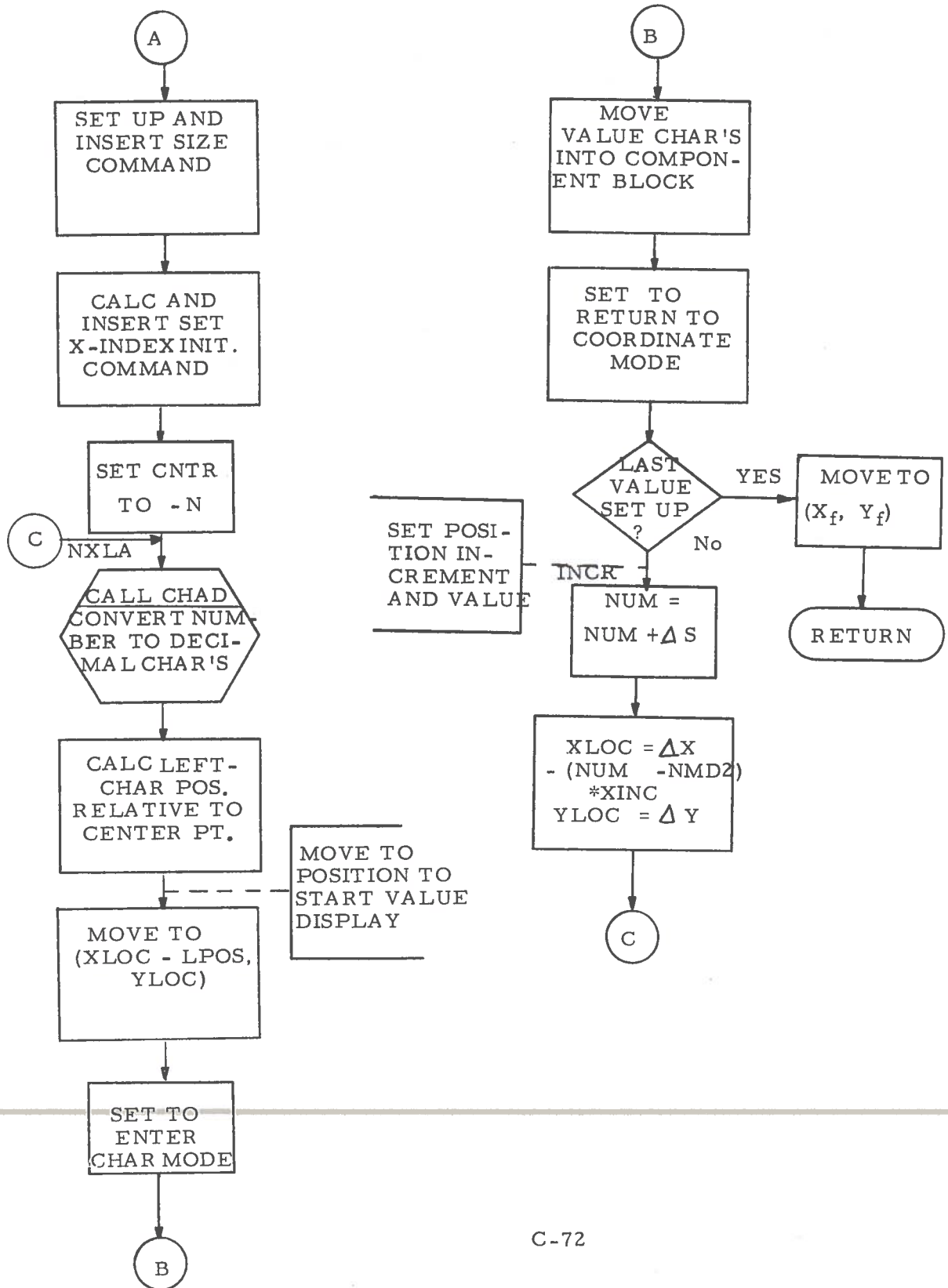
7. CORE USED:

257<sub>8</sub>

Scale for Lines

SCAL  
Flowchart





## SUBROUTINE

## SCAR

1. PURPOSE:

Set up commands to display scaled values for arcs

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	SCAR

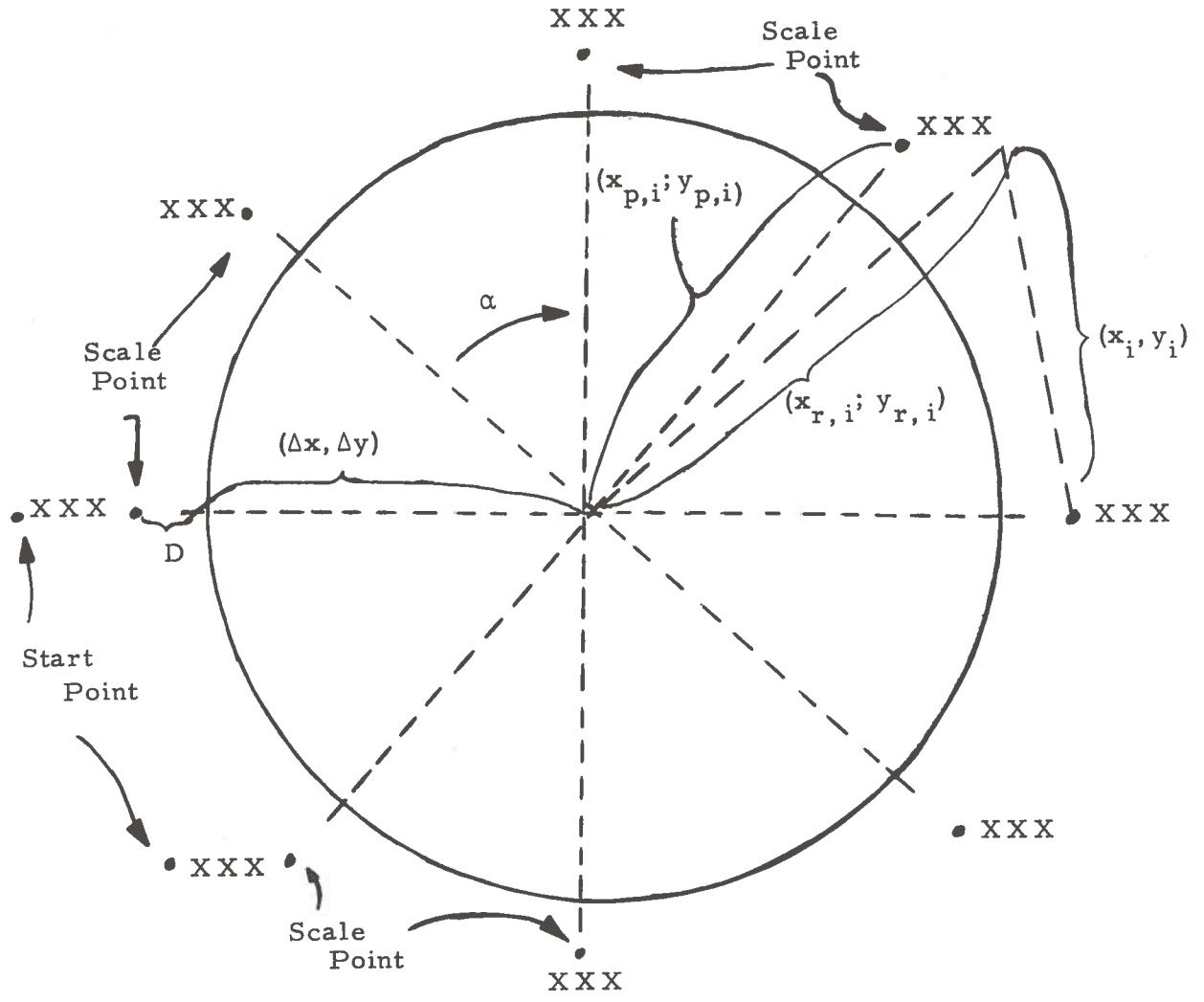
3. INPUT:

From the teletype buffer:

$\Delta x$	x-component, start position on arc
$\Delta y$	y-component, start position on arc
A	start value
B	end value
N	number of values
SIZE	character size; range = 1, 2, 3, 4
D	distance from arc to scale point along radius
$\theta$	angle of arc; (drawn clockwise for positive $\theta$ ; default gives full circle)

4. OUTPUT:

Display commands are inserted into component block.



Positioning of Scale Values on Arc

Figure 1.

5. ACTION:

Scale values are set up for display as follows

(see Figure 1):

- i) Calculate initial scale point  $(X_{p,i}, Y_{p,i})$
- ii) Apply offset for size and number of characters and for position along arc, to get start point  $(X_i, Y_i)$ .
- iii) Move to  $(X_i, Y_i)$  and display digits
- iv) Calculate return vector  $(X_{r,i}, Y_{r,i})$
- v) Calculate new scale point  $(X_{p,i}, Y_{p,i})$
- vi) Go to ii

When all scale values are set for display, a command to move back to the center of the arc using the current return vector is generated.

Terms used for calculations are:

NC = Number of char's for current scale value

CS = Horizontal character spacing in raster units

CSY = Vertical character spacing in raster units

$\alpha$  = Angle between scale points as fraction of circle  $*2^{15}$

$$\alpha = \frac{-0*2^{15}}{360(N-1)}, \text{ if } \theta \neq 0$$

$$\alpha = \frac{-2^{15}}{N}, \text{ if } \theta \text{ (input)} = 0 \quad (1)$$

In equation (1), the effect is to set  $\theta = 360$  and to eliminate repetition of scaling at the first scale point.

$$\Delta R = \text{length of radius} = \sqrt{\Delta x^2 + \Delta y^2}$$

$$R = \text{distance from center to scale point} = D + \Delta R$$

For  $i = 1$ ;

$$x_{p,i} = R \cdot \frac{\Delta x}{\Delta R} \quad ; \quad y_{p,i} = R \cdot \frac{\Delta y}{\Delta R}$$

For  $i = 2, \dots, N$

$$x_{p,i} = x_{p,i-1} \cos \alpha - y_{p,i-1} \sin \alpha$$

$$y_{p,i} = x_{p,i-1} \sin \alpha + y_{p,i-1} \cos \alpha$$

For  $i = 1, \dots, N$

$$x_i = x_{p,i} + (x_{p,i} - R) \left[ \frac{NC*CS}{2} - 2*SIZE \right] \frac{1}{R} + x_{r,i-1}$$

$$y_i = y_{p,i} + (y_{p,i} - R) \frac{CSY}{2} \frac{1}{R} + y_{r,i-1}$$

$$x_{r,i} = x_{r,i-1} - (x_i + NC*CS)$$

$$y_{r,i} = y_{r,i-1} - y_i$$

$$\text{Where } x_{r,0} = y_{r,0} = 0$$

Finally, the beam is returned to the center of the arc,  $(x_{r,n}, y_{r,n})$

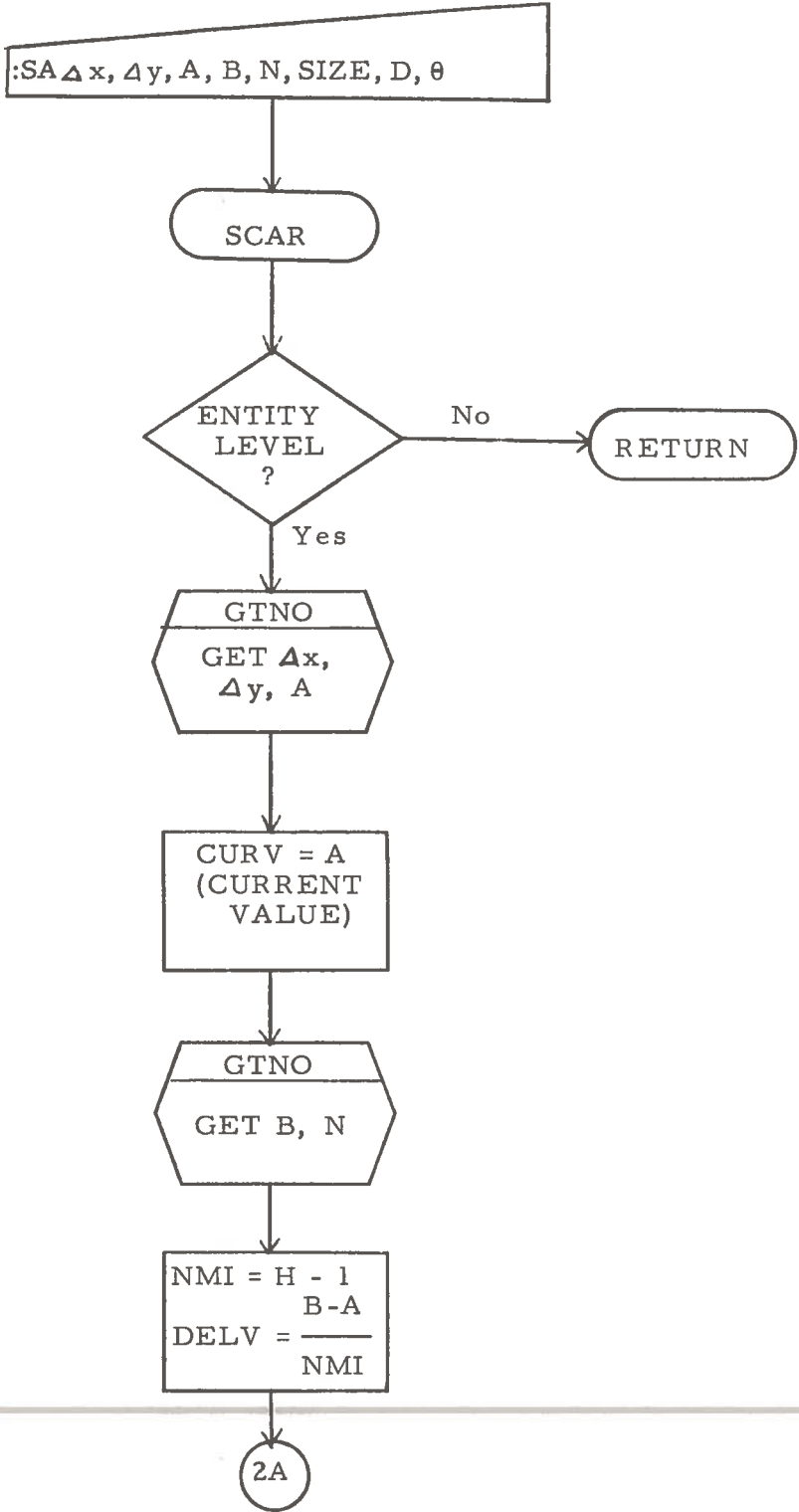
6. EXTERNAL REFERENCES

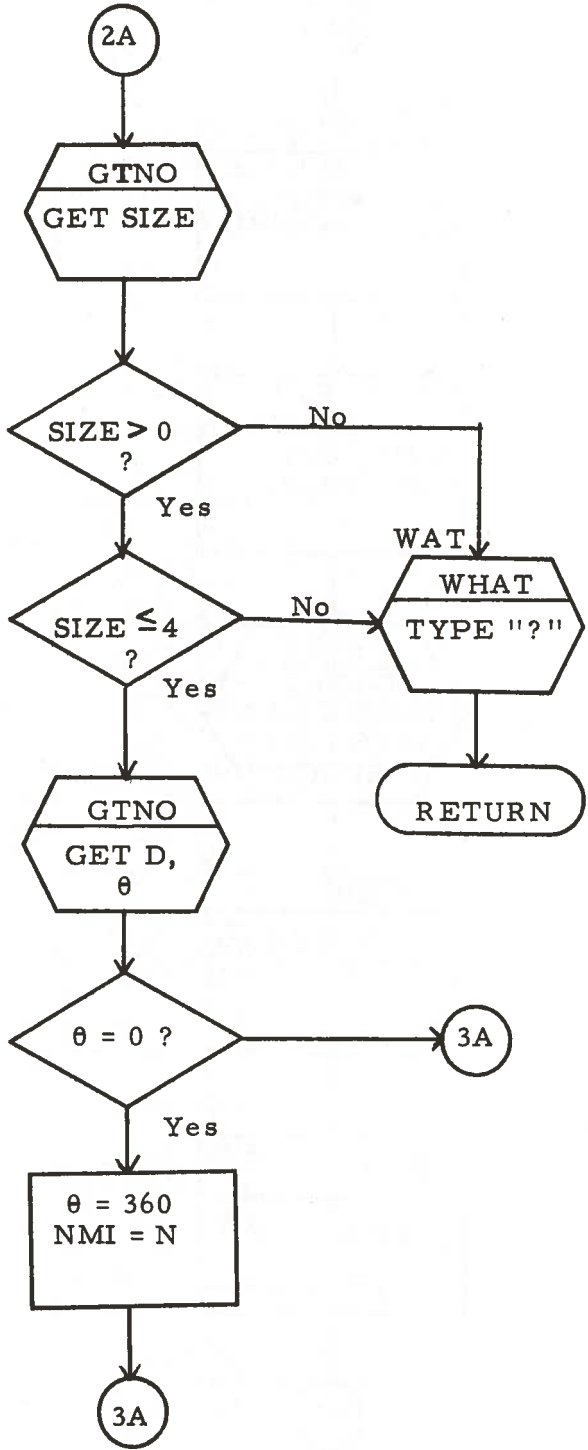
CMOD, GTNO, WHAT, SINX2, COSX2, COP, CIN,  
SQRJAM, CHAD, CLO

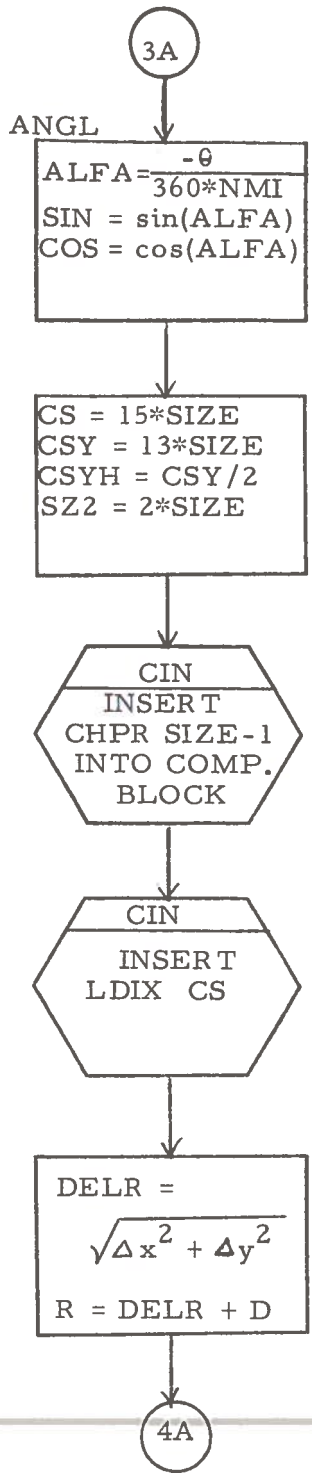
7. CORE USED:

400<sub>8</sub>

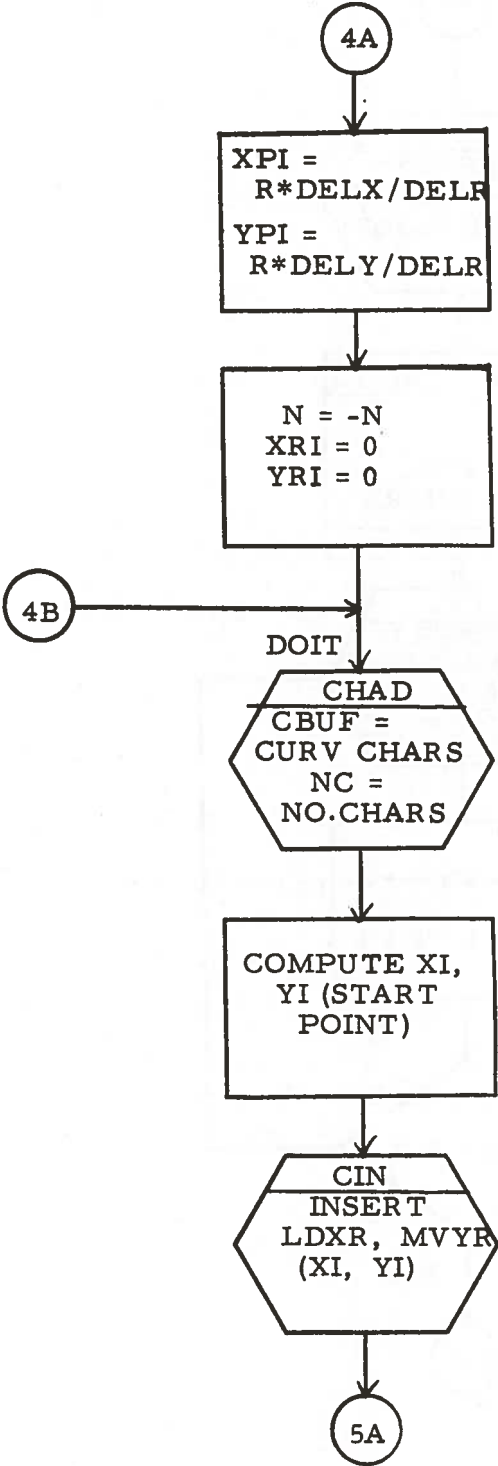


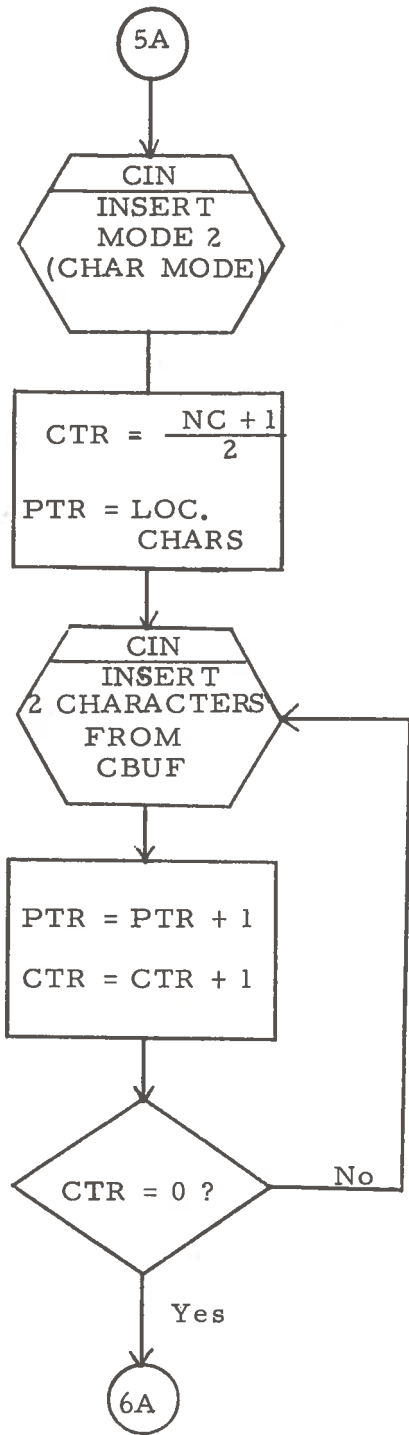


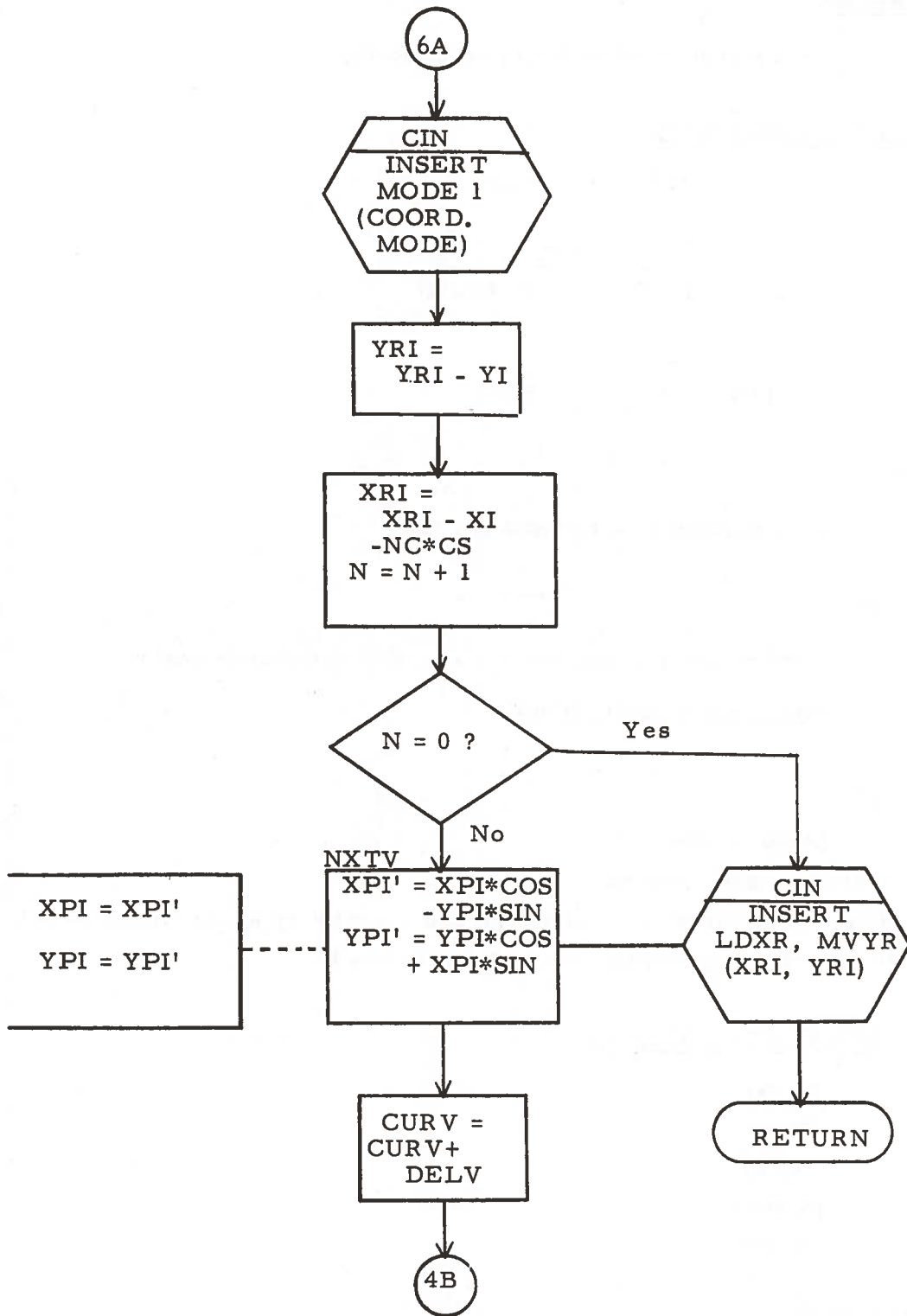




SCAR







## SUBROUTINE

## SETP

1. PURPOSE:

Set reference point for current entity

2. CALLING SEQUENCE:

	JST*	LOC
	⋮	
LOC	DAC	COML+N
	⋮	
COM+N	XAC	SETP

3. INPUT:

x, y positions from input buffer

4. OUTPUT:

Load x absolute and move y absolute commands inserted  
into current entity block.

5. ACTION:

Mode is checked.

If not entity mode, return.

If entity mode, input x, y positions and insert display command to set  
entity reference point into current entity block.

6. EXTERNAL REFERENCES:

GTNO -  
ENAD -  
CMOD -  
NONO -  
WHAT

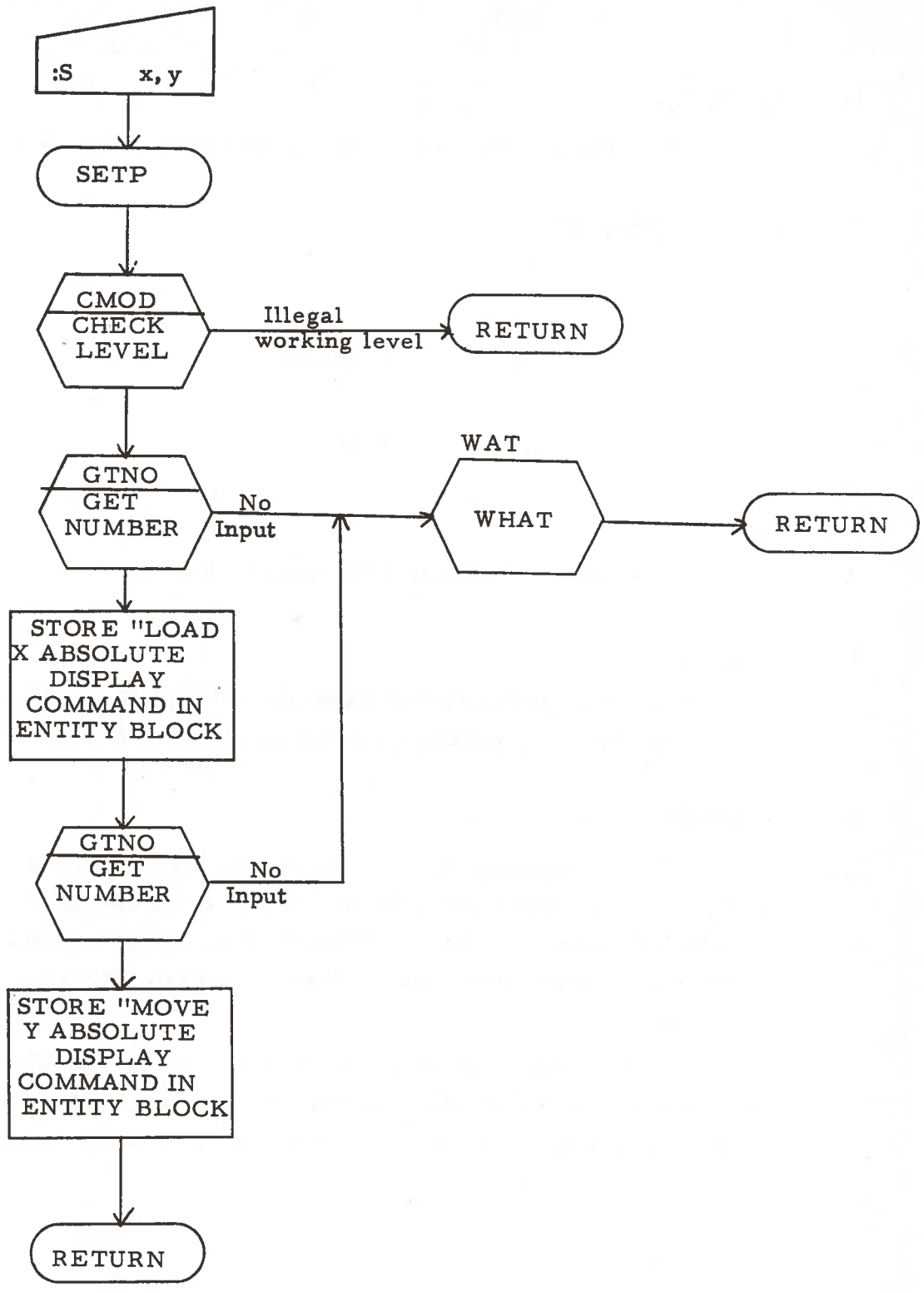
7. CORE USED:

51<sub>8</sub>

C-84

SET POINT

SETP





## SITI

1. PURPOSE:

Set initial texture and intensity for entity or indicator

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	SITI

3. INPUT:

Texture and Intensity from input buffer

4. OUTPUT:

Texture, Intensity command inserted into given entity block, or all entity blocks for given indicator.

5. ACTION:

Checks working level. Gets texture input. If no texture input, check for default and intensity input. If texture input is present, then gets intensity. If intensity input is first, then attempts to get texture input again. If still no input, check for texture default.

After texture and intensity are set, they are combined into a display command, and inserted into an entity block, or if in indicator mode, inserted into each entity block on the indicated ring.

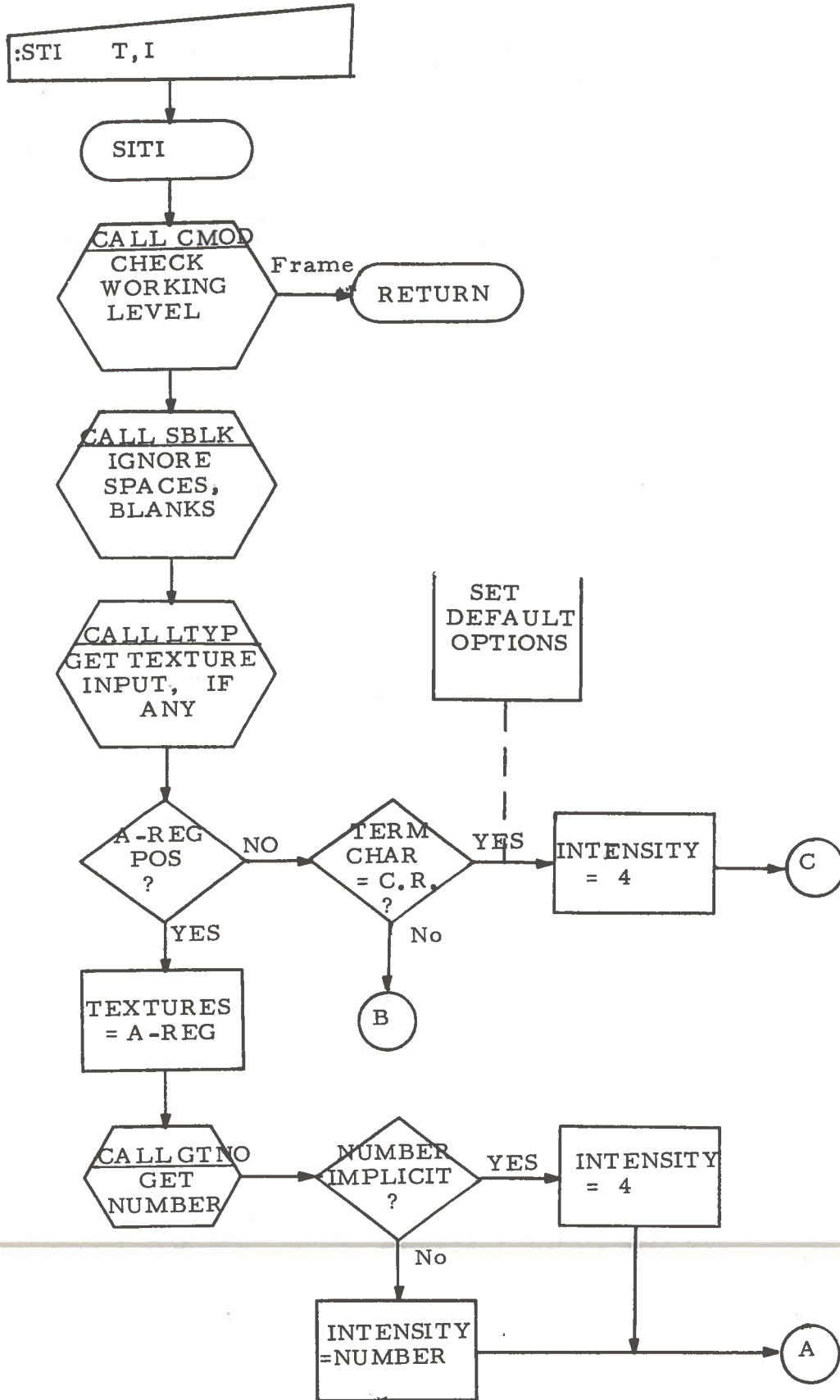
6. EXTERNAL REFERENCES:

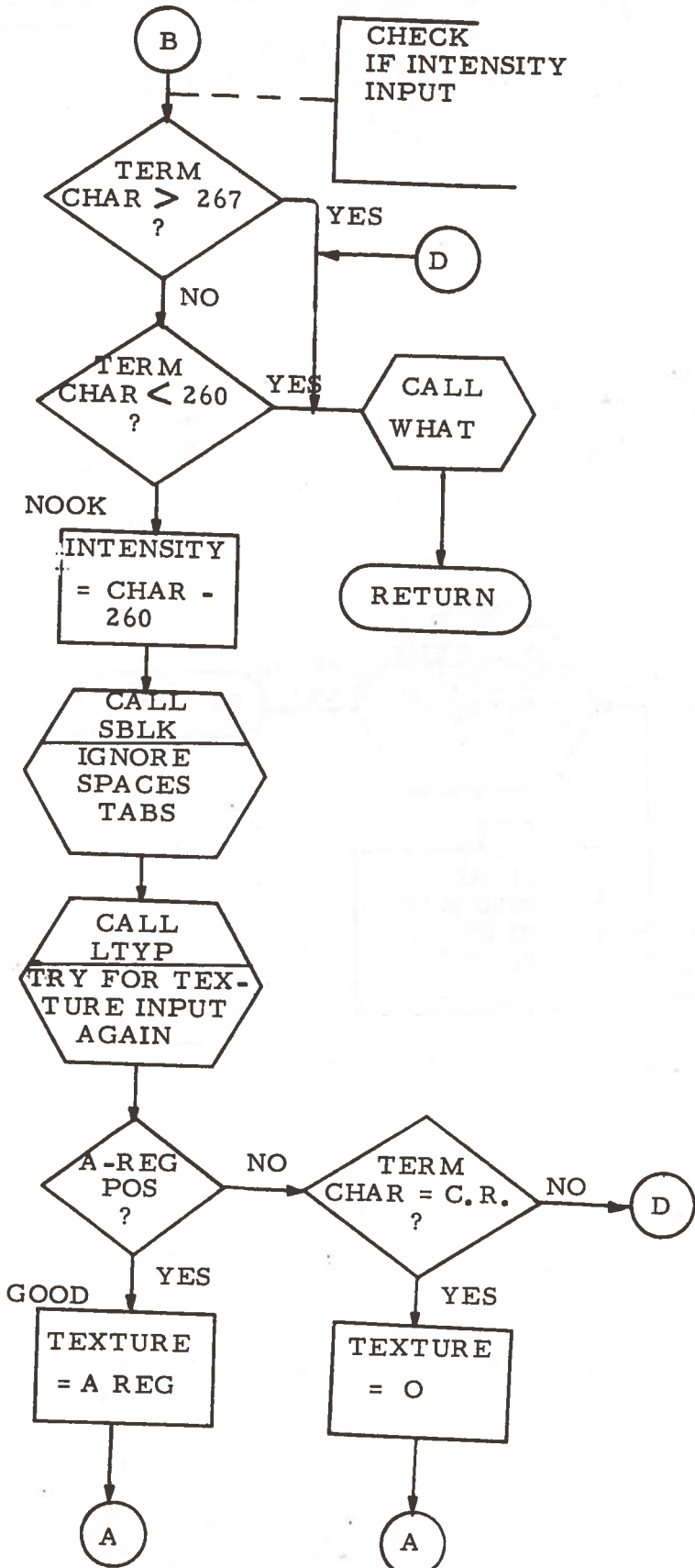
CMOD  
SBLK  
LTYP  
GTNO  
NONO  
WHAT  
CLVL  
GENT  
ENAD

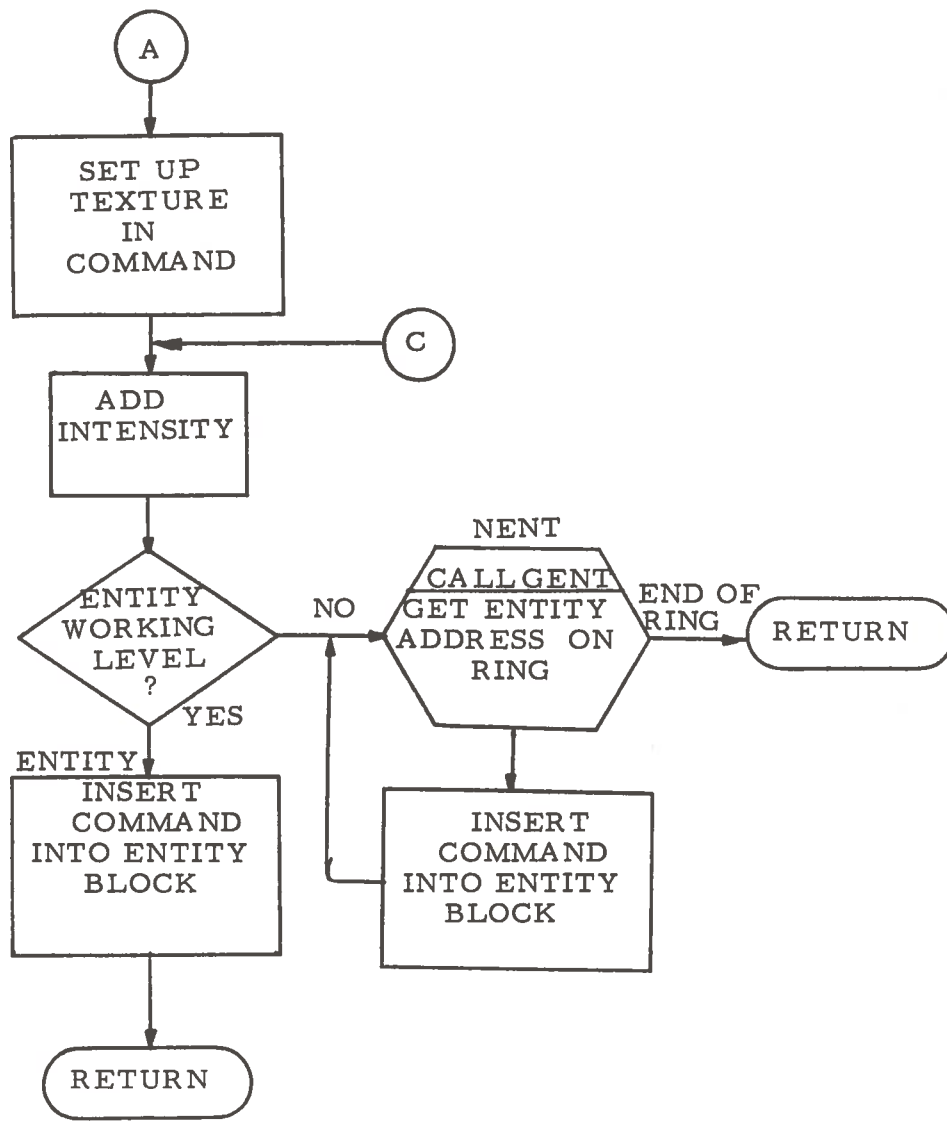
7. CORE USED:

143<sub>8</sub>

Set Initial Texture, Intensity







## SKIP

1. PURPOSE:

Process skip instructions

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	SUBR

where:

SUBR = SKIP, EQAL, GRET, GREQ, LESS, LEQU

3. INPUT:

Register numbers and skip numbers from input buffer

4. OUTPUT:

Skip instruction in conditioning block

5. ACTION:

a) SKIP - checks working level

Gets skip number, checks if greater than 0. Combined with SKIP code and inserted into conditioning block.

b) EQAL - Skip if  $A = B$

c) GRET - Skip if  $A > B$

d) GREQ - Skip if  $A \geq B$

e) LESS - Skip if  $A < B$

f) LEQU - Skip if  $A \leq B$

b-f) Check working level, load appropriate skip code, CALL GABN.

6. EXTERNAL REFERENCES:

CMOD

GTNO

WHAT

INCB

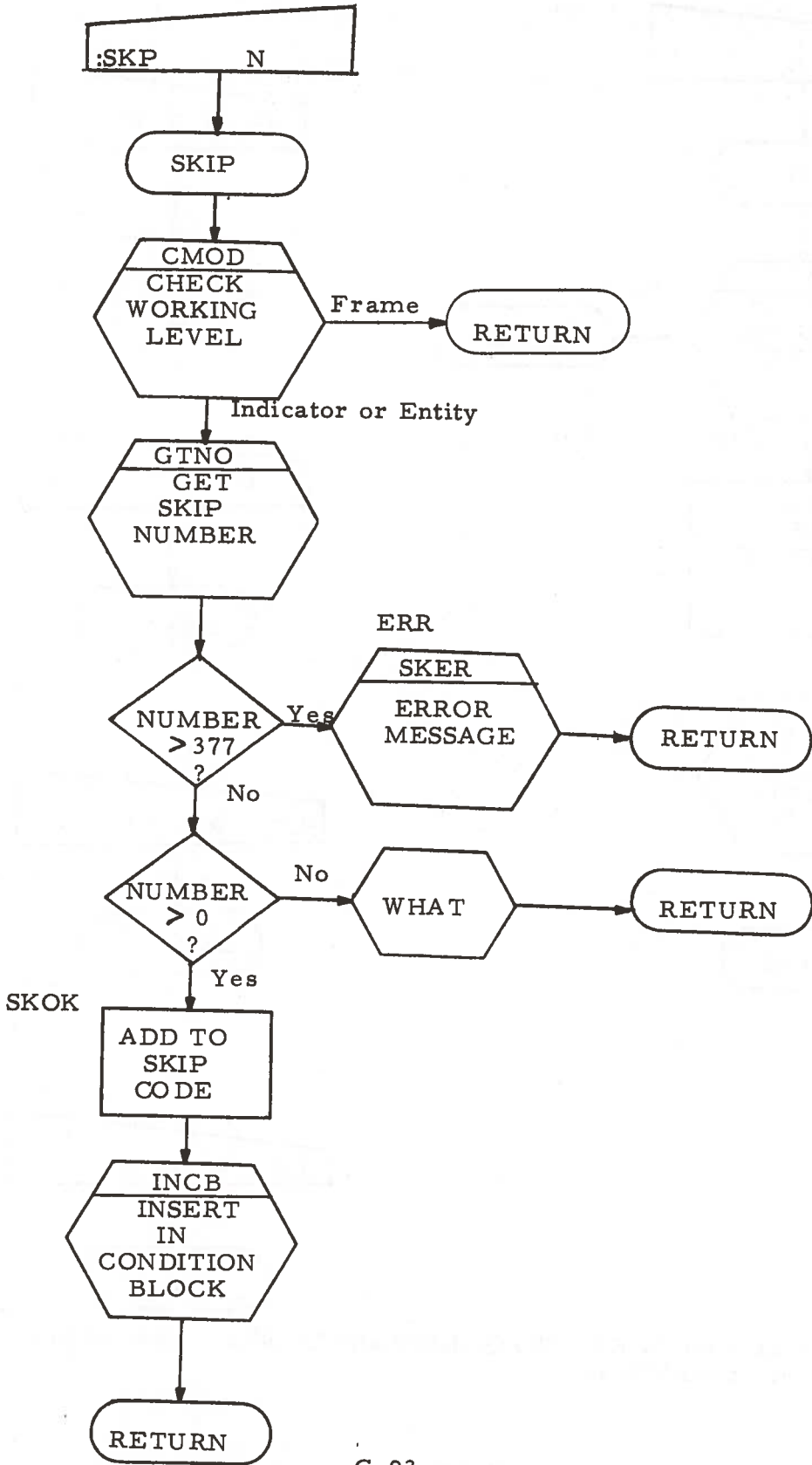
GABN

SKER

7. CORE USED:

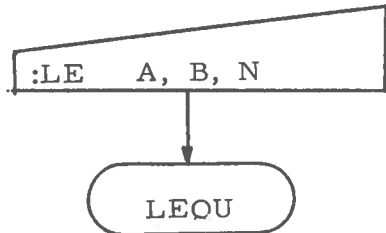
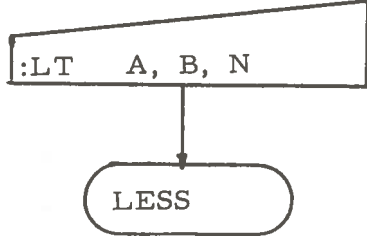
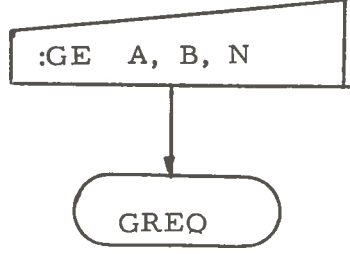
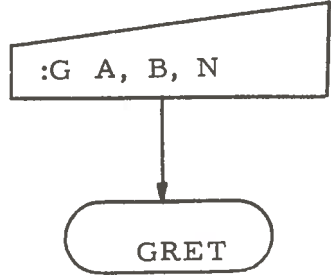
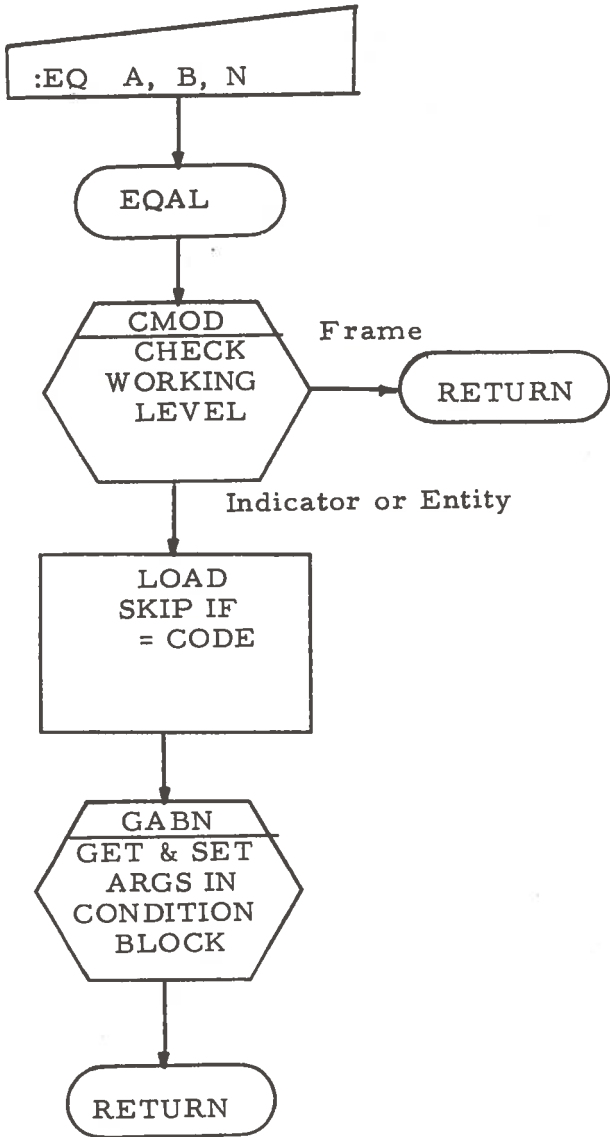
76<sub>8</sub>

SKIP





SKIP



Note: Flowcharts for GRET, GREQ, LESS and LEQU are similar to the Flowcharts for EQAL.

## SUBROUTINE

## TICK

1. PURPOSE:

Insert component display commands to draw tick marks as indicated by input.

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	TICK

3. INPUT:

Arguments for tick mark specifications from input buffer

4. OUTPUT:

Display Commands to draw tick marks inserted in component buffer

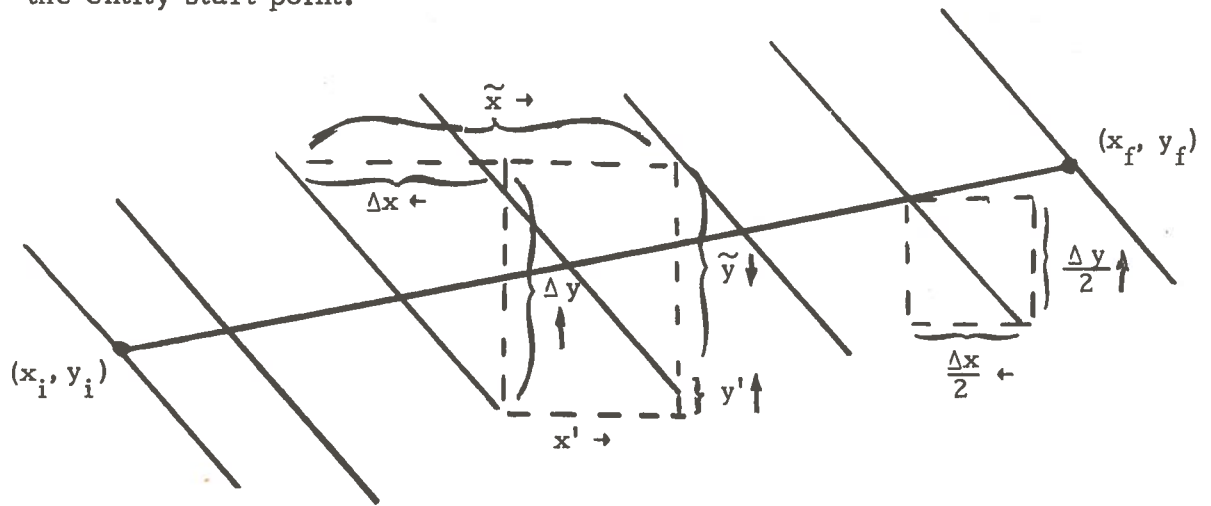
5. ACTION:

Retrieves number of ticks,  $N$ , initial point  $(x_i, y_i)$  and final point  $(x_f, y_f)$ . If early termination of line, error return is made.

$\Delta x, \Delta y$  are retrieved. If neither are present, tick marks are automatically of length 12 rasters and are perpendicular to the line connecting  $(x_i, y_i)$  and  $(x_f, y_f)$ . If only  $\Delta x$  is present, it is assumed to be the magnitude of the tick mark length, and again tick marks are perpendicular to the line connecting  $(x_i, y_i)$  and  $(x_f, y_f)$ . If both  $\Delta x, \Delta y$  are present, then they are assumed to be both magnitude and direction of the  $x, y$  components of the tick, respectively.

The parameters described in the following figure are calculated and the appropriate display commands are inserted into the component block. These commands leave the final position of the beam

at  $(x_f, y_f)$ . NOTE:  $(x_i, y_i)$  and  $(x_f, y_f)$  are assumed to be relative to the entity start point.



(Arrows indicate direction of quantity.)

Where:

N given

$$\Delta x = \frac{\text{magn.} * (y_i - y_f)}{\sqrt{(x_f - x_i)^2 + (y_f - y_i)^2}} \quad \text{or given}$$

$$\Delta y = \frac{\text{magn.} * (x_f - x_i)}{\sqrt{(x_f - x_i)^2 + (y_f - y_i)^2}} \quad \text{or given}$$

$$x' = \frac{x_f - x_i}{N-1}$$

$$y' = \frac{y_f - y_i}{N-1}$$

$$\tilde{x} = x' - \Delta x$$

$$\tilde{y} = y' - \Delta y$$

6. EXTERNAL REFERENCES:

CMOD  
GTNO  
NONO  
SQRJAM  
COP  
CIN  
CLO  
CPAD

7. CORE USED:

254<sub>8</sub>

:TML N, X<sub>i</sub>, Y<sub>i</sub>, X<sub>f</sub>, Y<sub>f</sub>, Δx, Δy

TICK

ENTITY WORKING LEVEL ?

No  
RETURN

GET & SAVE  
N, X<sub>i</sub>, Y<sub>i</sub>,  
X<sub>f</sub>, Y<sub>f</sub>

EARLY LINE TERMINATION ?

Yes  
WHAT TYPE QUESTION MARK  
RETURN

GTNO  
GET  
Δx

TERM CHAR = G.R. ?

No  
GETY  
GTNO  
GET Δy  
Δy IMPLICIT ?  
No  
A

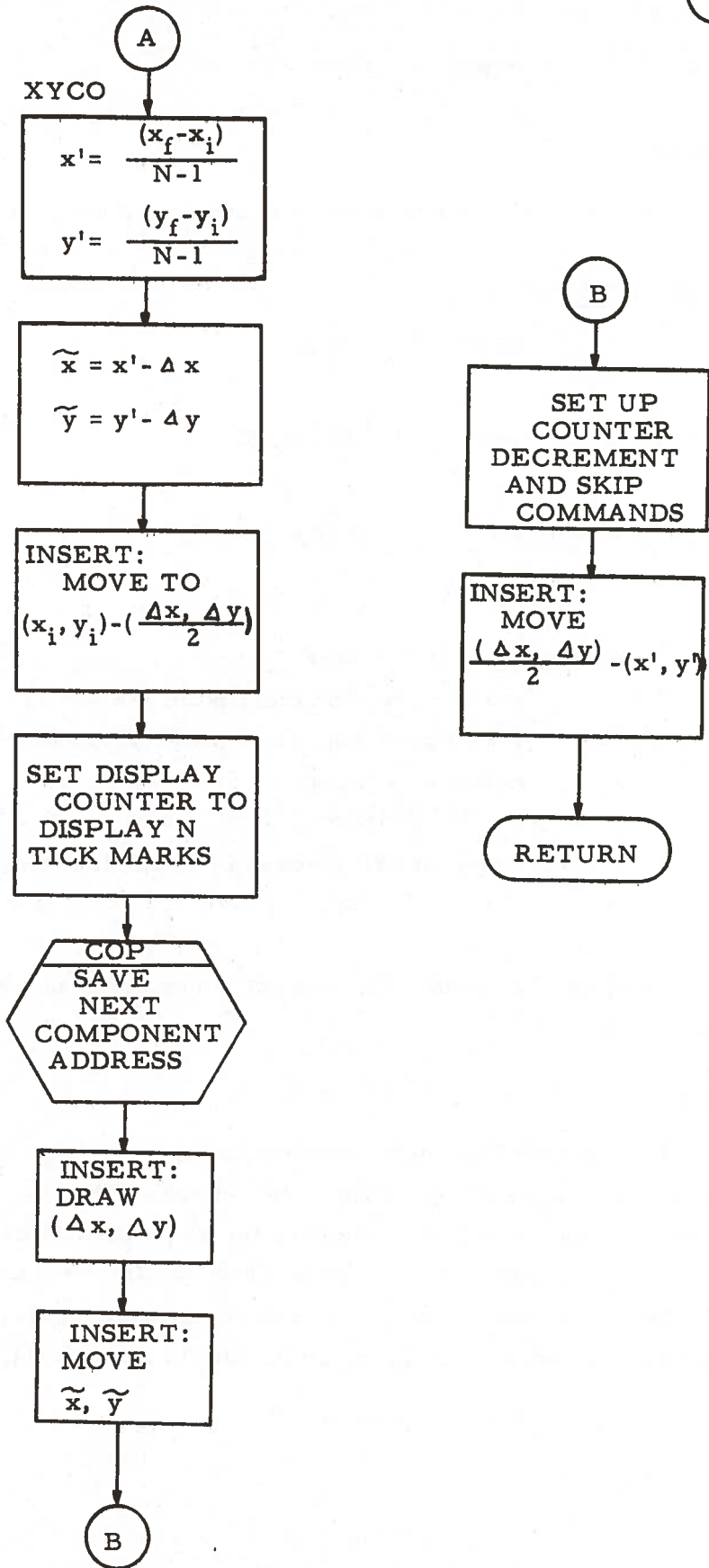
Yes  
XMAG Δx = 0 ?  
Yes  
Δx = 12

$$\Delta y = \frac{\Delta x * (x_f - x_i)}{\sqrt{(x_f - x_i)^2 + (y_f - y_i)^2}}$$

$$\Delta x = \frac{\Delta x * (y_i - y_f)}{\sqrt{(x_f - x_i)^2 + (y_f - y_i)^2}}$$

A

TICK



SUBROUTINE                   TIKA

1.    PURPOSE:

Set up display commands to draw tick marks for arcs

2.    CALLING SEQUENCE:

```
                          JST*       PTR
                          :
                          :
PTR           PAC       COML+N
                          :
                          :
COML+N XAC       TIKA
```

3.    INPUT:

From teletype input buffer:

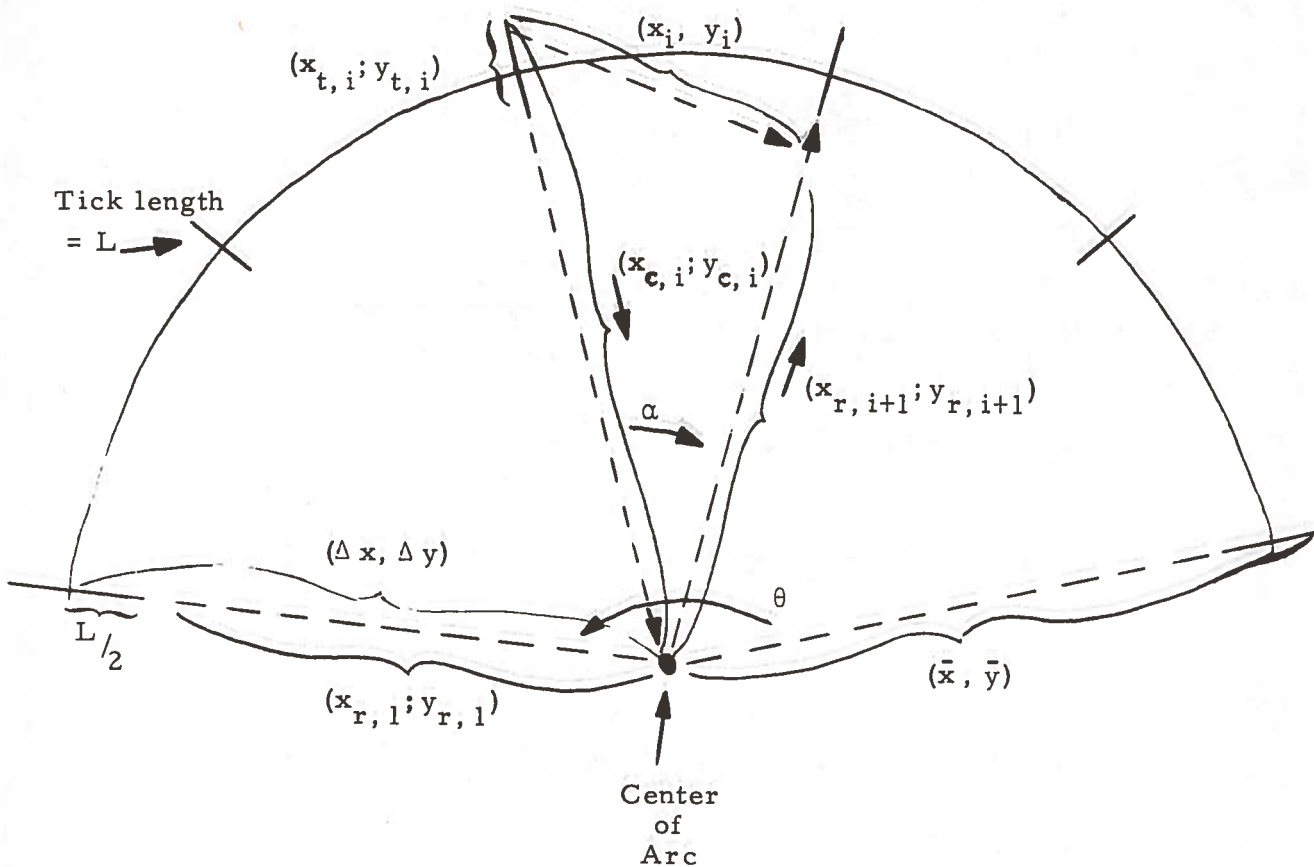
$\Delta x$        x-component of start point (relative)  
 $\Delta y$        y-component of start point (relative)  
N            number of ticks  
L            length of ticks  
 $\theta$         Angle of arc (between 1st and last ticks)

4.    OUTPUT:

Display commands inserted into component block for current entity

5.    ACTION:

Determines the angle between ticks,  $\alpha$ ; and the length of a radial vector,  $(x_r, y_r)$ , drawing of tick mark. The tick mark is drawn perpendicular to and straddling the arc with radius  $|\Delta x, \Delta y|$ . The vector  $(x_i, y_i)$  used to move to position for the next tick mark is the resultant of a vector,  $(x_c, y_c)$ , returning to the center of the arc followed by the radial vector,  $(x_r, y_r)$ , for the next tick (see figure below).



Construction of Tick Marks

Figure 1.

The following formula are used:

$$\alpha = \frac{-\theta}{360(n-1)} ; \quad \alpha \text{ is in fractions of a circle}$$

$\theta$  is in degrees

$$x_{r,1} = \Delta x - \frac{L}{2} * \frac{\Delta x}{D_0}$$

$$y_{r,1} = \Delta y - \frac{L}{2} * \frac{\Delta y}{D_0}$$

where:  $D_0 = |(\Delta x, \Delta y)| = \sqrt{\Delta x^2 + \Delta y^2}$



$$x_{t,i} = L * \frac{x_{r,i}}{DI}$$

$$y_{t,i} = L * \frac{y_{r,i}}{DI}$$

where:  $DI = |(x_{r,1}; y_{r,1})| = \sqrt{x_{r,1}^2 + y_{r,1}^2}$

$$x_{c,i} = -(x_{r,i} + x_{t,i})$$

$$y_{c,i} = -(y_{r,i} + y_{t,i})$$

$$x_{r,i+1} = x_{r,i} \cos \alpha - y_{r,i} \sin \alpha$$

$$y_{r,i+1} = x_{r,i} \sin \alpha + y_{r,i} \cos \alpha$$

$$x_i = x_{e,i} + x_{r,i+1}$$

$$y_i = y_{c,i} + y_{r,i+1}$$

The return vector  $(\bar{x}, \bar{y})$  to return to the center of the arc is defined by:

$$\bar{x} = -(x_{r,1} + \sum_{i=1}^n x_{t,i} + \sum_{i=1}^n x_i)$$

$$\bar{y} = -(y_{r,1} + \sum_{i=1}^n y_{t,i} + \sum_{i=1}^{n-1} y_i)$$

6. EXTERNAL REFERENCES:

CMOD

GTNO

COP

CIN

CLO

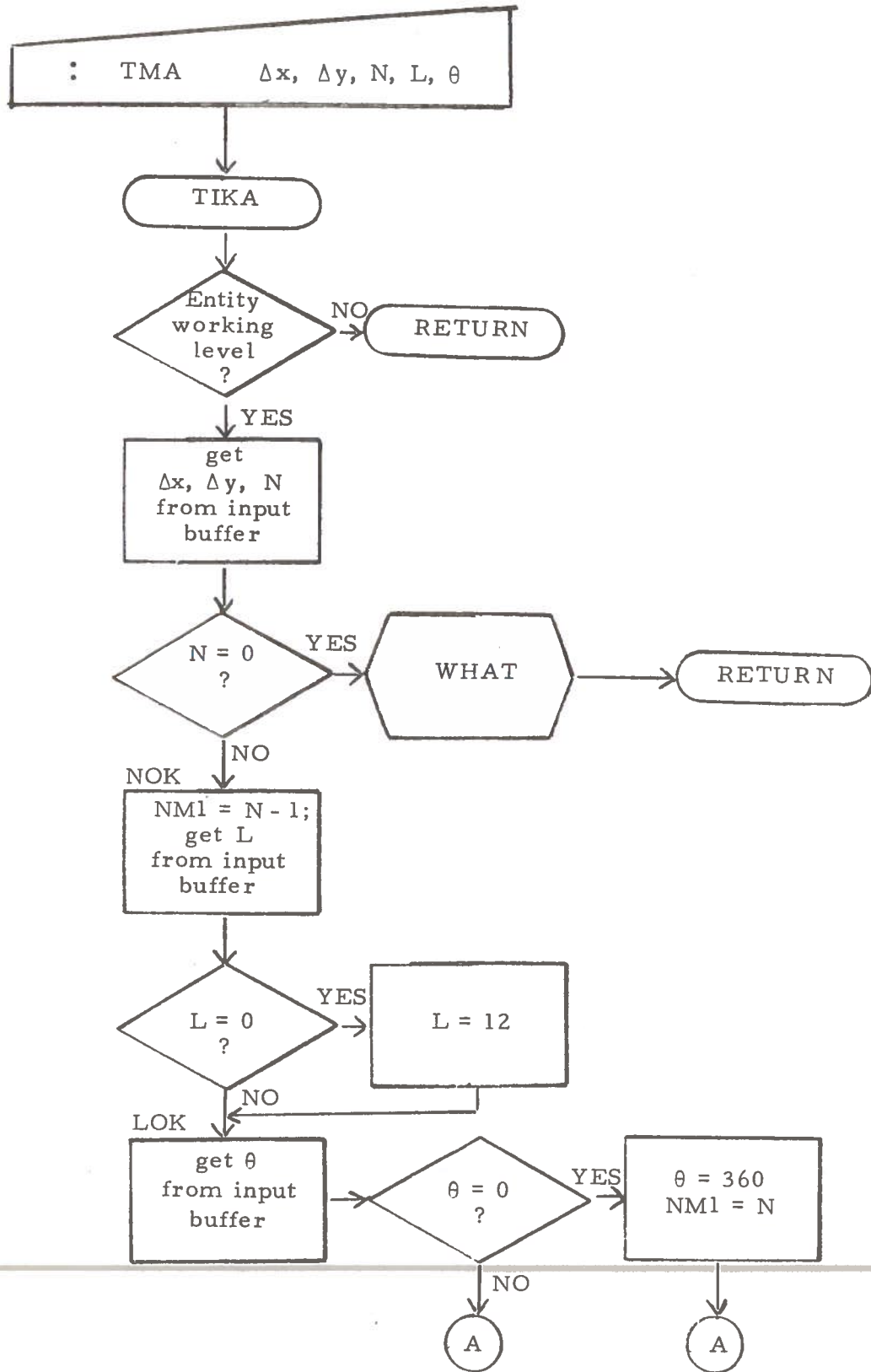
6. EXTERNAL REFERENCES (cont.):

SINX2  
COSX2  
SQRJAM

7. CORE USED:

356<sub>8</sub>

Tick Marks for Arcs



SUBROUTINE

TRLT

1. PURPOSE:

Translate indicators or set up dynamic expression for entity translation.

2. CALLING SEQUENCE:

	JST*	PTR
	⋮	
PTR	DAC	COML+N
	⋮	
COML+N	XAC	TRLT

3. INPUT:

$\Delta x, \Delta y$  from input buffers.

If in indicator mode,  $\Delta x, \Delta y$  assumed to be numbers.

If in entity mode,  $\Delta x, \Delta y$  assumed to be dynamic expressions.

4. OUTPUT:

For indicators, all entities in entity ring are translated  $\Delta x, \Delta y$  relative to current set points by resetting the set points.

For entities, a dynamic expression is set in the current entities conditioning block.

5. ACTION:

In indicator mode, retrieves  $\Delta x, \Delta y$ . Cycle thru the entity ring, resetting the entity set points.

In entity mode, moves dynamic expressions into conditioning block. At end of moving an expression, check to insure that a null character has been moved into conditioning block. The null character / word is used as a delimiter for the expressions. The code set in the conditioning block is in the following format:

1	000 000 1		ignored
0	7-bit ASCII char.	X	7-bit ASCII char.
0	:	X	:
0	7-bit ASCII char.		7-bit ASCII char.

$\Delta x$   
 and/or  
 $\Delta y$   
 expressions

If no  $\Delta x$ -expression, first word is the null word. A null word or char. will separate the  $\Delta x$ - and  $\Delta y$ -expressions. If no  $\Delta y$ -expression, a null word will be packed if  $\Delta x$ -expression has an odd number of characters. The  $\Delta x$ -expression will then be followed by a null character and a null word, then the next conditioning command.

E. g. ,

0	7-bit ASCII char.	X	7-bit ASCII char.
0	0 0 0 0 0 0 0	X	0 0 0 0 0 0 0
0	0 0 0 0 0 0 0	X	0 0 0 0 0 0 0
1	command		

←  $\Delta x$ -expr. with even number of char.  
 ← null to terminate  $\Delta x$ -expr.  
 ← null for  $\Delta y$ -expr., here none

0	7-bit ASCII character	X	0 0 0 0 0 0 0
0	0 0 0 0 0 0 0	X	0 0 0 0 0 0 0
1	command	X	

←  $\Delta x$ -expr. with odd number of char.  
 ← Null for  $\Delta y$ -expr., none

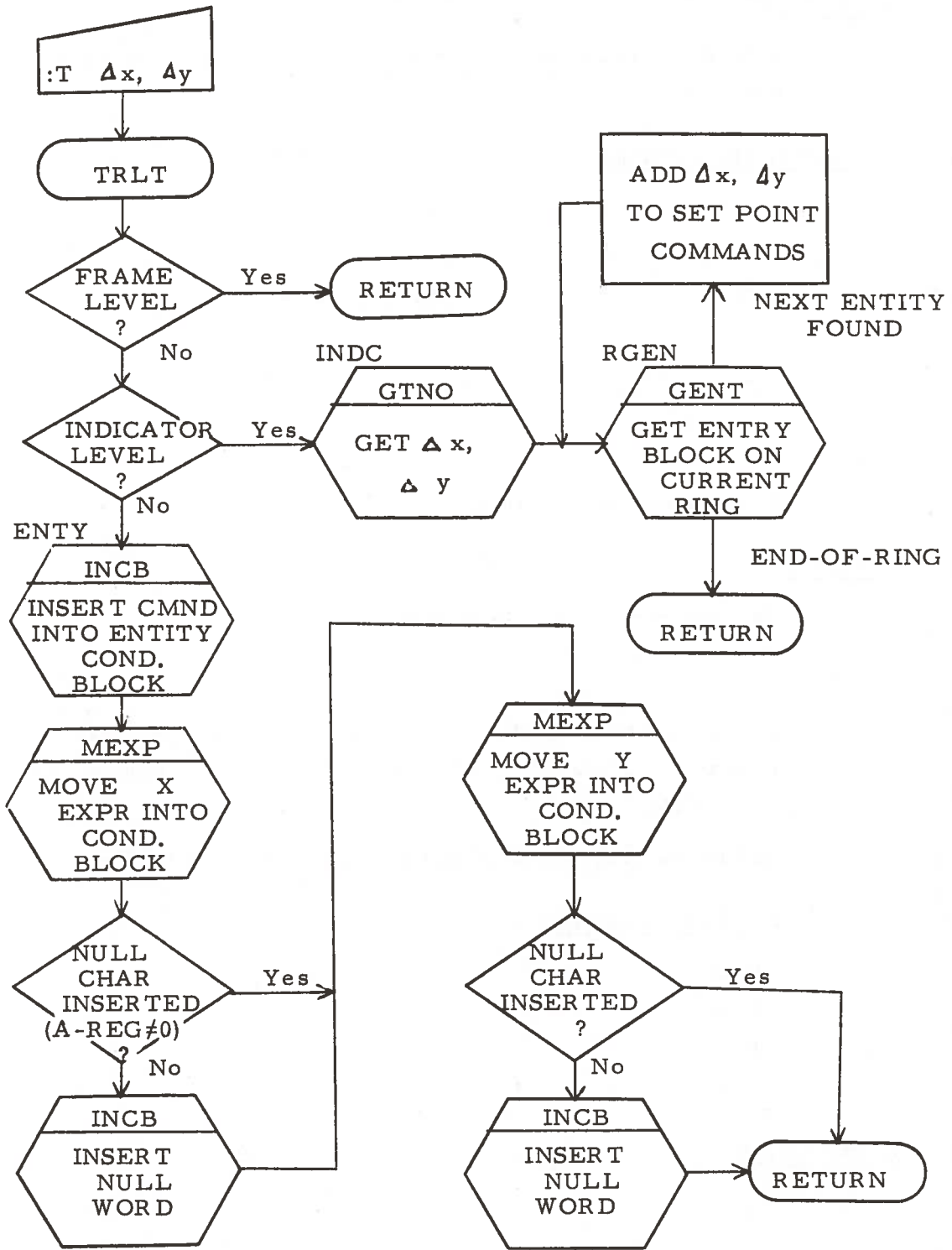
6. EXTERNAL REFERENCES:

CLVL

7. CORE USED:

72<sub>8</sub>

TRLT



SUBROUTINE TXTR

1. PURPOSE:

Interpret argument for line type (texture) specification and insert into conditioning block.

2. CALLING SEQUENCE:

	JST*	LOC
	⋮	
LOC	DAC	COML+N
	⋮	
COML+N	XAC	TXTR

3. INPUT:

Char. from input buffer.

4. OUTPUT:

Texture command in conditioning block.

5. ACTION:

Checks working level. Get code for line type specified. If illegal character indicated, set default code if illegal char. = C. R., otherwise call WHAT.

Add to texture command and set in conditioning block.

6. EXTERNAL REFERENCES:

CMOD  
LTYP  
WHAT  
INCB

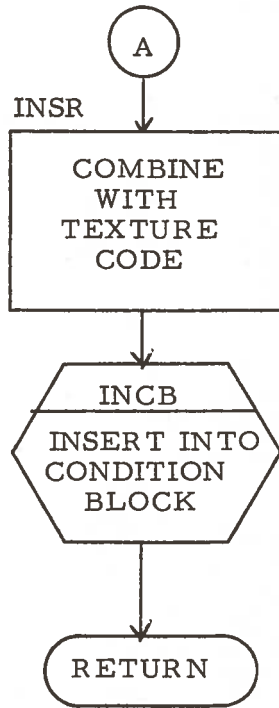
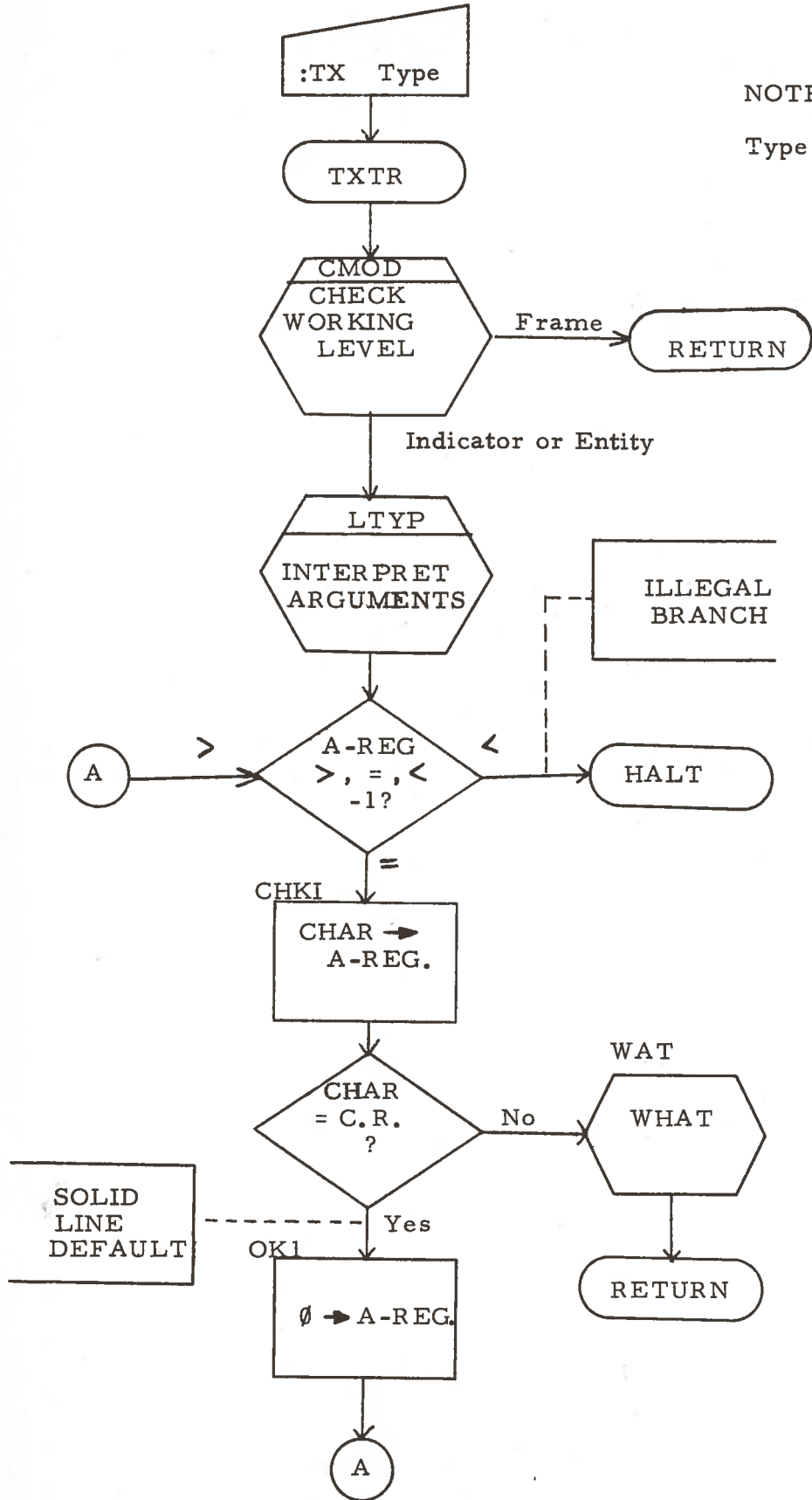
7. CORE USED:

27<sub>8</sub>

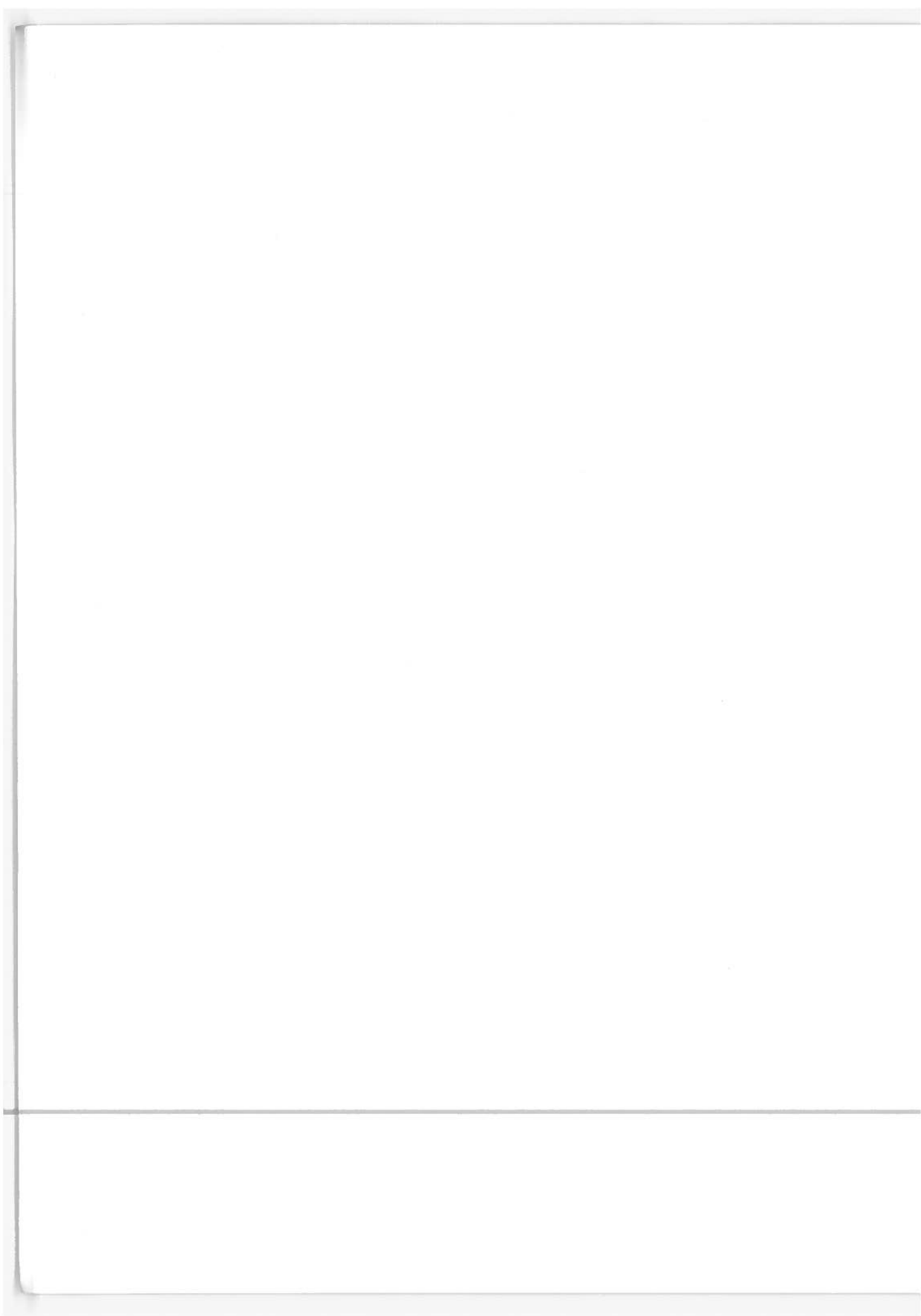
TXTR

NOTE:

Type = Solid  
Dot  
Dash  
DD (dot, dash)







## GREG

1. PURPOSE:

Retrieve and check register number from input buffer.

2. CALLING SEQUENCE:

CALL GREG  
ERROR RETURN  
NORMAL RETURN

3. INPUT:

Character string in input buffer

4. OUTPUT:

Returns register number in A-Reg.

5. ACTION:

Sets to skip blanks and tabs, retrieves one char. If char = R, continue; otherwise return.

Get number from input buffer. Check reg. number for range 0-99. Return reg. number in A-Reg.

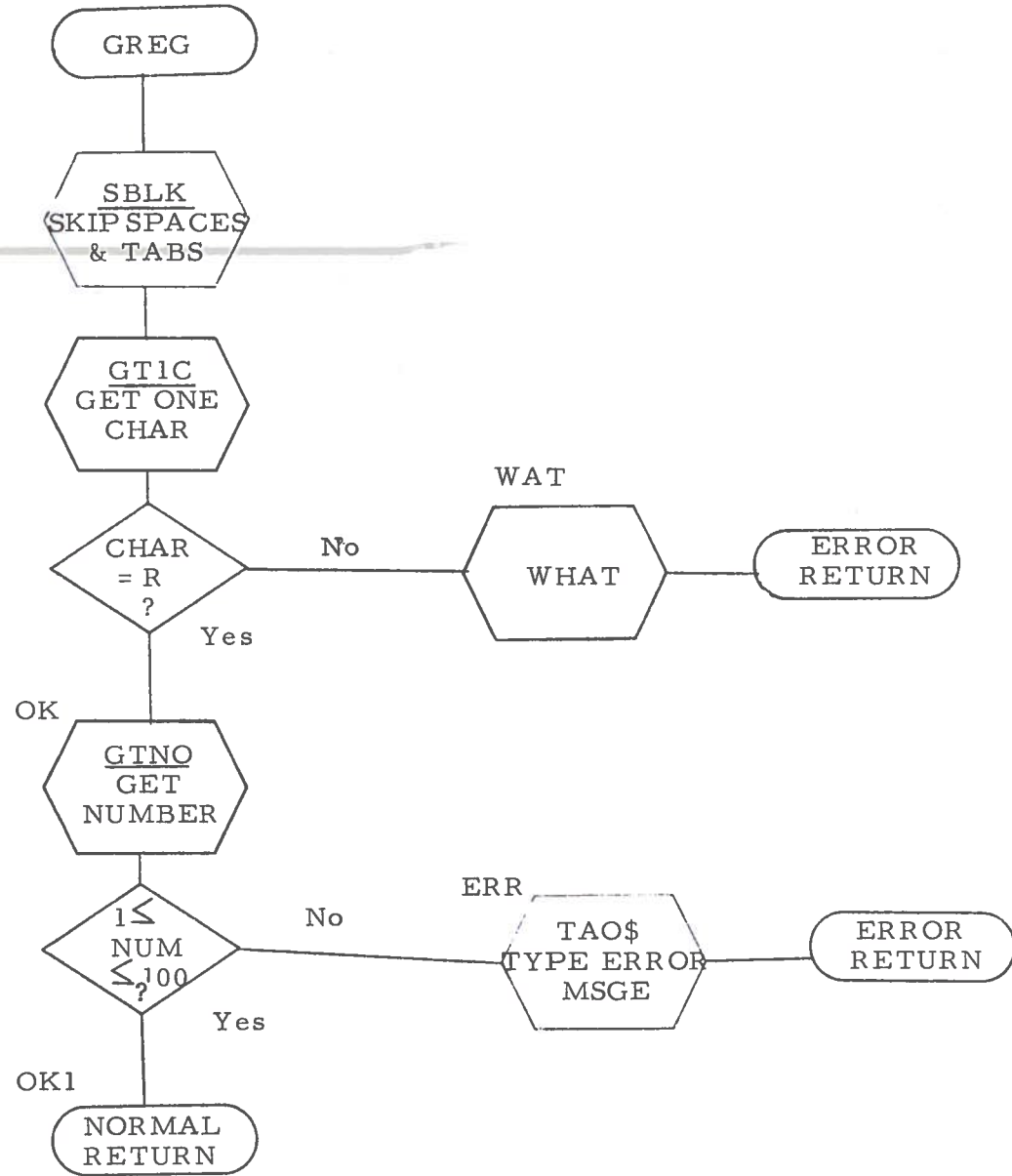
6. EXTERNAL REFERENCES:

None

7. CORE USED:

46<sub>8</sub>

GREG  
FLOWCHART



SUBROUTINE: GTNO, NONO

- a) Get Number
- b) Returns 1 if last number returned was explicit;  
otherwise 0

1. PURPOSE:

- a) Retrieves characters from buffer specifying a decimal number, converts, and outputs equivalent octal number in two's complement notation. Non-numeric characters, spaces, and tabs preceding the number in the input buffer are ignored on option. If no number is found, a zero is returned. Zero also returned on overflow.
- b) NONO =  $\emptyset$  if no number found, otherwise 1.  
/A-Reg 0 to ignore spaces, tabs, non-numeric char.  
/A-Reg = 0 to ignore space, tabs only  
/A-Reg 0 do not ignore any char.

2. CALLING SEQUENCE:

- a) CALL GTNO
- b) XAC NONO

3. INPUT:

Characters from input buffer

4. OUTPUT:

- a. 1) Octal number in two's complement notation in A-register
- a. 2) Terminating ASCII character in B-register
- b) If last number returned by GTNO was a zero because no number found, NONO returns zero.

5. ACTION:

NUM, which is to contain the octal number, is zeroed, and flags are initialized. A character is retrieved from the buffer. If it is numeric, NUM is reset to  $NUM * 12_8 + (\text{octal value of input numeric})$ , and another character is retrieved.

If the character is not numeric, a check is made to determine if it is a terminating character, i. e., if any previous character was numeric. If so, a jump is made to complement the input number if necessary and return with the terminating character in the B-register.

If the character is not a terminating character and is a negative sign, a flag is set to indicate complementation is needed before returning to the calling routine, and the next character is retrieved.

If the character is a carriage return, the A-register is zeroed, and return is made to the calling routine with the carriage return ASCII code in the B-register.

Otherwise the character is ignored and the next character is received.

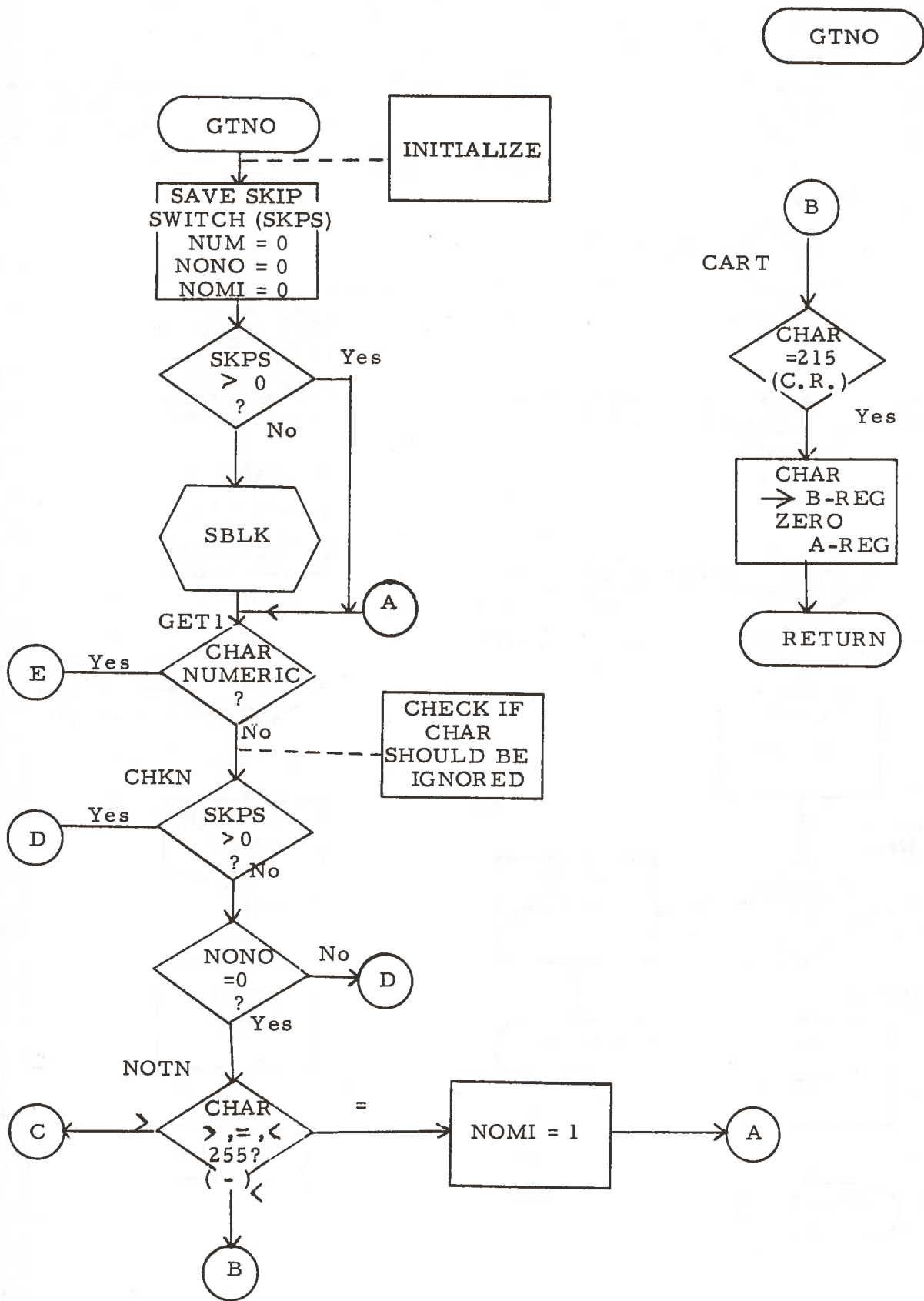
If the input number overflows, an error message is typed, and control is transferred to the calling routine, with the A-register zero.

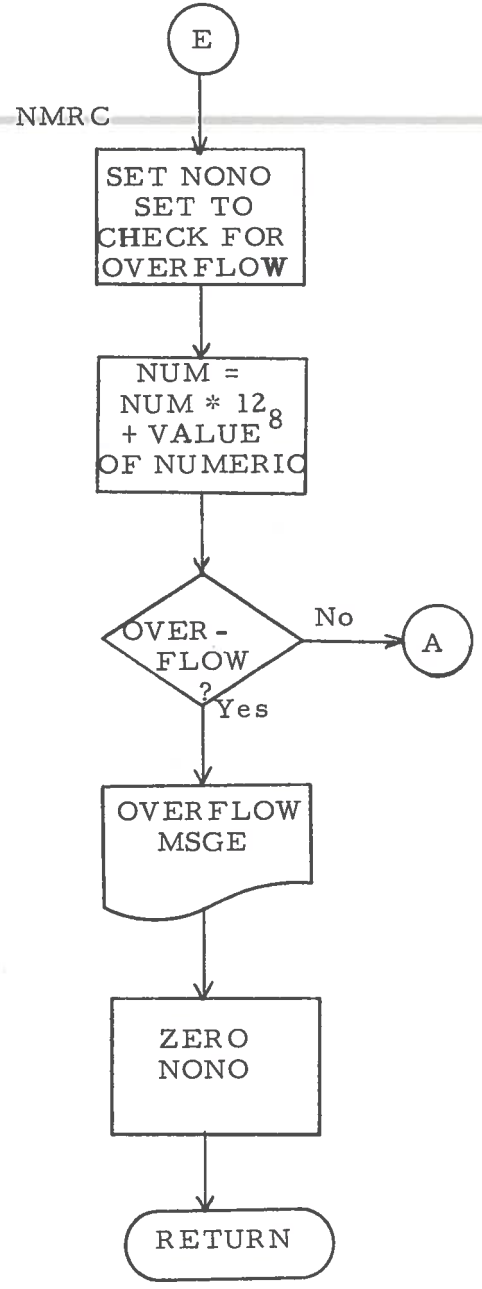
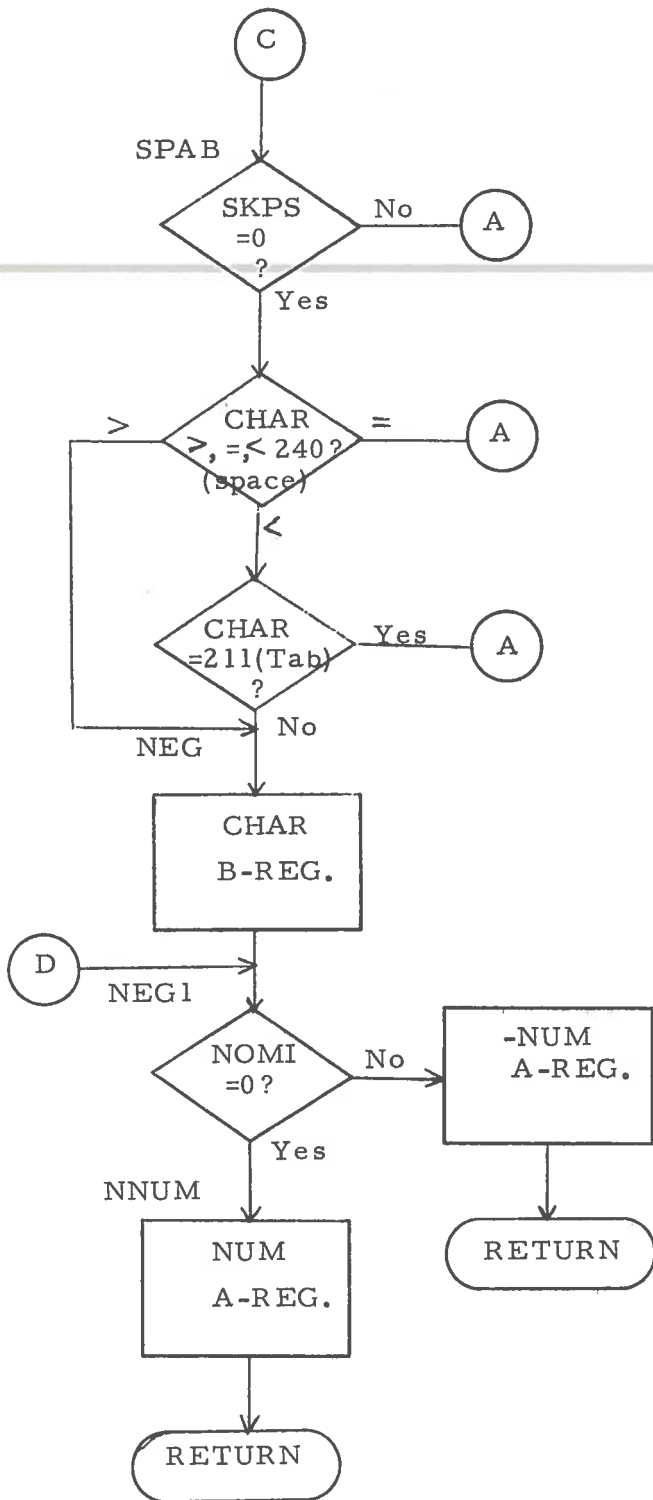
6. EXTERNAL REFERENCES:

SBLK  
GT1C  
TAO\$

7. CORE USED:

$161_8$





SUBROUTINE:

IDER

Check Identification Number

1. PURPOSE:

Get identification number and check range.

2. CALLING SEQUENCE:

CALL IDER

3. INPUT:

ID number extracted from input buffer

4. OUTPUT:

- a) If  $1 \leq ID \leq 64$ , return ID number in A-register.  
otherwise output error message.

5. ACTION:

A number is extracted from the input buffer. If it is a number from 1 to 64, it is returned to the calling program in the A-register.

If the number is not in the above range, an error message is typed and the error return is taken.

6. EXTERNAL REFERENCE:

GTNO

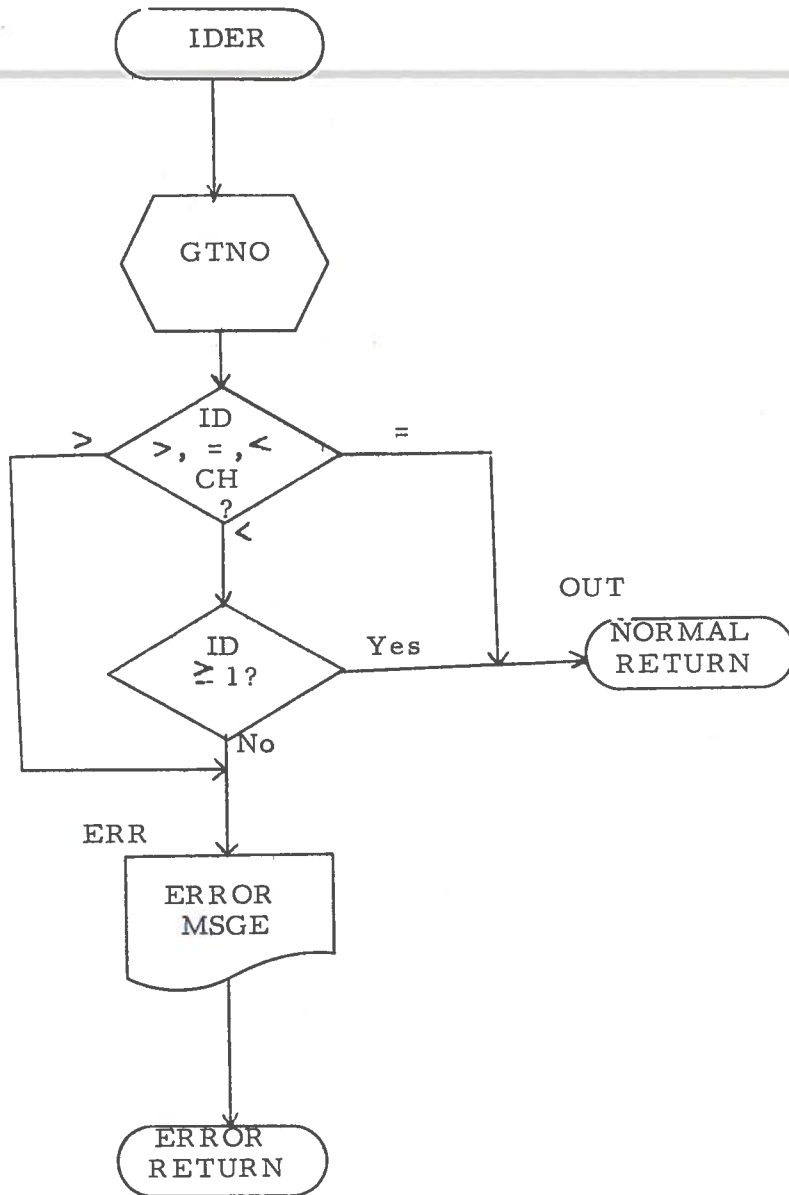
TAO\$

7. CORE USED:

34<sub>8</sub>



IDER



## INPUT BUFFER SERVICE ROUTINES

### 1. PURPOSE:

To retrieve characters from input buffer

### 2. ENTRY POINTS AND CALLING SEQUENCE:

- a) CHBF            70 character buffer; no calling sequence
- b) CALL CHI        Initializes flags and pointers to retrieve  
initial character from buffer ignoring  
spaces and tabs
- c) CALL SBLK      Resets flag to ignore spaces and tabs
- d) CALL GTCH      Retrieve two characters; return with  
first in A-register, second in  
B-register
- e) CALL GT1C      Retrieve one character; return with  
character in A-register
- f) CALL SAVP      Saves flags and ptrs to return to current  
retrieval location
- g) CALL RESP      Restores flags and ptrs to previously  
saved location

### 3. INPUT:

Characters in CHBF

### 4. OUTPUT:

None for a, b, c, f, g. Two characters from buffer for d.  
One character from buffer for e.

### 5. ACTION:

- a) No action
- b) Pointer is set to beginning of input buffer (CHBF). Half  
word indicator (LEFT) is zeroed to indicate character is to be  
retrieved from left half of buffer word. Resets switch (BSSW) to  
ignore spaces and tabs. Returns to calling program.

c) BSSW reset to ignore spaces and tabs. Returns to calling program.

d) Calls GT1C to get one character. Stores in B-register. Calls GT1C to get second character. Exchanges A-and B-registers. Returns to calling program.

e) Checks LEFT to determine which half of buffer word to take character from. If character is a carriage return, pointers are not incremented and control is returned to the calling program. Otherwise LEFT is switched to get next character from opposite half of word. The buffer pointer is incremented if LEFT now indicates the left half of the word. If the character is a space or a tab, BSSW is checked to determine if the space or tab should be ignored and another character retrieved. When an acceptable character is found, it is returned to the calling program through the A-register.

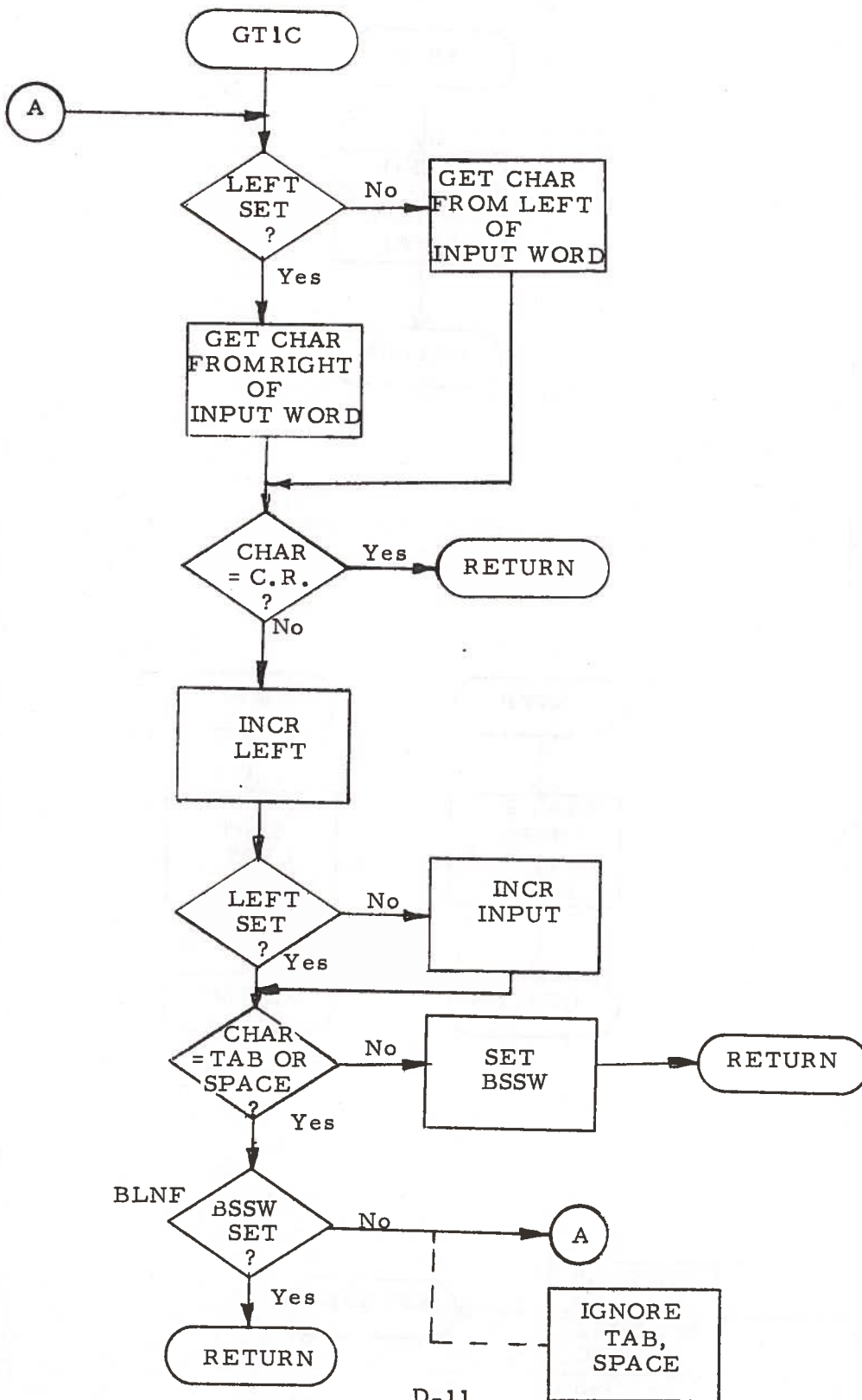
f) Stores ptrs and flags used for retrieval of chars. in temporary locations.

g) Restores ptrs and flags previously saved.

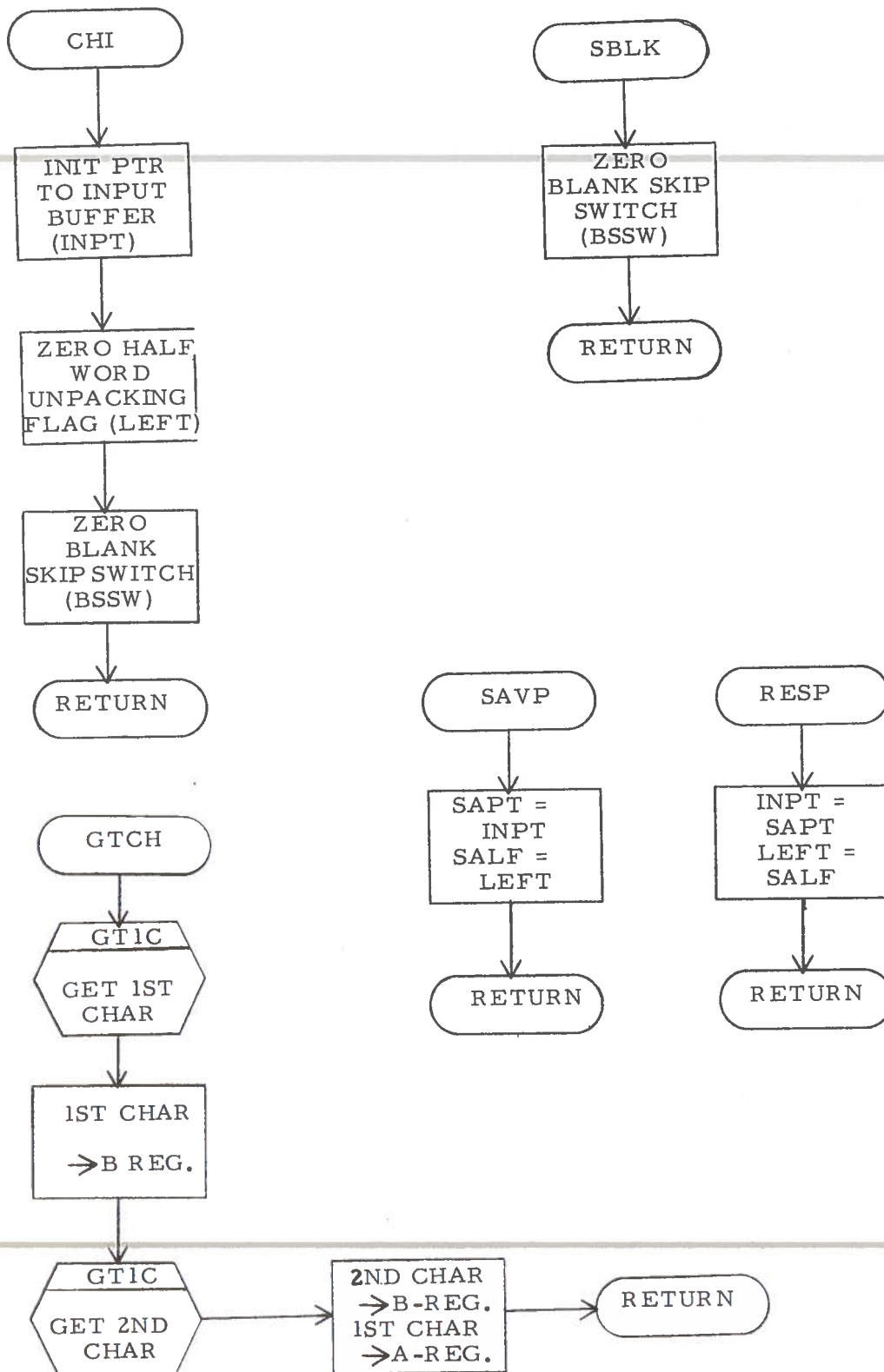
7. CORE USED:

151<sub>8</sub>

INPUT BUFFER  
SERVICE RTNS



INPUT BUFFER  
SERVICE R TNS



## LTYP

1. PURPOSE:

Interprets arguments specifying line type (texture).

2. CALLING SEQUENCE:

CALL LTYP

3. INPUT:

Characters from input buffer

4. OUTPUT:

A-Reg = -1; illegal first char.; illegal char new in B-Reg.  
= 0; code for solid line  
= 1; code for dotted line  
= 2; code for dashed line  
= 3; code for dot-dash line

5. ACTION:

Gets char. and checks for S or D. If D, checks for A, or D for second char. Returns appropriate type or error code.

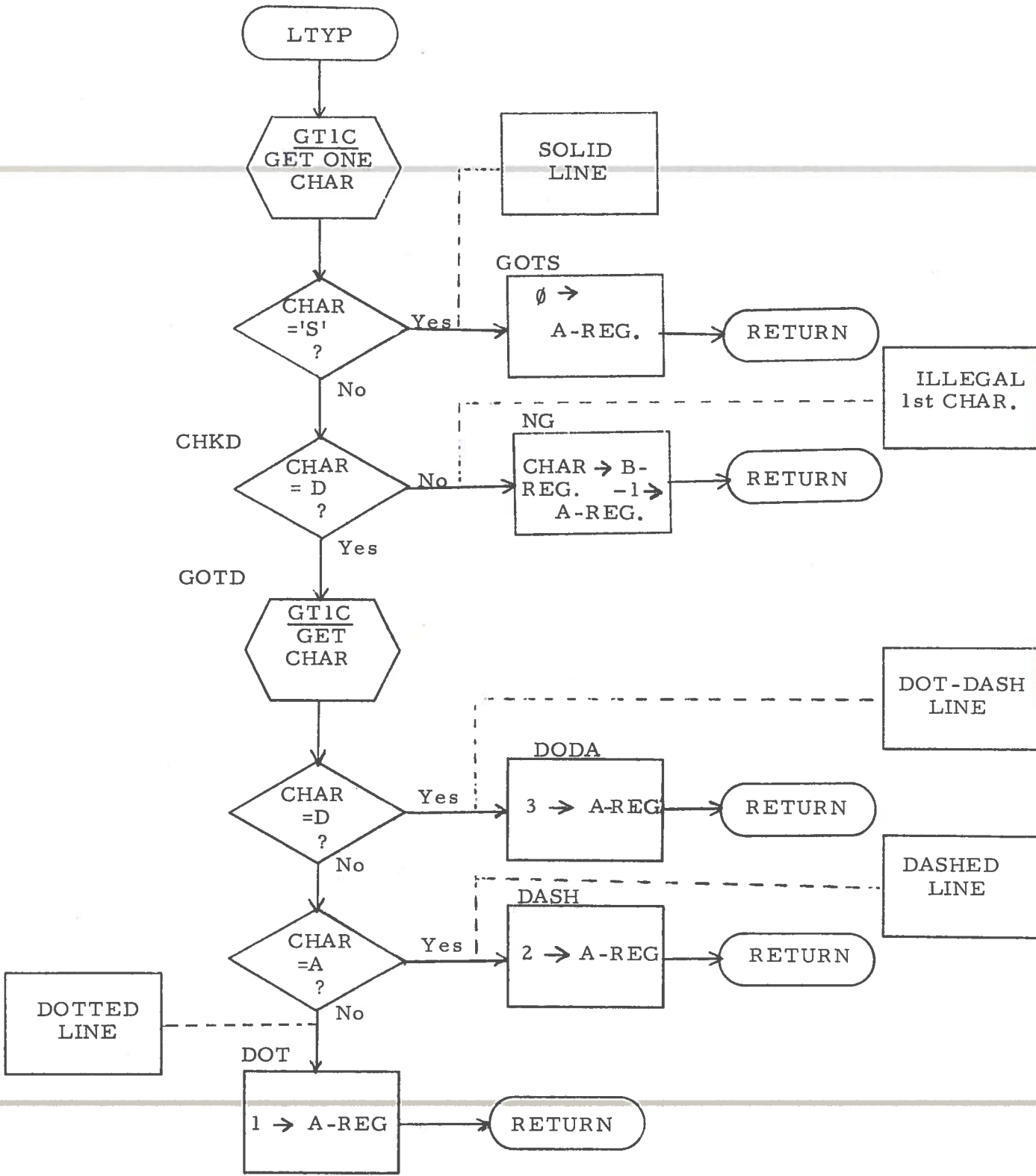
6. EXTERNAL REFERENCES:

GT1C

7. CORE USED:

41<sub>8</sub>

LTYP  
FLOWCHART



## SUBROUTINE

## OUTP

### 1. PURPOSE:

Stores output file on disc or paper tape after compiling any expression found in register definition and conditioning blocks.

### 2. CALLING SEQUENCE:

JST OUTP  
DAC FRHD  
DAC FPNT  
DAC FLG3  
XAC GETF  
DAC RELF  
DAC FREE  
DAC LRG  
DAC LST  
(NAME)  
(NAME + 1)  
(NAME + 2)

### 3. INPUT:

See calling sequence.

### 4. OUTPUT:

The data structure is saved on disc or paper tape after the dynamic expressions are compiled and the conditioning blocks are compressed.

### 5. ACTION:

The conditioning blocks in the data structure are searched for any dynamic expressions which have not been previously compiled. If not, the mini-compiler (SCOM) is called and the start address of the object code is stored in the conditioning block. After conditioning block compression, unused storage is returned to the free storage ring.

Finally, the data structure is output on either disc or paper tape.

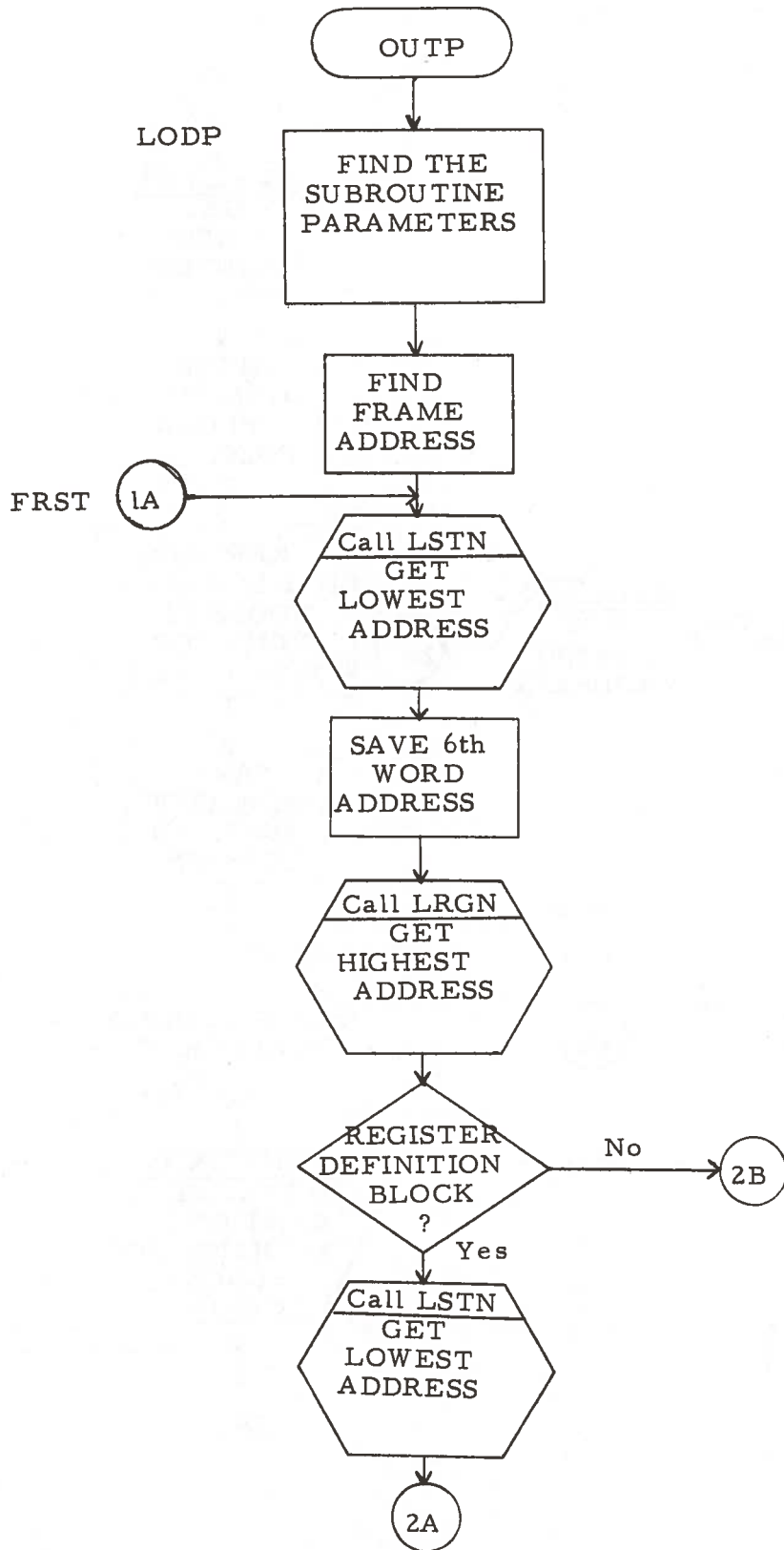


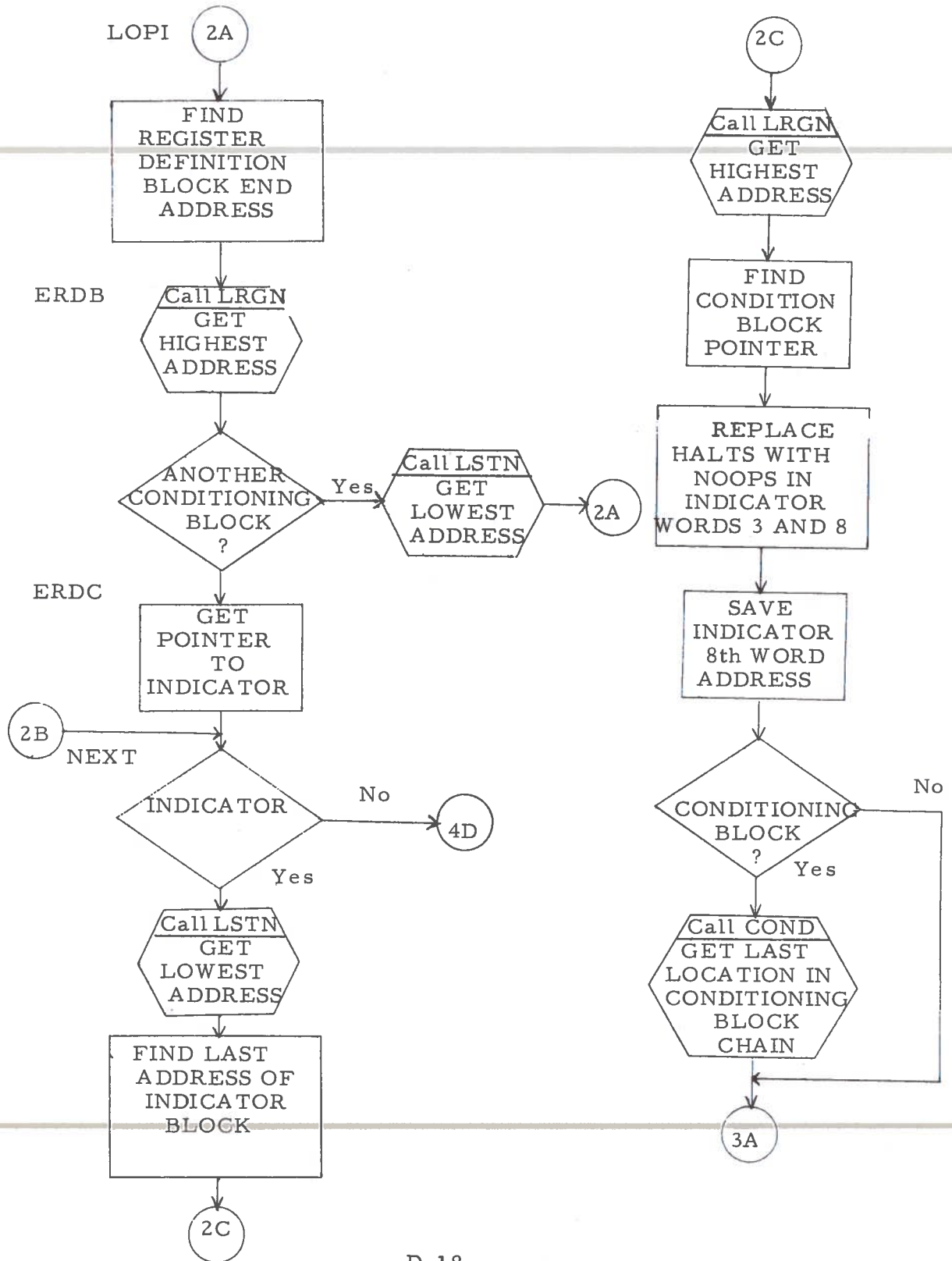
6. EXTERNAL REFERENCES:

External subroutines and addresses are referenced by the calling sequence.

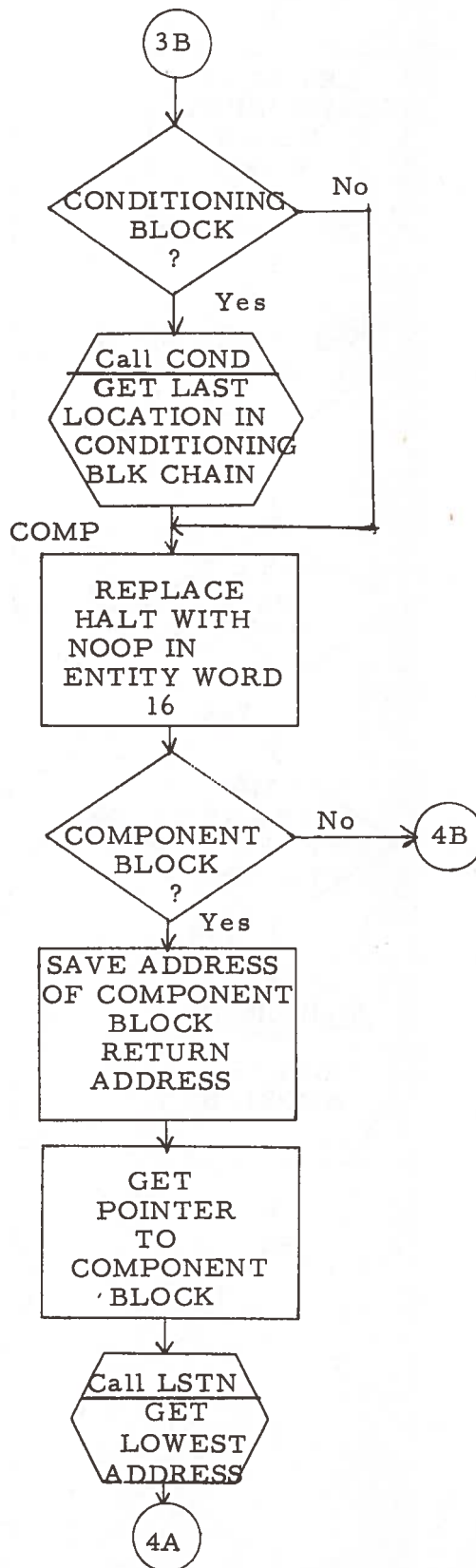
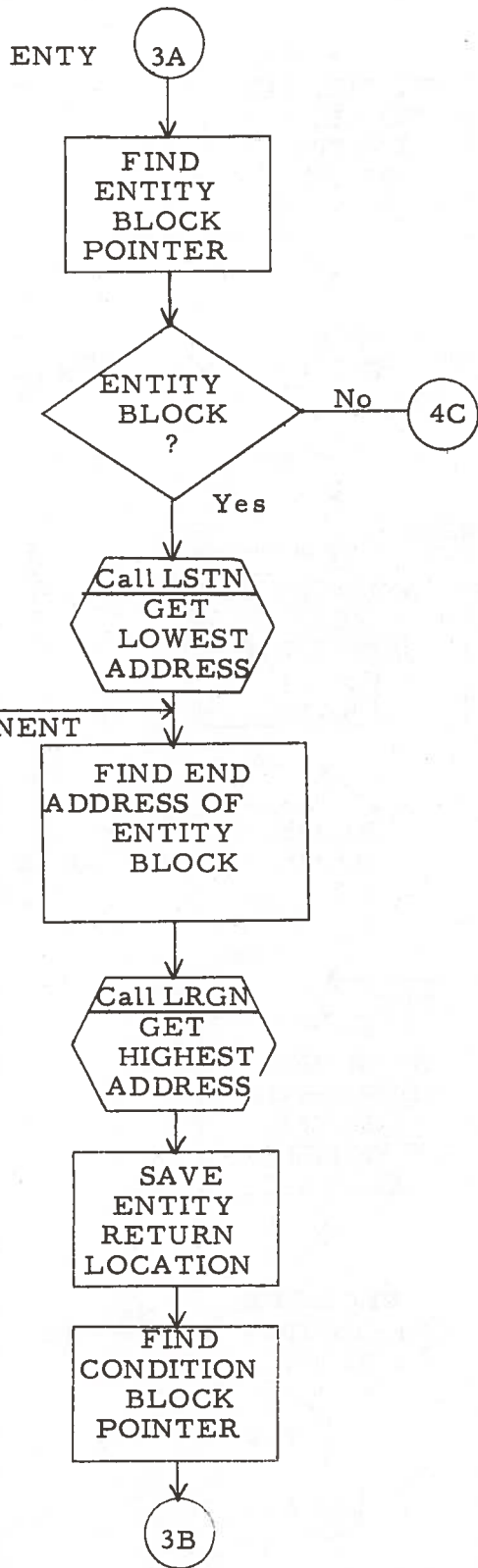
7. CORE USED:

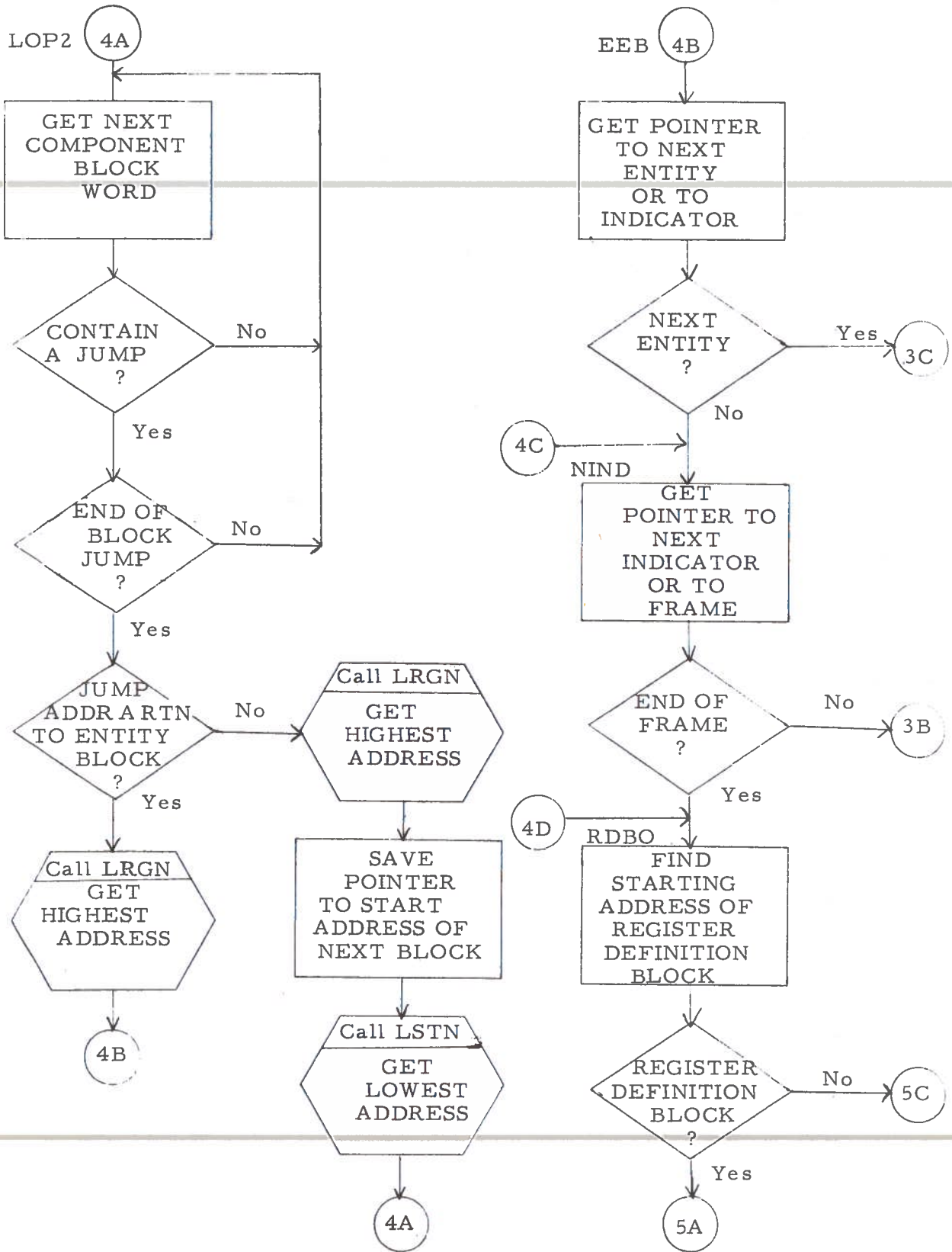
1103<sub>8</sub>



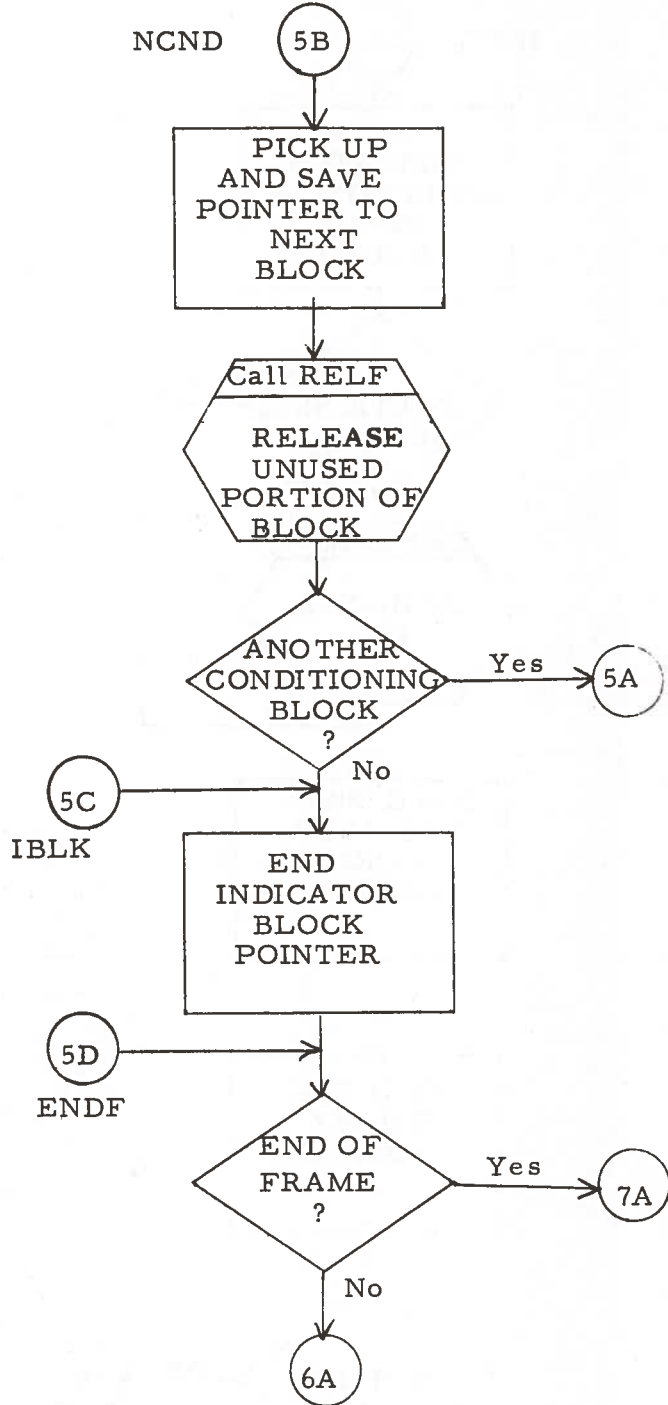
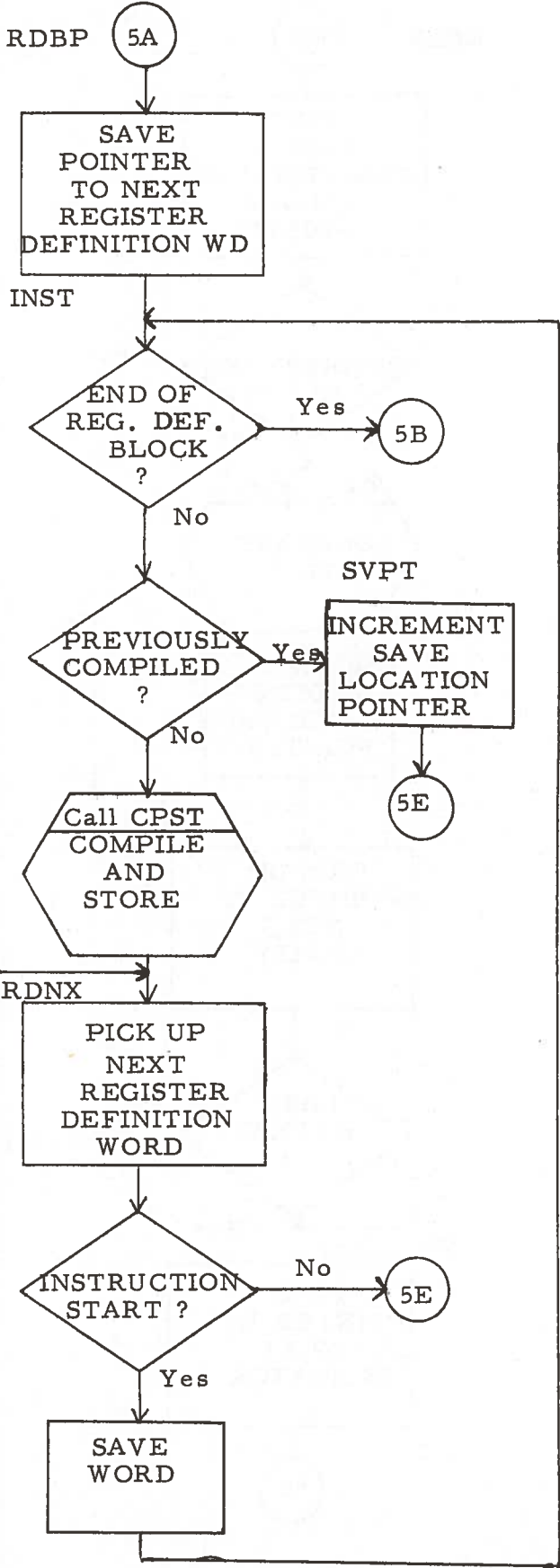


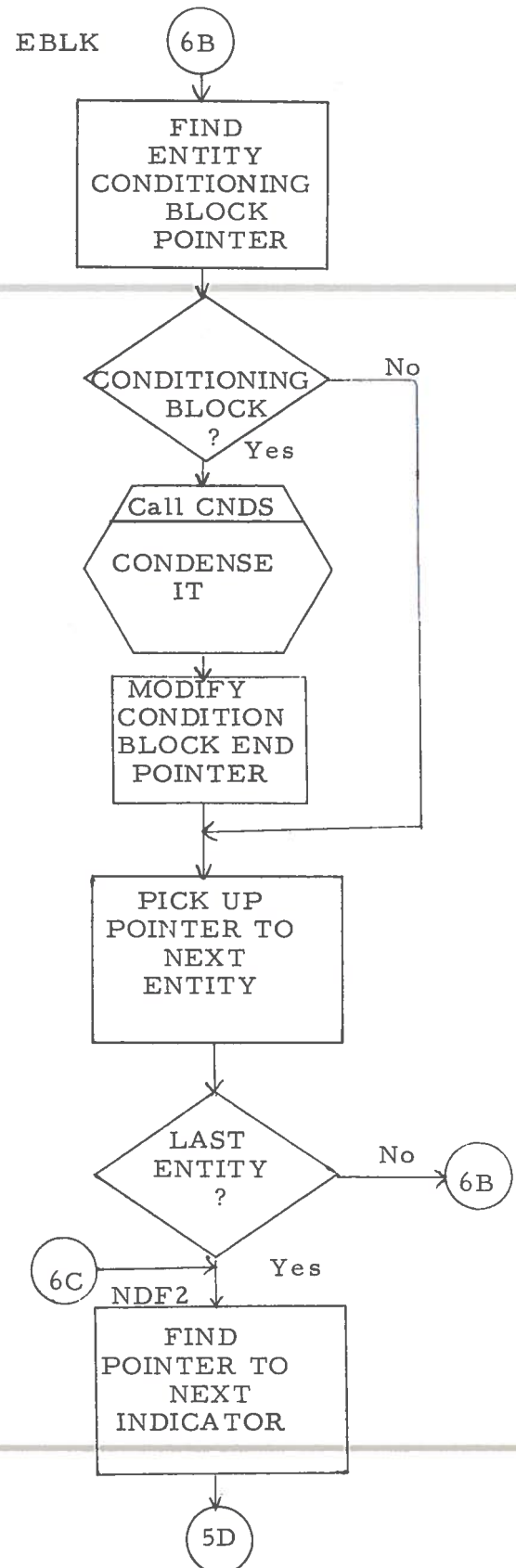
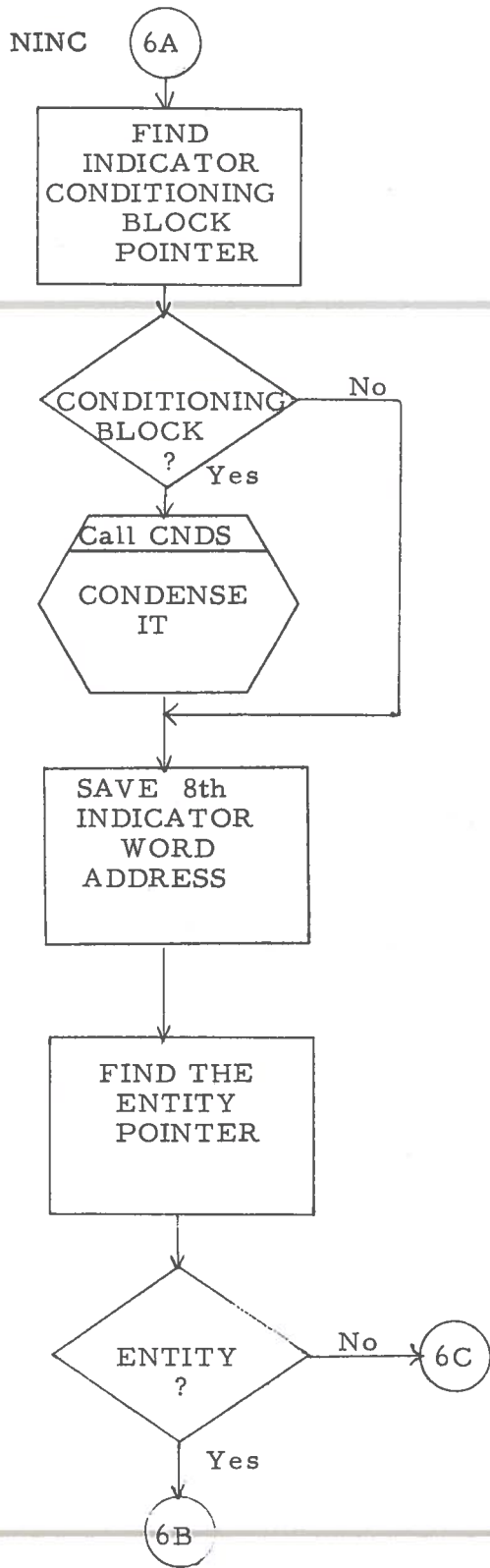
OUTP

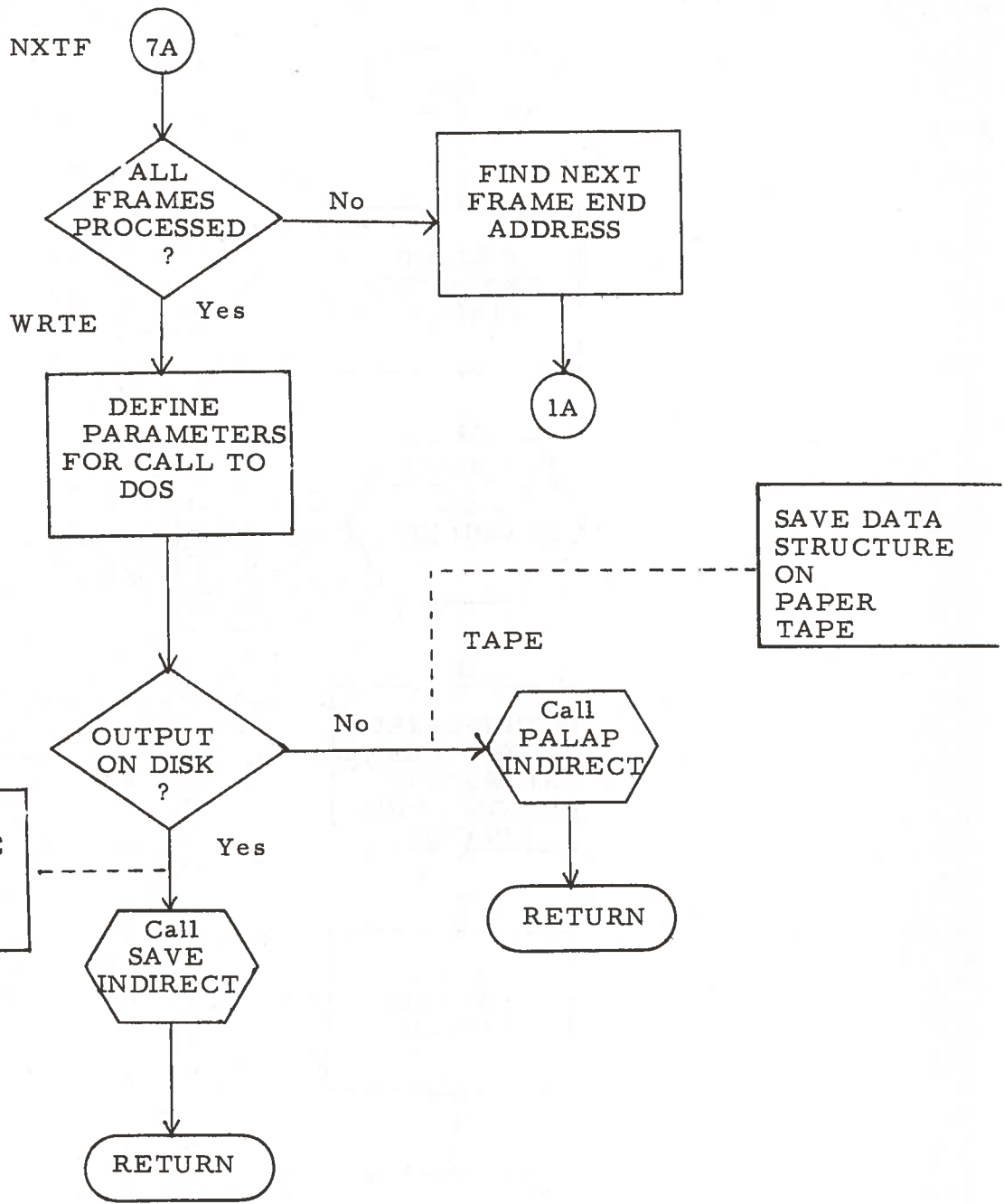




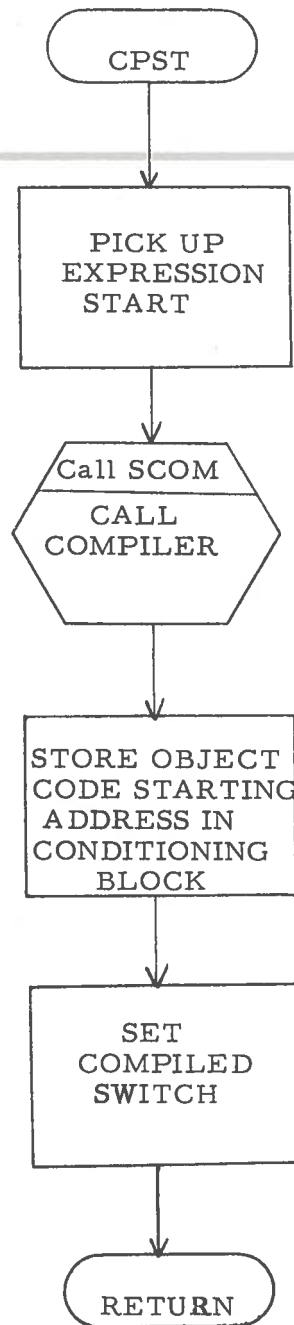
OUTP





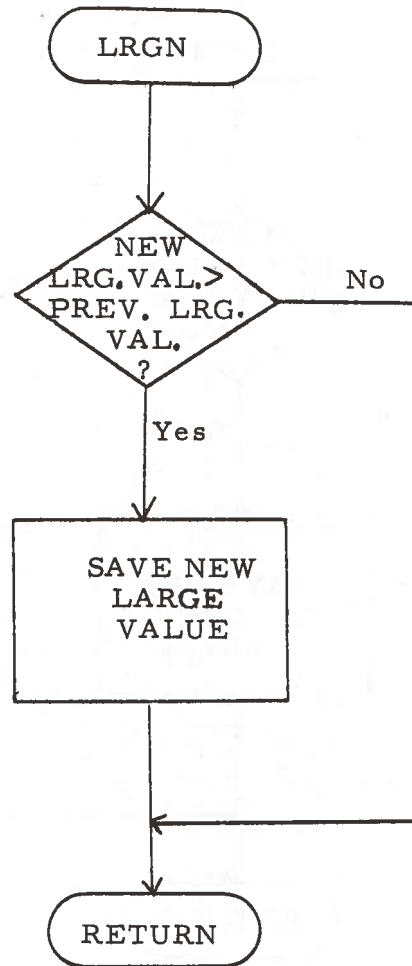




CPST: Compile and Store

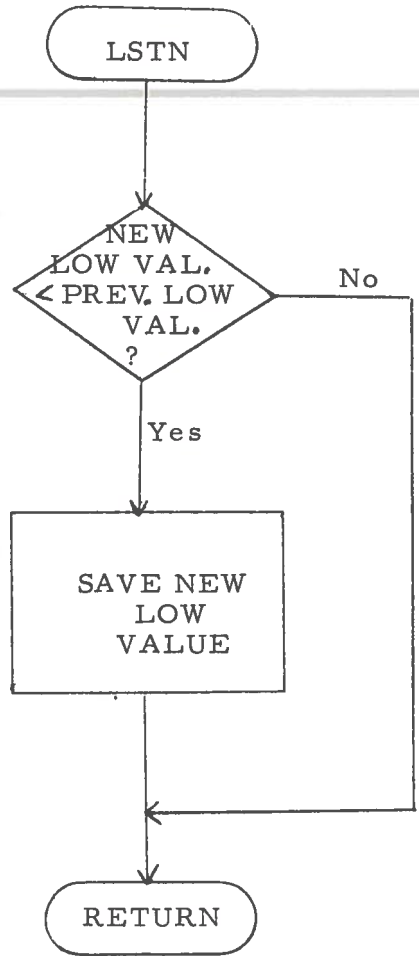
OUTP

LRGN: Find Highest Value



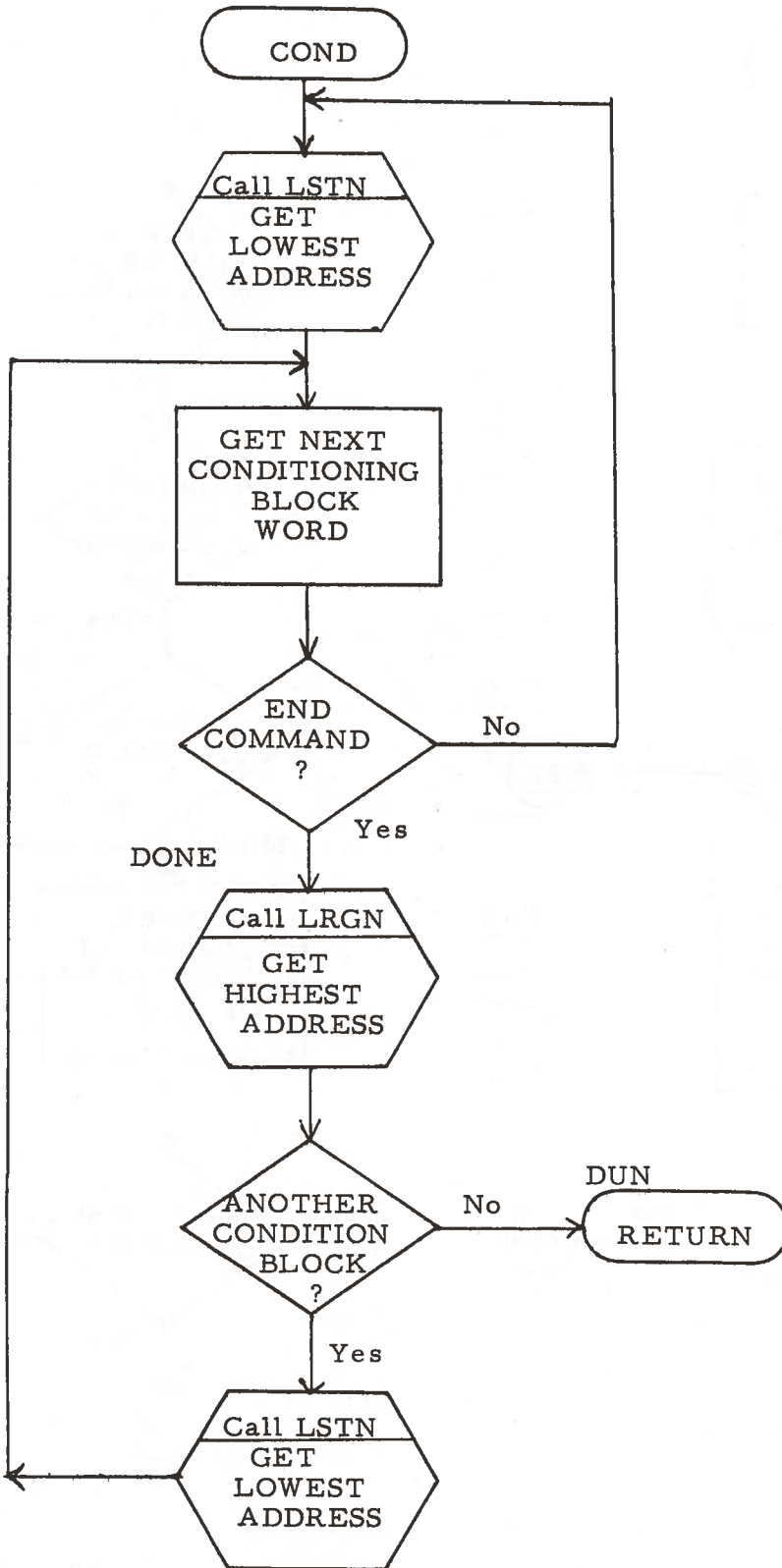
LSTN: Find Lowest Value

OUTP

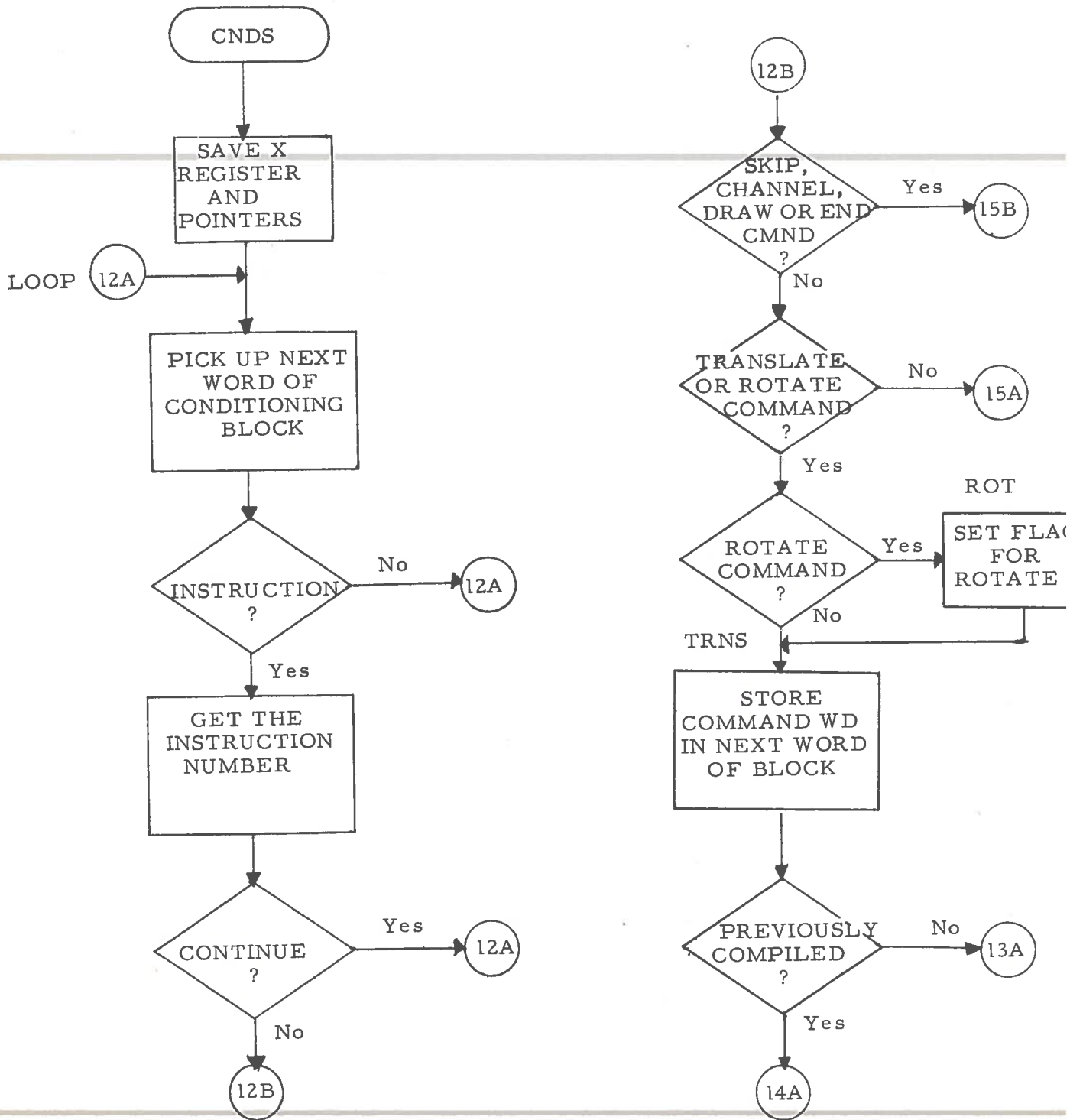


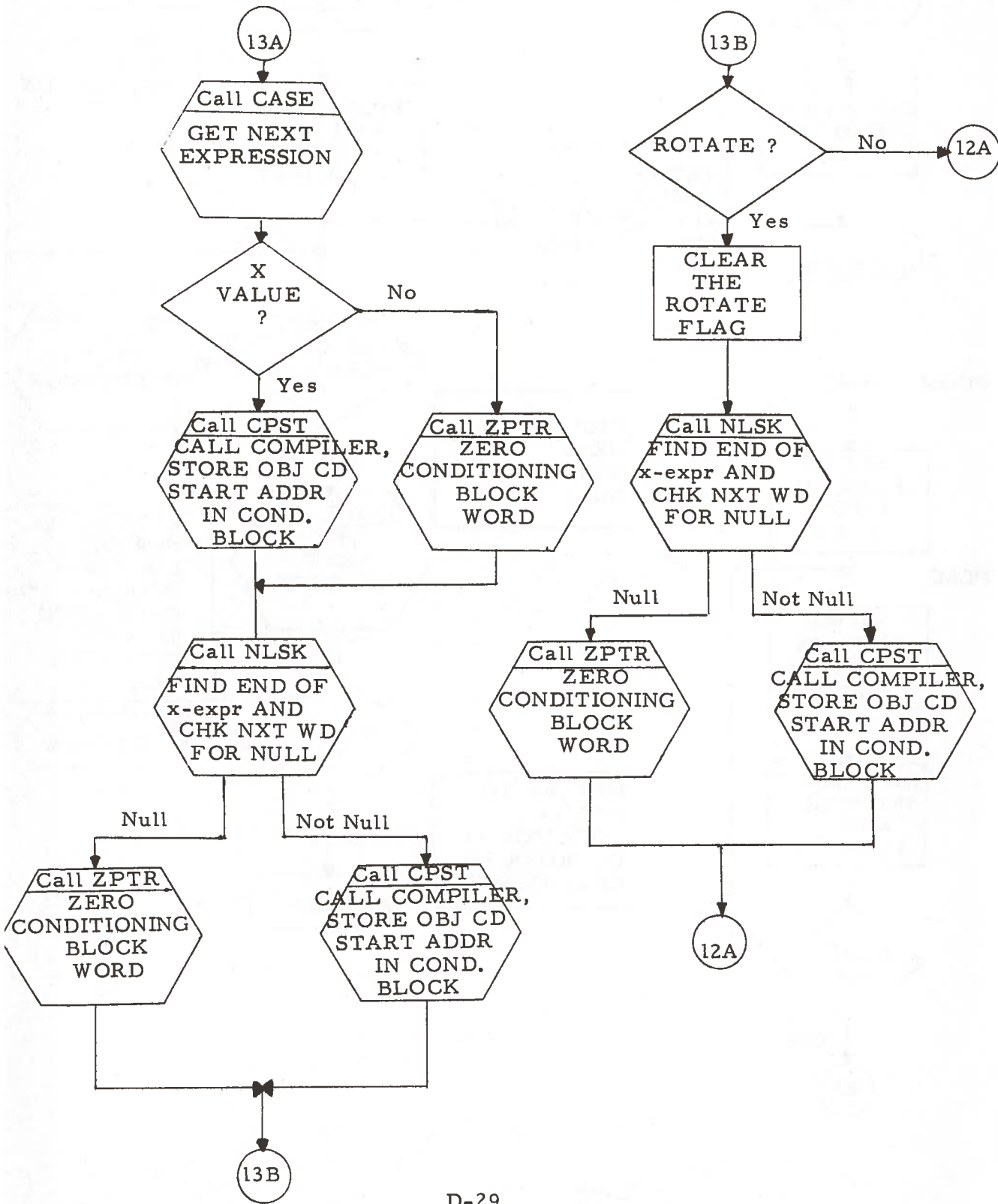
COND: Get Last Location in Conditioning Block Chain

OUTP



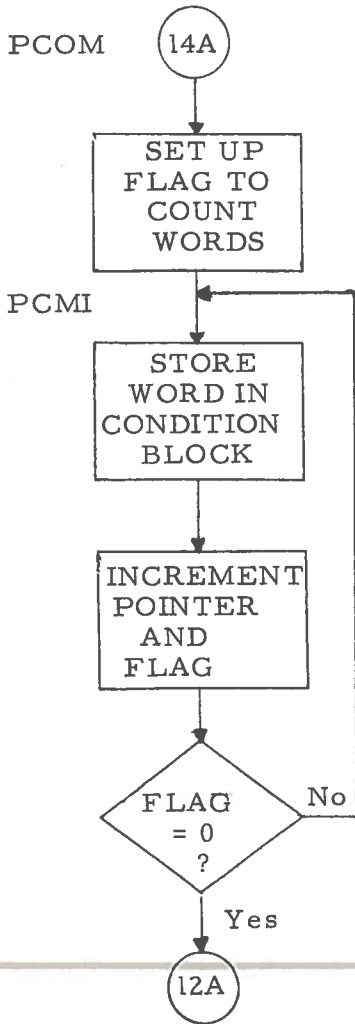
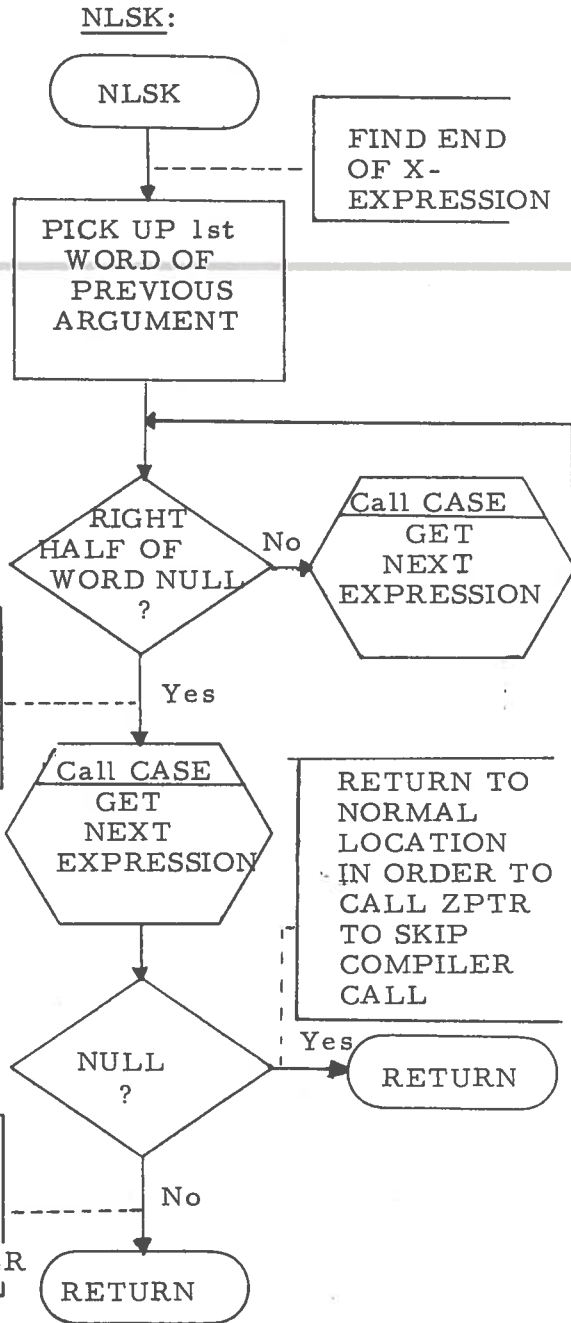
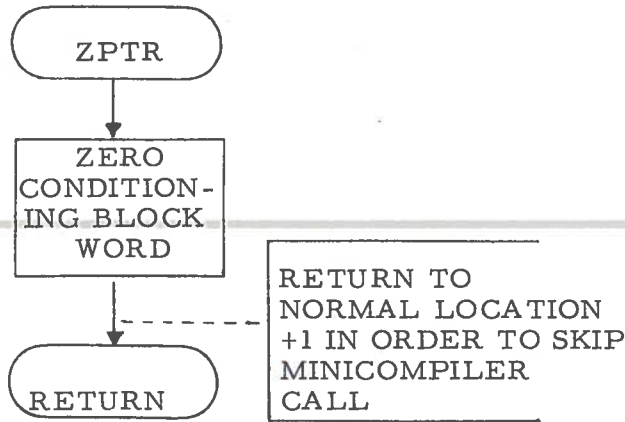
OUTP

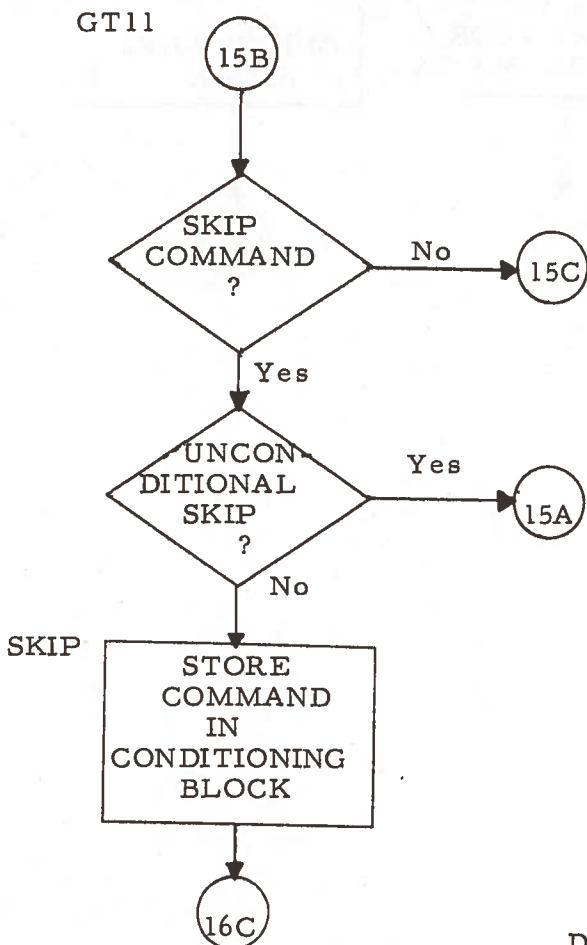
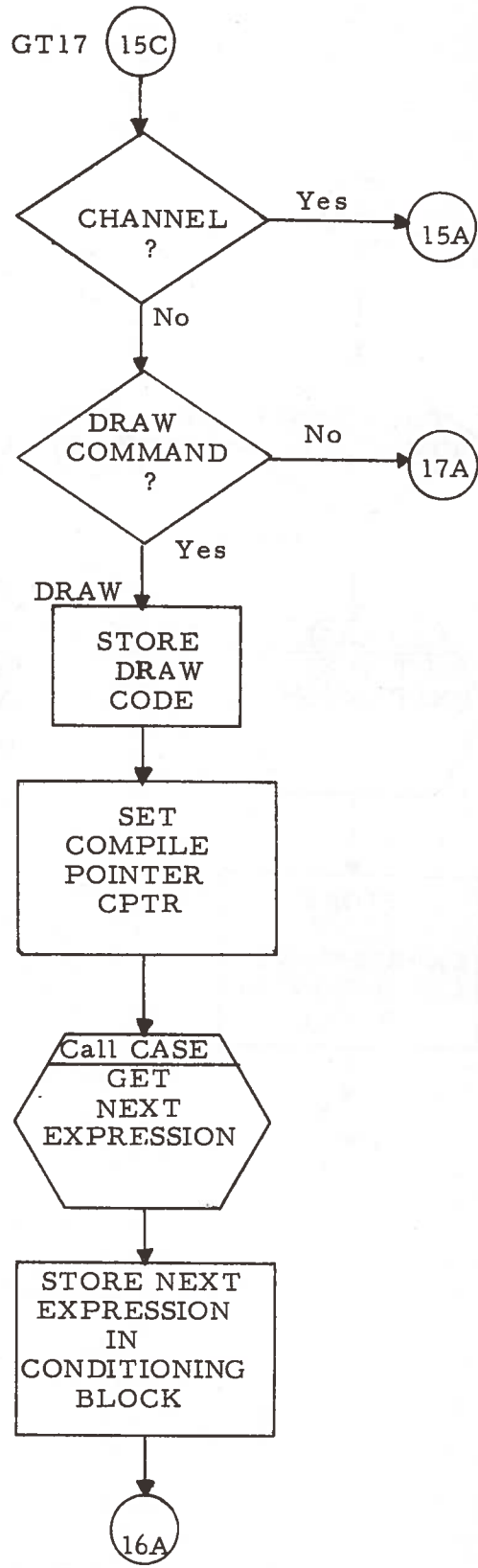
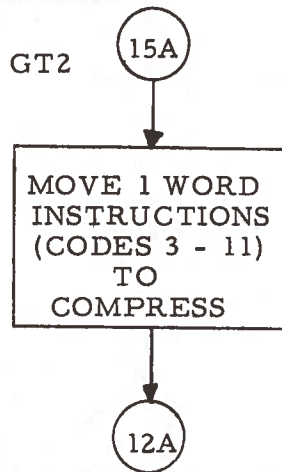




ZPTR: Zero Conditioning Block Word

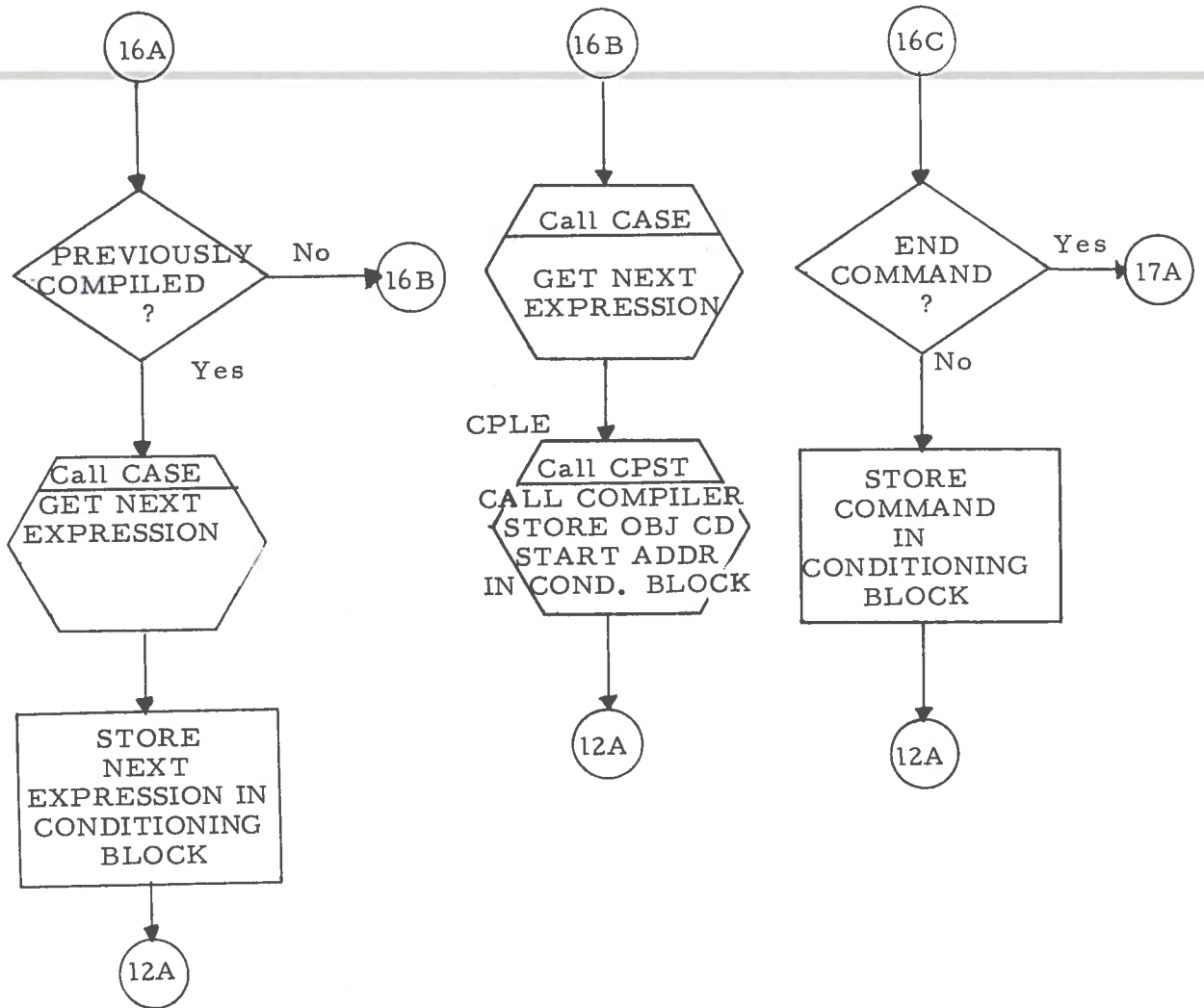
OUTP

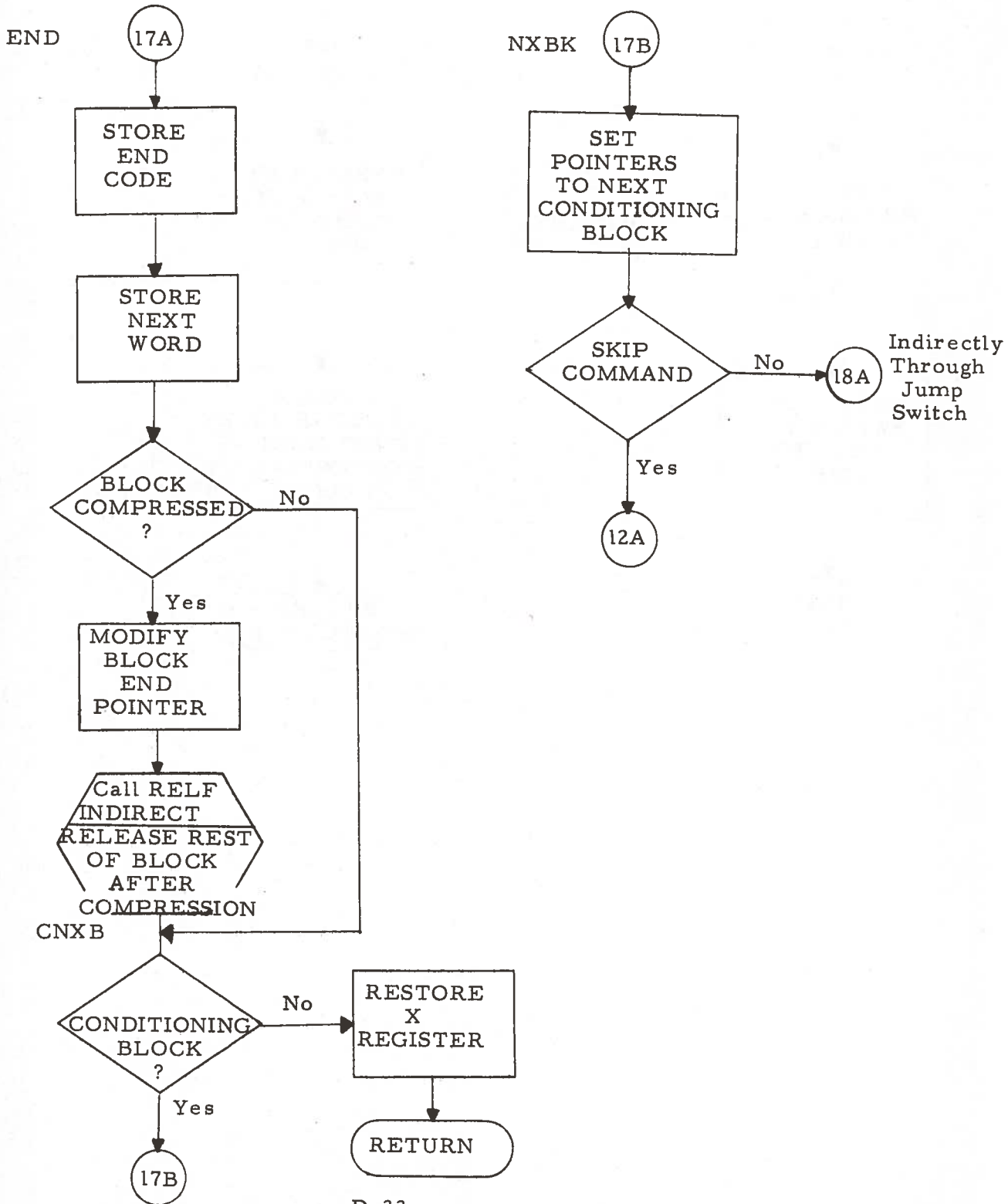




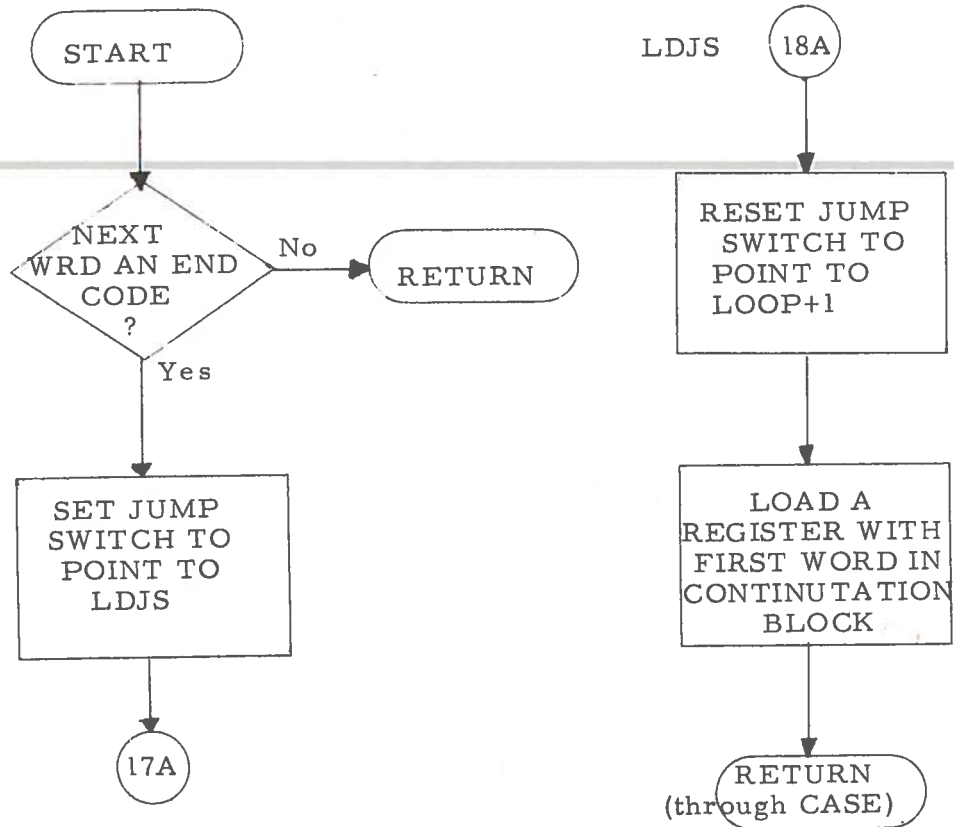


OUTP





CASE



## SUBROUTINE

## SCOM

1. PURPOSE:

Compile Fortran type arithmetic statements using integer arithmetic and register numbers as variables.

2. CALLING SEQUENCE:

```
LDA    PTR
CALL   SCOM
```

Where PTR is start address of dynamic expression.

3. INPUT:

Start address of dynamic expression from calling sequence.

4. OUTPUT:

Start address of compiled object code in A register.

5. ACTION:

SCOM compiles a dynamic expression found in the register definition or conditioning blocks. The free storage necessary for the object code is obtained from the free storage ring. The hierarchy of arithmetic operation is:

	Subroutines	Class 0
**	Exponentiation	Class 1
/	Division	Class 2
*	Multiplication	Class 2
+	Addition	Class 3
-	Subtraction	Class 3

6. EXTERNAL REFERENCES:

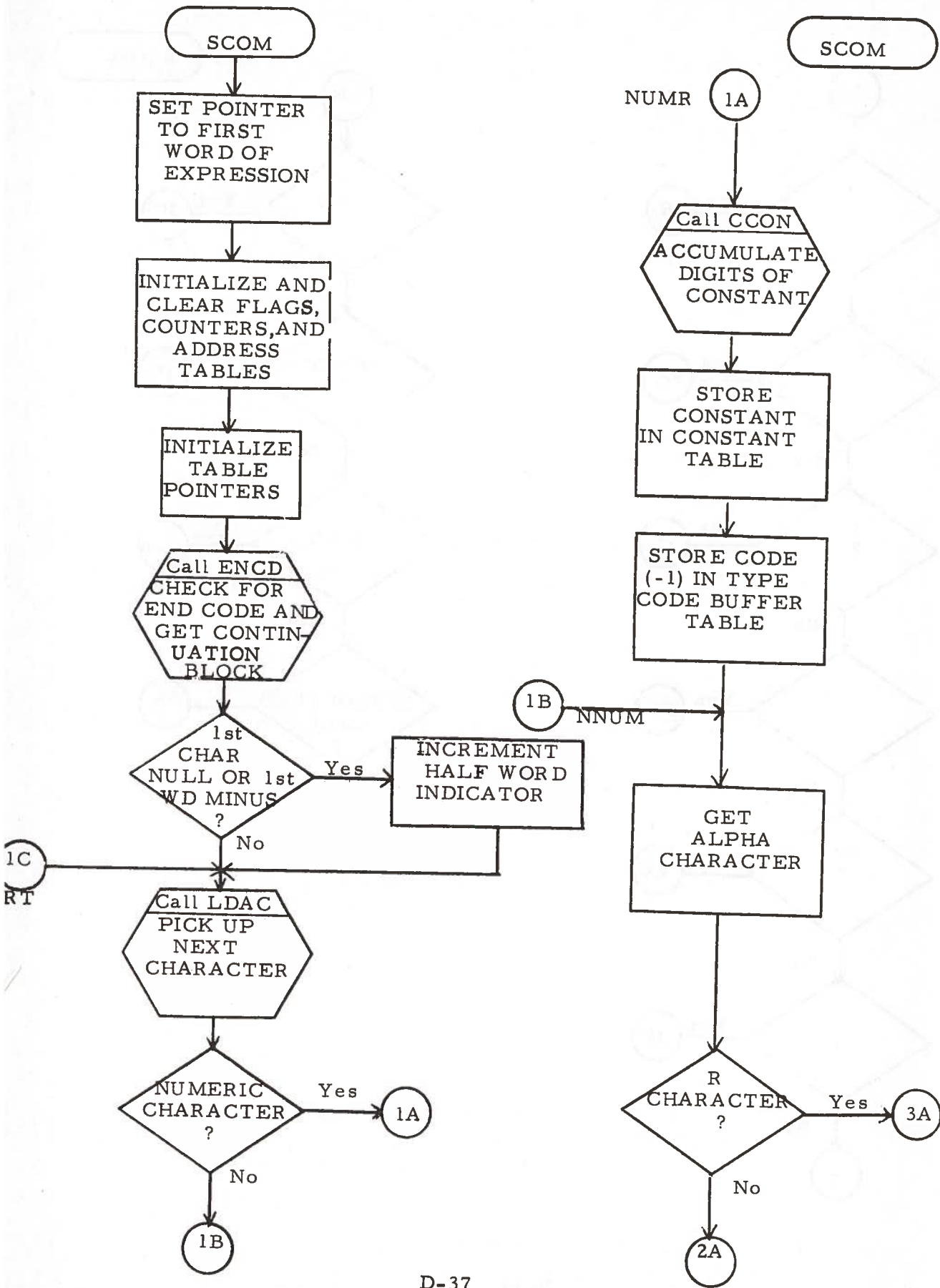
GETF	Get block from free storage
IABS	Computes absolute value of integer argument
ISQRT	Computes square root of integer argument
IMAX	Maximum value of specified variables
IMIN	Minimum value of specified variables
ISIGN	Transfers sign from argument j to absolute value of argument i. (ISIGN (i, j)).

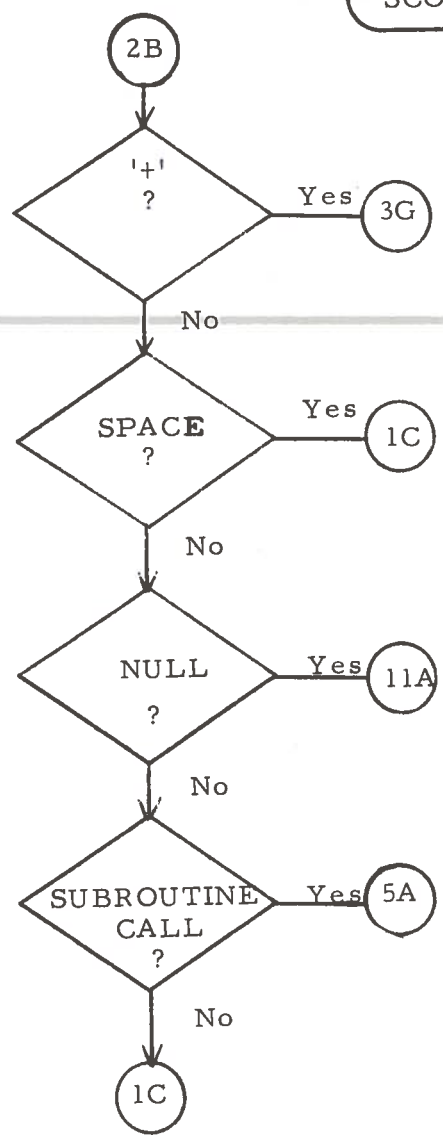
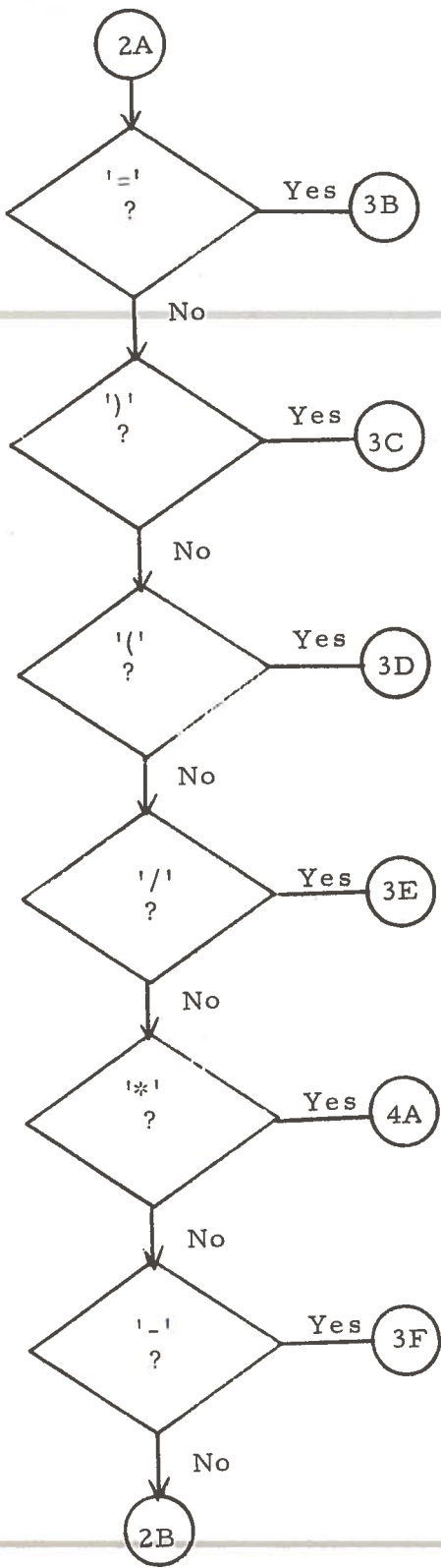
IDIM

IMOD

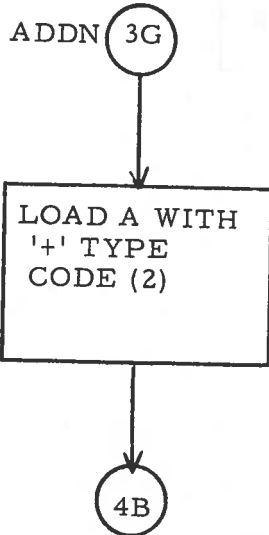
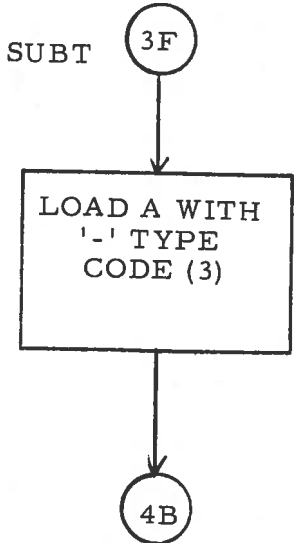
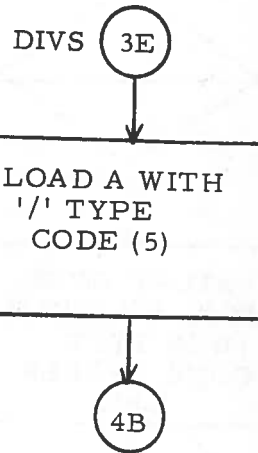
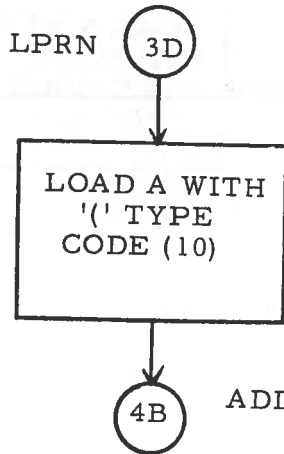
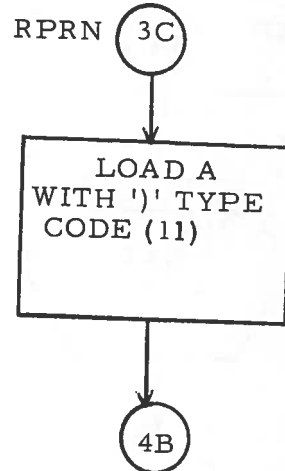
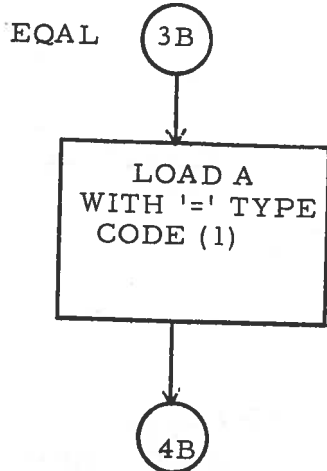
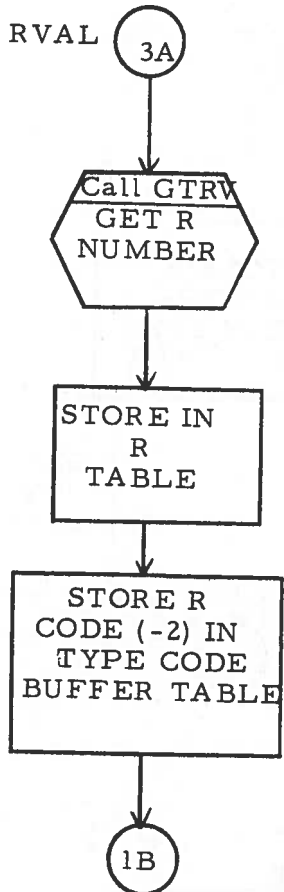
7. CORE USED:

2032<sub>8</sub>





SCOM





MOXP

4A

Call LDAC  
PICK UP  
NEXT  
CHAR

ALPHA  
CHARACTER  
?

No

NXNO

STORE CODE  
FOR MULTIPLY  
(4) IN TYPE  
CODE BUFFER  
TBL

1A

1C

Yes

!\*!  
?

Yes

EXPN

LOAD A WITH  
EXPONENT  
TYPE CODE  
(6)

4B

No

STORE CODE  
FOR MULTIPLY  
(4) IN TYPE  
CODE BUFFER  
TBL

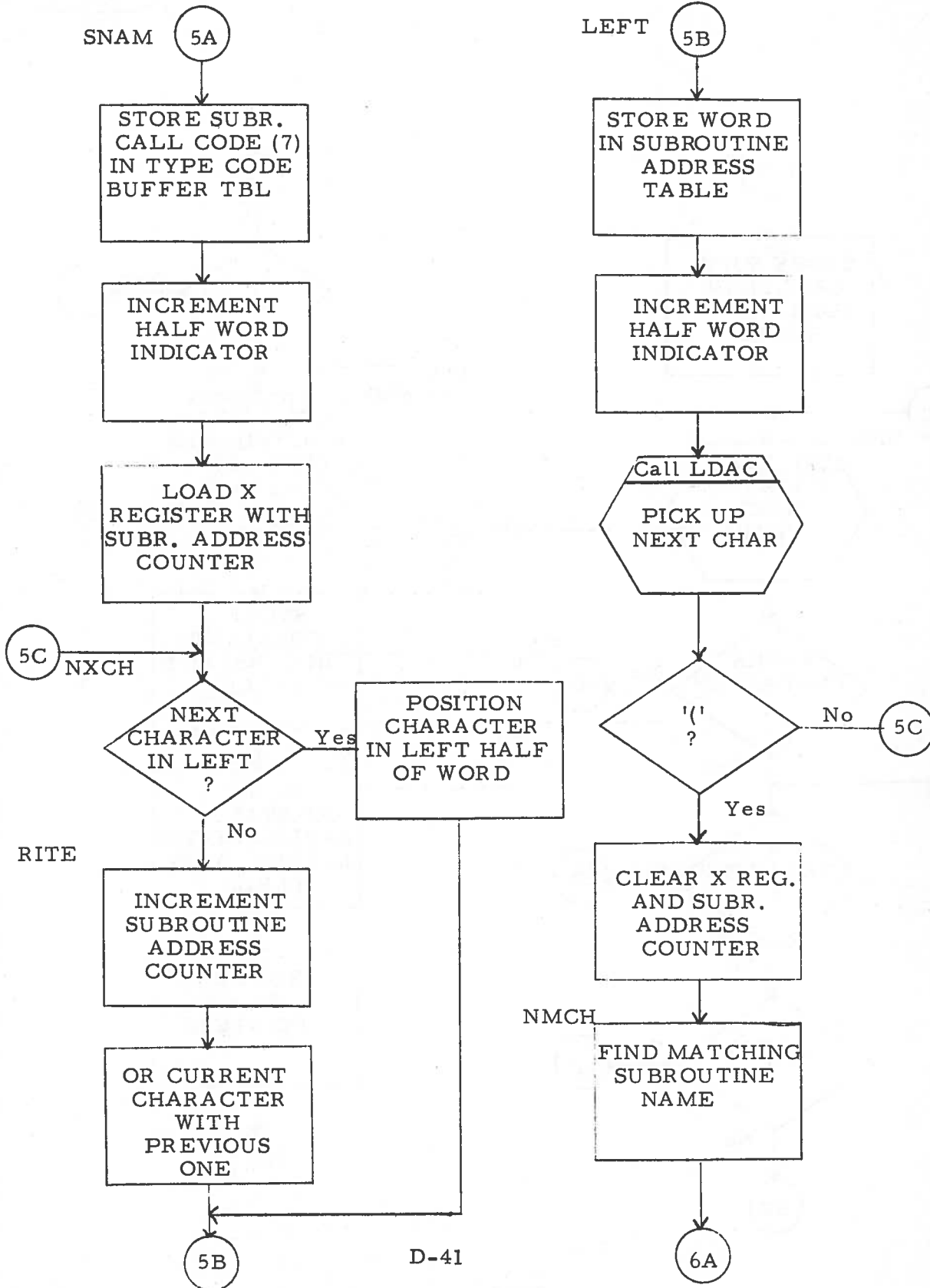
1B

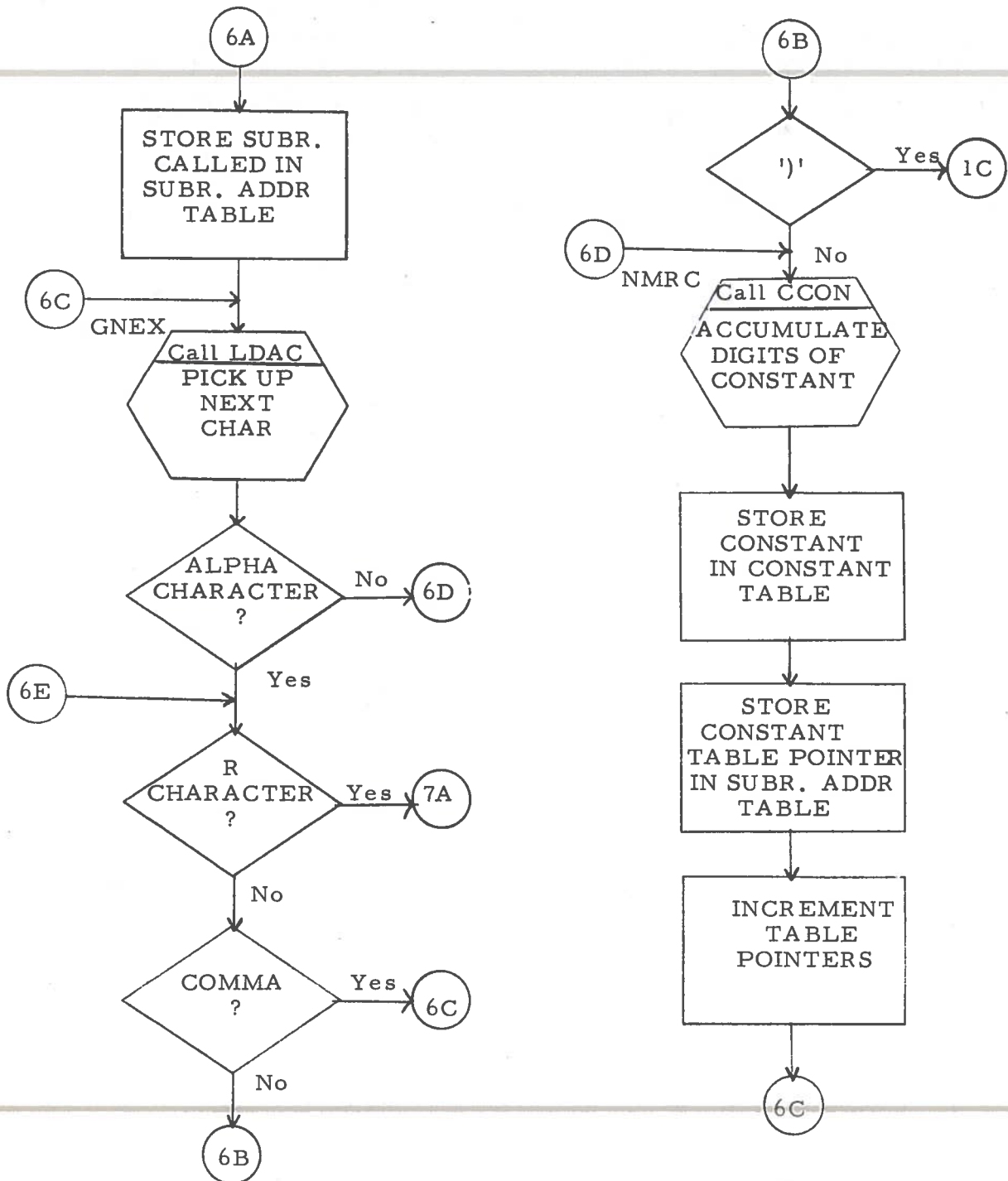
STCD

4B

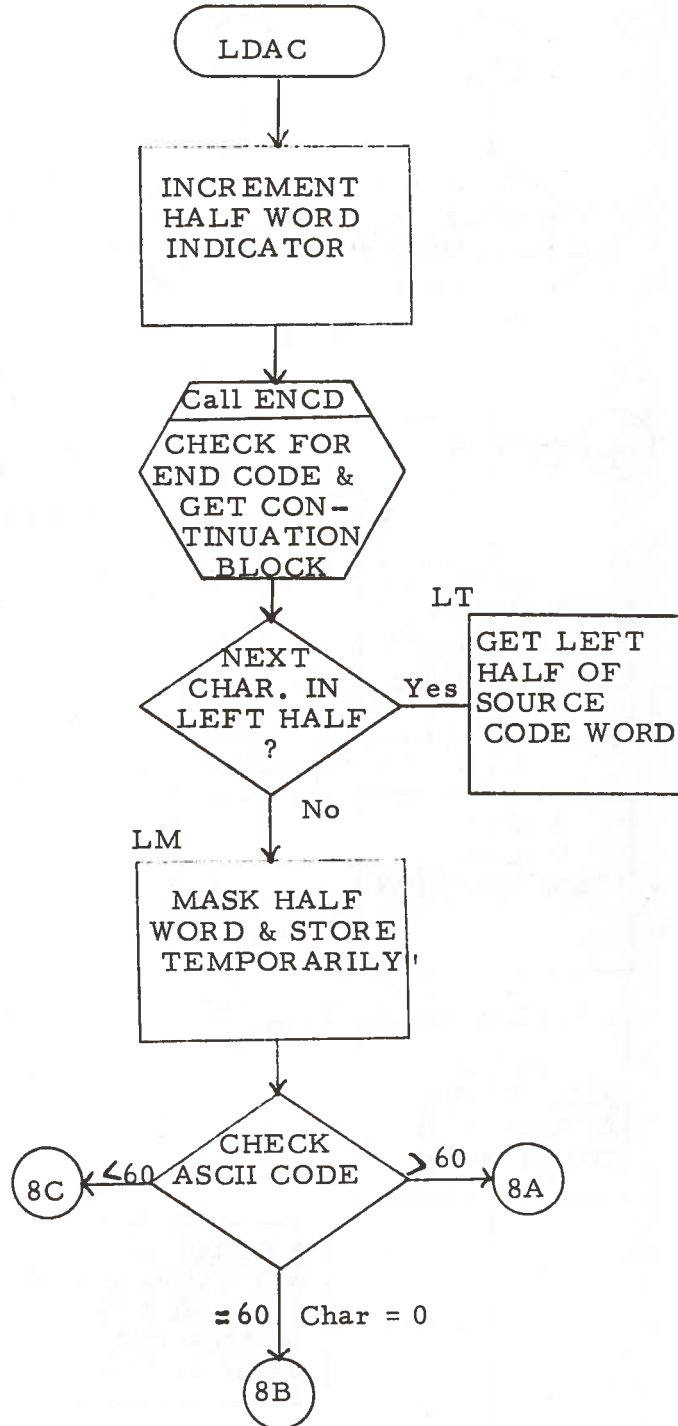
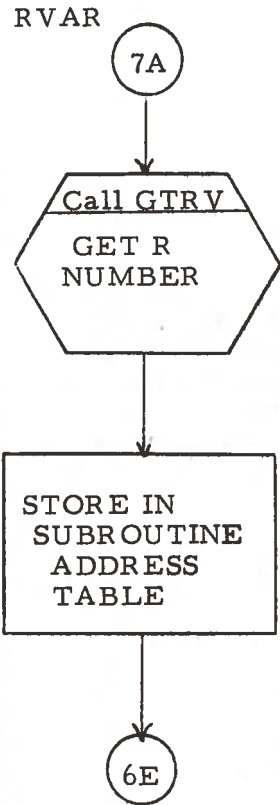
STORE A  
(CHAR TYPE  
CODE) IN TYPE  
CODE BUFFER  
TBL

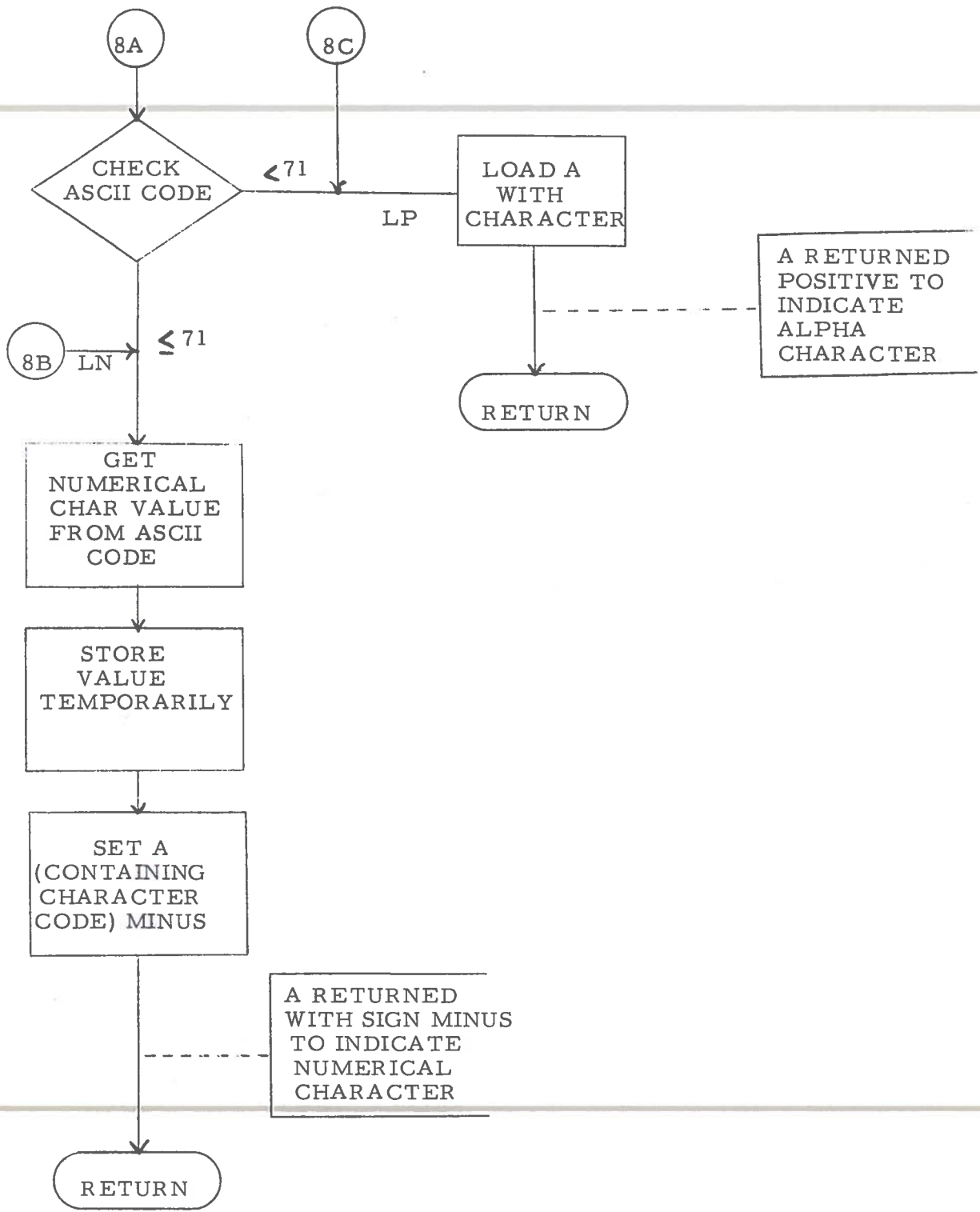
GET  
NEXT  
CHARACTER

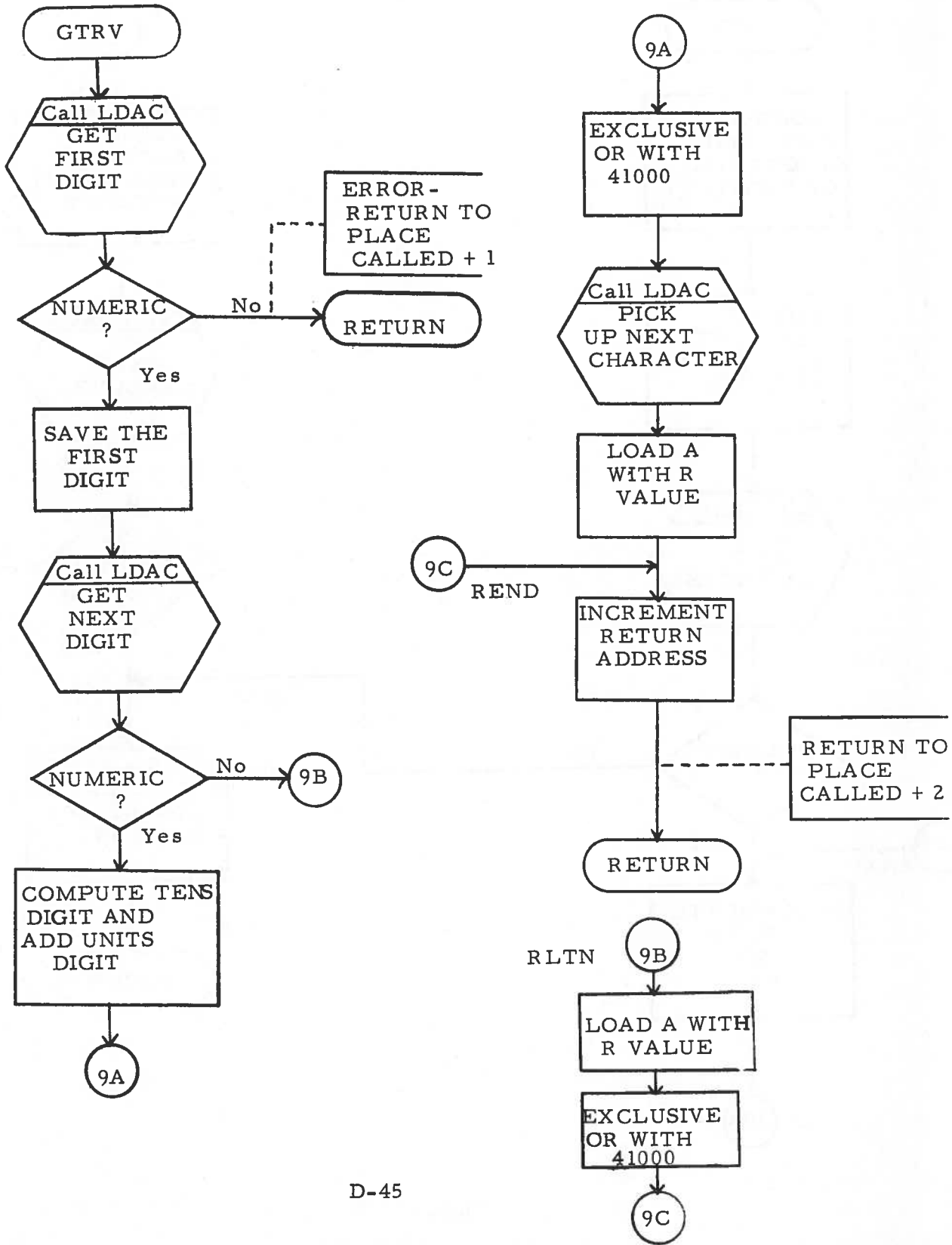




LDAC: Get Next Character

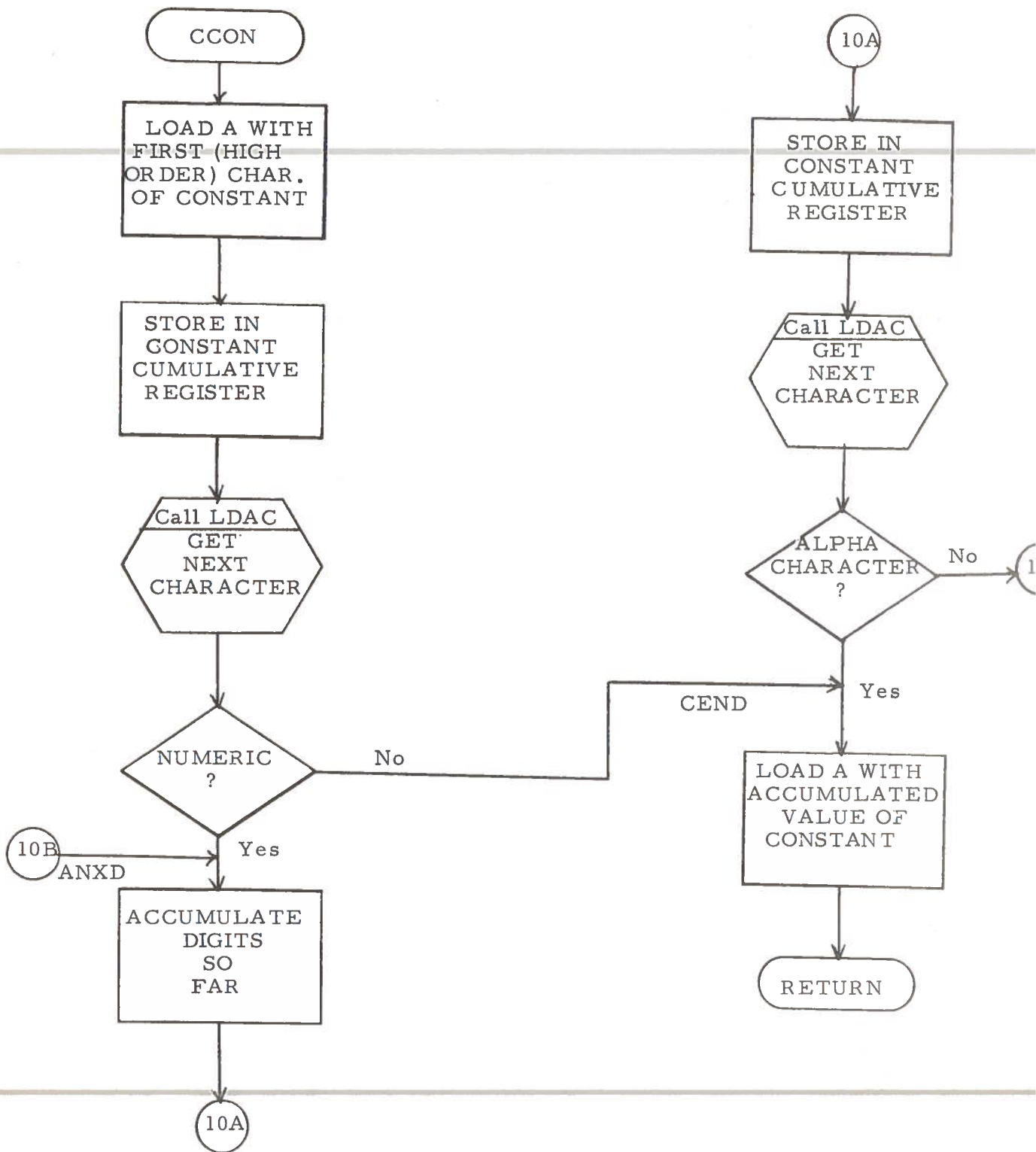






CCON: Accumulate Digits of Constant

SCOM



SCOM

OBJO

11A

SET MINIMUM  
NO. OBJECT  
CODE  
LOCATIONS  
REQUIRED TO 2

RESET  
POINTERS  
AND  
COUNTERS

SCNX

PICK UP THE  
NEXT ENTRY  
IN TYPE CODE  
BUFFER  
TABLE

INCREMENT  
COUNT BY NO.  
OF OBJECT  
CODE  
LOCATIONS  
AND TEMP.  
STORAGE  
LOCATIONS  
REQUIRED FOR  
THIS ENTRY

ADER

HAS  
ENTIRE  
TABLE BEEN  
SEARCHED  
?

11B

11B

INCREASE  
WORD COUNT  
IF SINGLE  
CONSTANT  
OR VARIABLE

NXSC  
CLBK

DETERMINE  
THE NUMBER  
OF BLOCKS (64  
WORDS EACH)  
STORAGE  
REQUIRED

GETS

IS  
BLOCK COUNT  
= 1  
?

Yes

12A

No

GETB

Call GETF  
GET A  
BLOCK OF  
STORAGE

11C

STORE  
BLOCK  
ADDRESS

11D

CLEAR  
A  
REGISTER

GNTB

Call GETF  
GET  
ANOTHER  
BLOCK

ARE  
BLOCKS  
WITHIN SAME  
SECTOR  
?

No

RJTB

SET NEW BLOCK  
AS FIRST ONE  
AND GET NEW  
SECOND BLOCK

11C

Yes

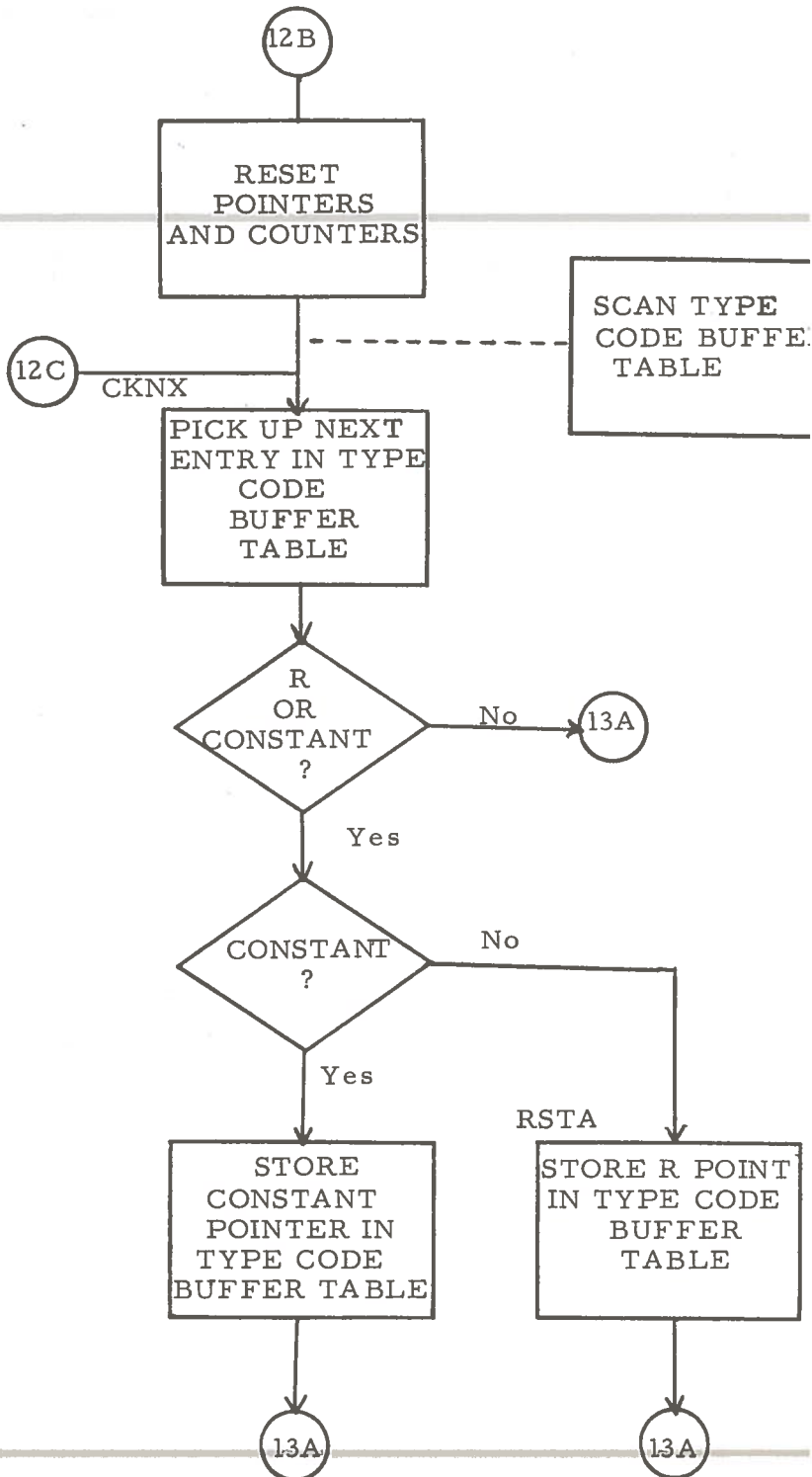
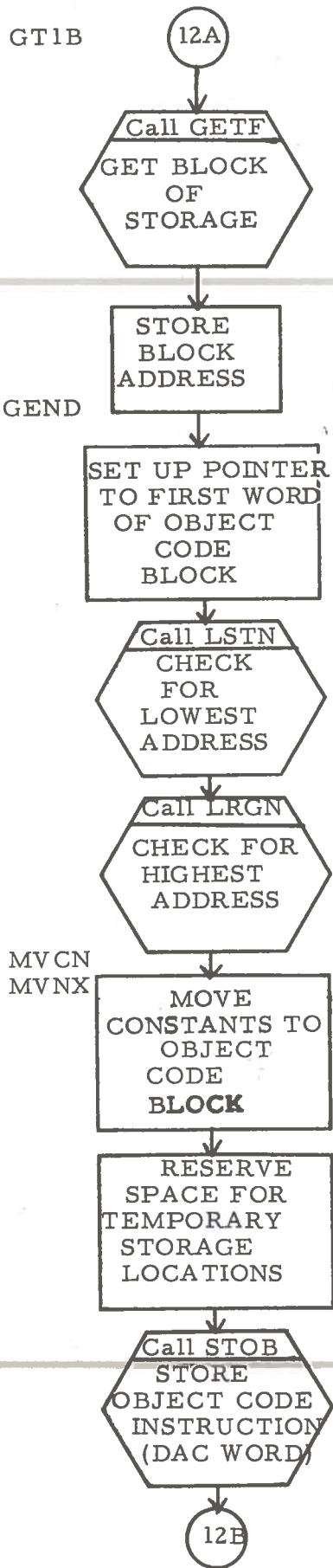
ARE  
BLOCKS  
CONTIG-  
UOUS  
?

No

Yes

11D

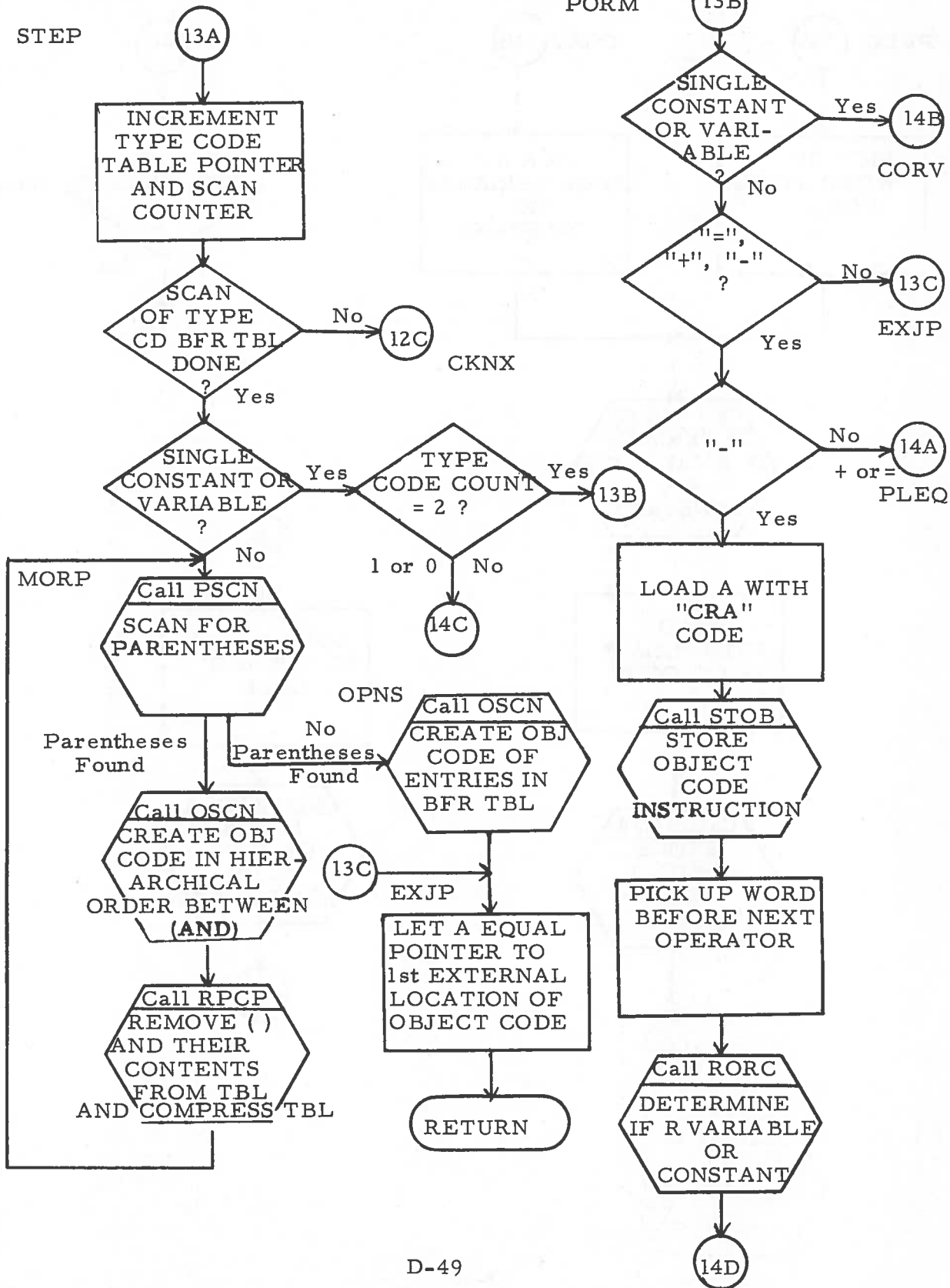


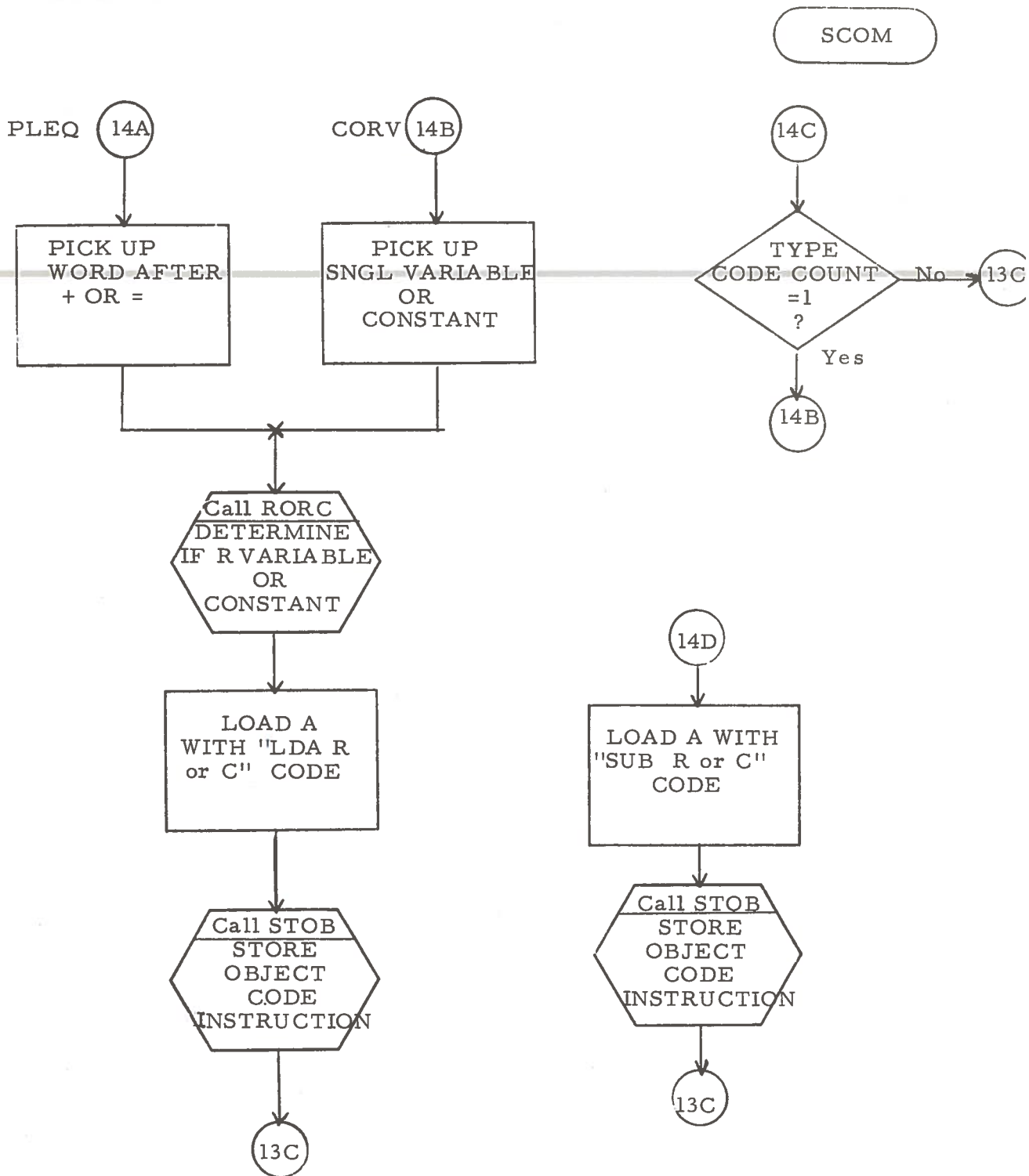


SCOM

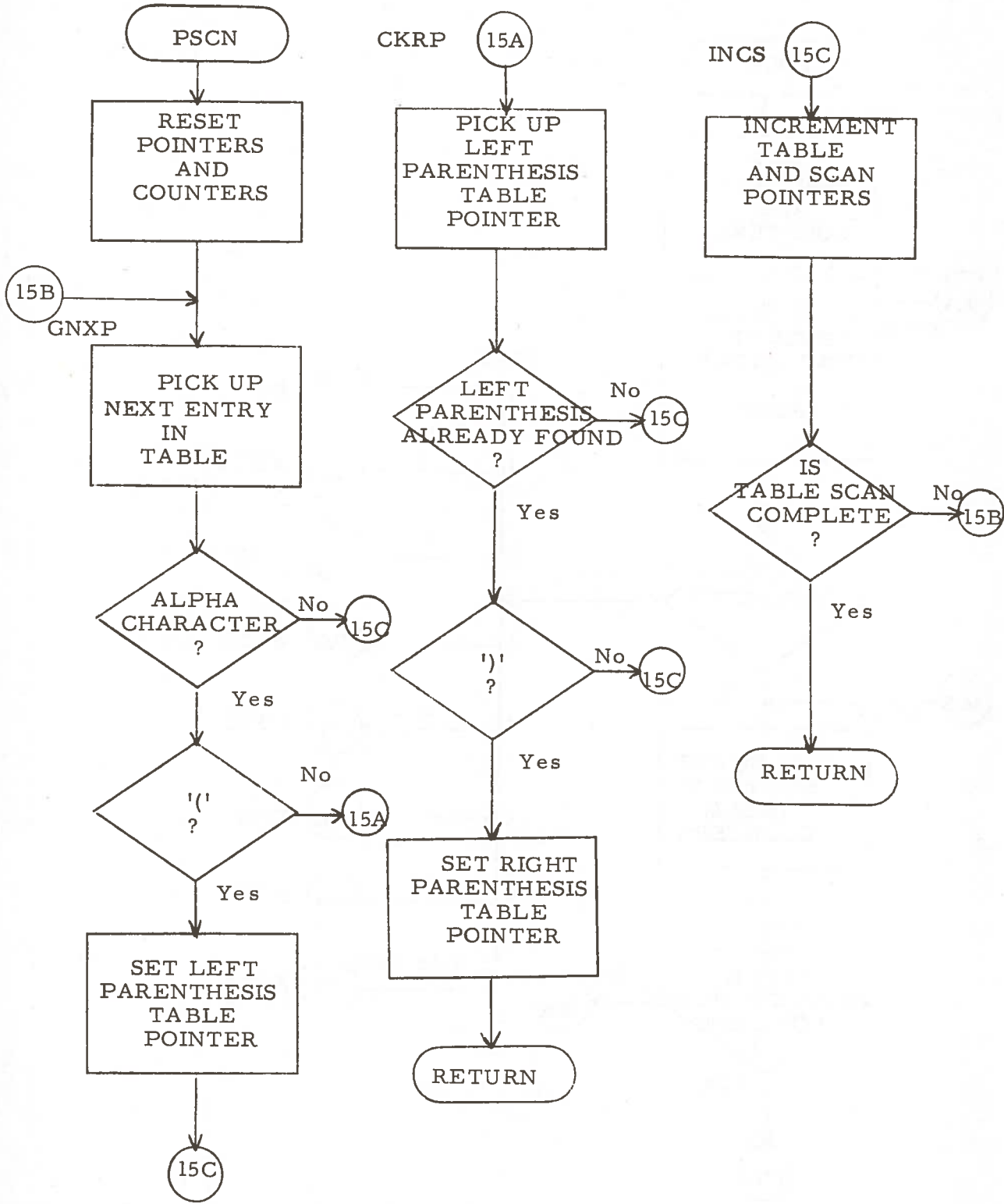
PORM

STEP



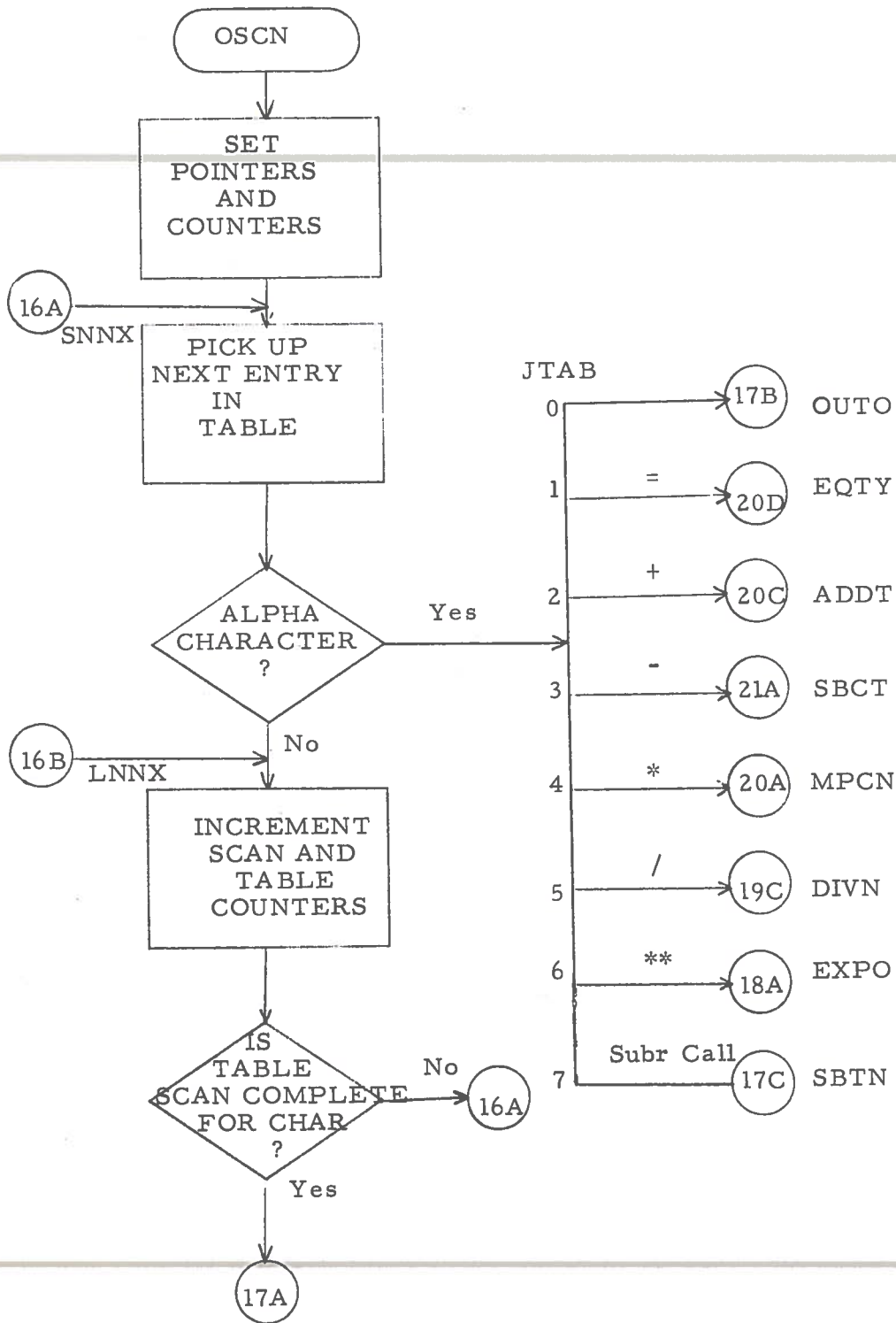


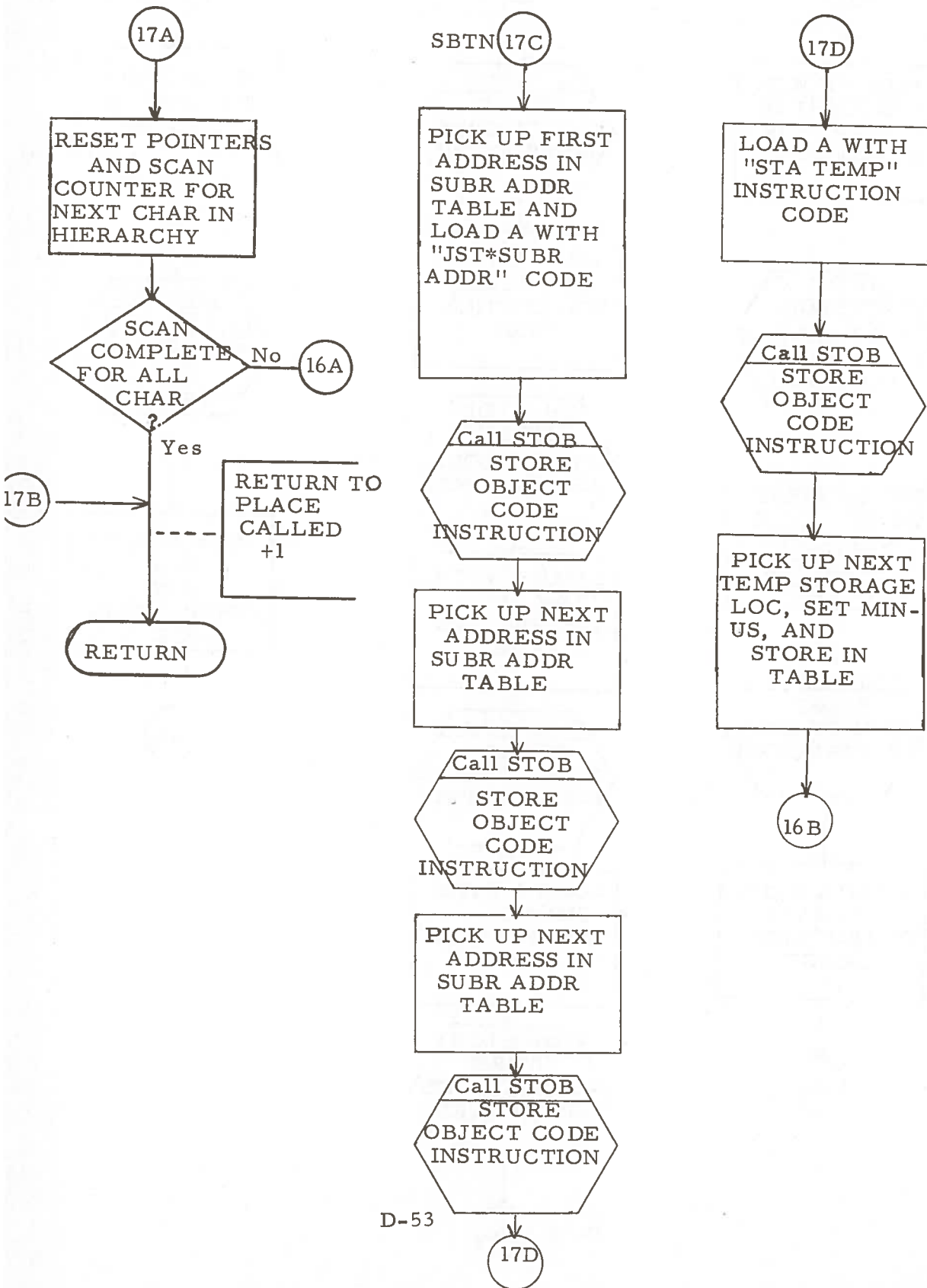
PSCN: Scan for Parentheses



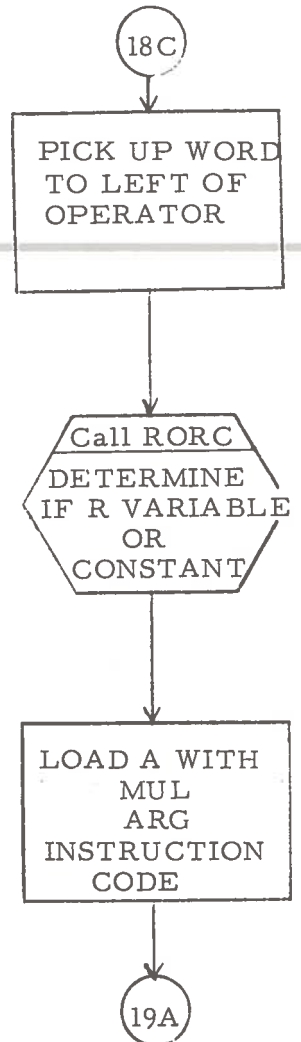
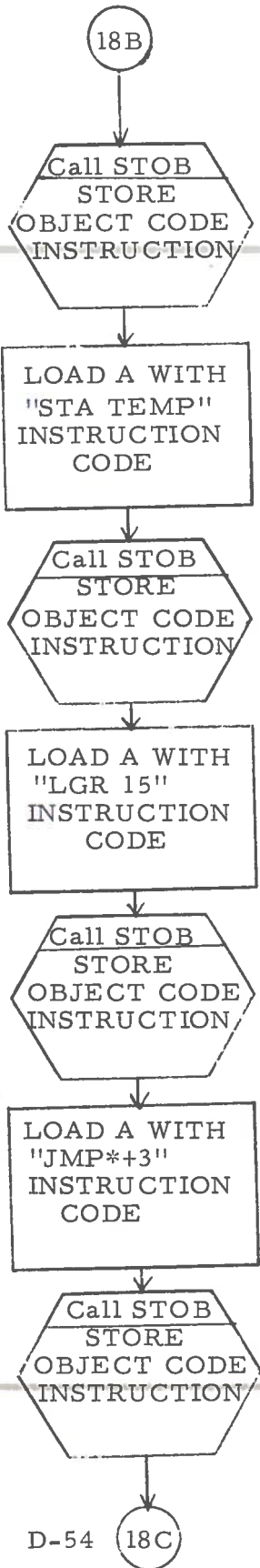
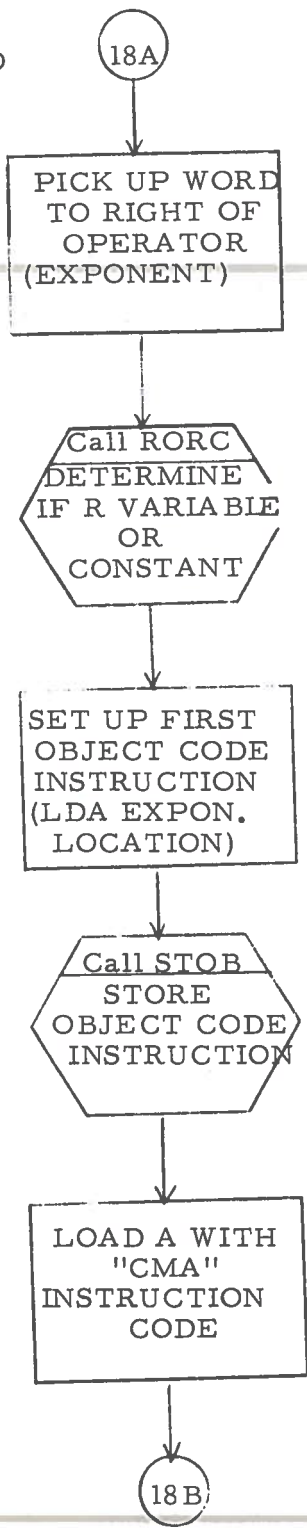
OSCN: Create Object Code in Hierarchical Order  
between Left and Right Parentheses

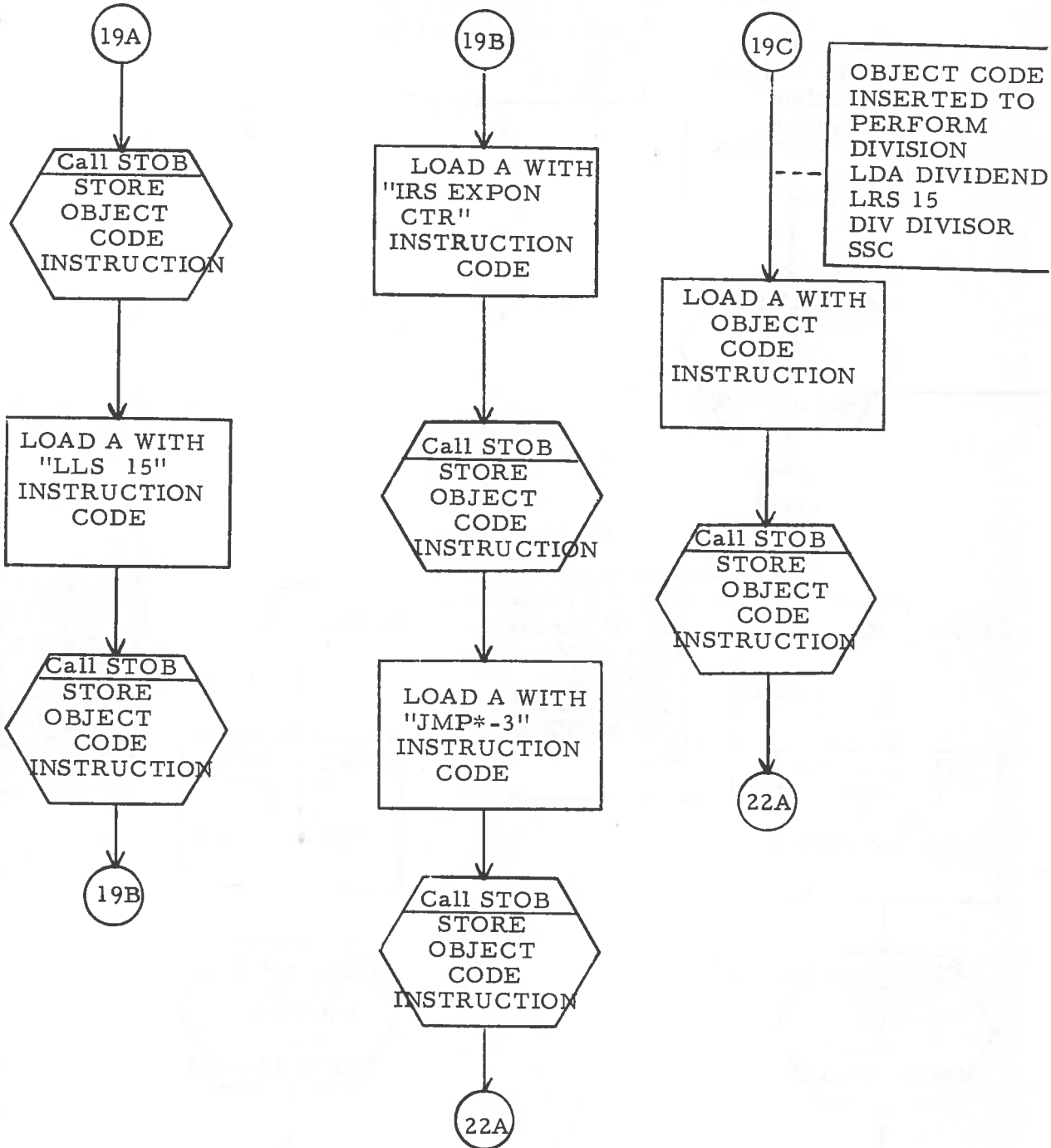
SCOM





EXPO







MPCN

20A

LOAD A WITH  
OBJECT  
CODE  
INSTRUCTION

OBJECT CODE  
INSERTED TO  
PERFORM  
MULTIPLICATION:  
LDA MULTIPLICAND  
MPY MULTIPLIER  
SSC  
LLS 15

Call STOB  
STORE  
OBJECT  
CODE  
INSTRUCTION

22A

ADDT

20C

LOAD A WITH  
OBJECT  
CODE  
INSTRUCTION

OBJECT CODE  
INSERTED TO  
PERFORM  
ADDITION:  
LDA AUGEND  
ADD ADDEND  
SSC

Call STOB  
STORE  
OBJECT  
CODE  
INSTRUCTION

22A

EQTY

20D

LOAD A WITH  
OBJECT  
CODE  
INSTRUCTION

OBJECT COD  
INSERTED TO  
PERFORM  
EQUALITY:  
LDA WORD  
AFTER =  
STA WORD  
BEFORE =

Call STOB  
STORE  
OBJECT  
CODE  
INSTRUCTION

22B

SCOM

SBCT

21A

PICK UP WORD  
TO LEFT OF  
OPERATOR

WORD  
= 0?

Yes

HANDLE UNARY  
MINUS  
EXPRESSION

LOAD A WITH  
"CRA"  
CODE

Call STOB  
STORE  
OBJECT  
CODE  
INSTRUCTION

21B

No

Call RORC  
DETERMINE  
IF R VARIABLE  
OR  
CONSTANT

LOAD A WITH  
"LDA MINUEND"  
CODE

Call STOB  
STORE  
OBJECT CODE  
INSTRUCTION

21B

UNRT

21B

PICK UP WORD  
TO RIGHT OF  
OPERATOR

Call RORC  
DETERMINE  
IF R VARIABLE  
OR  
CONSTANT

LOAD A WITH  
"SUB SUBTRA-  
HEND" CODE

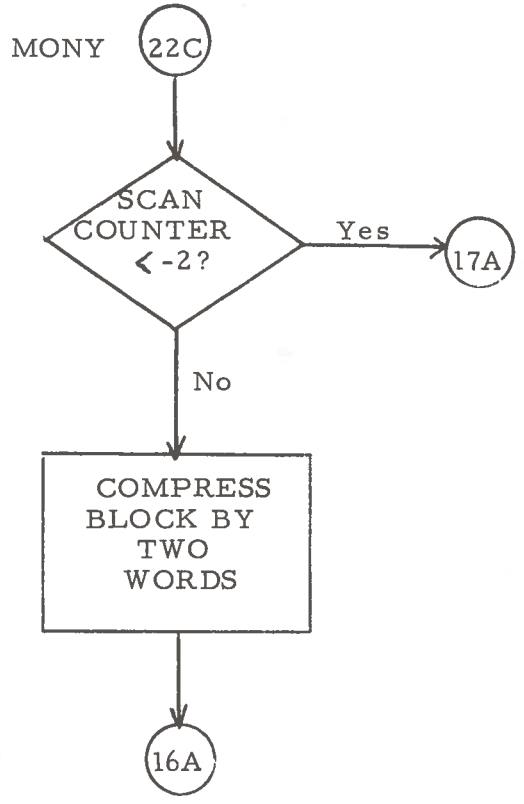
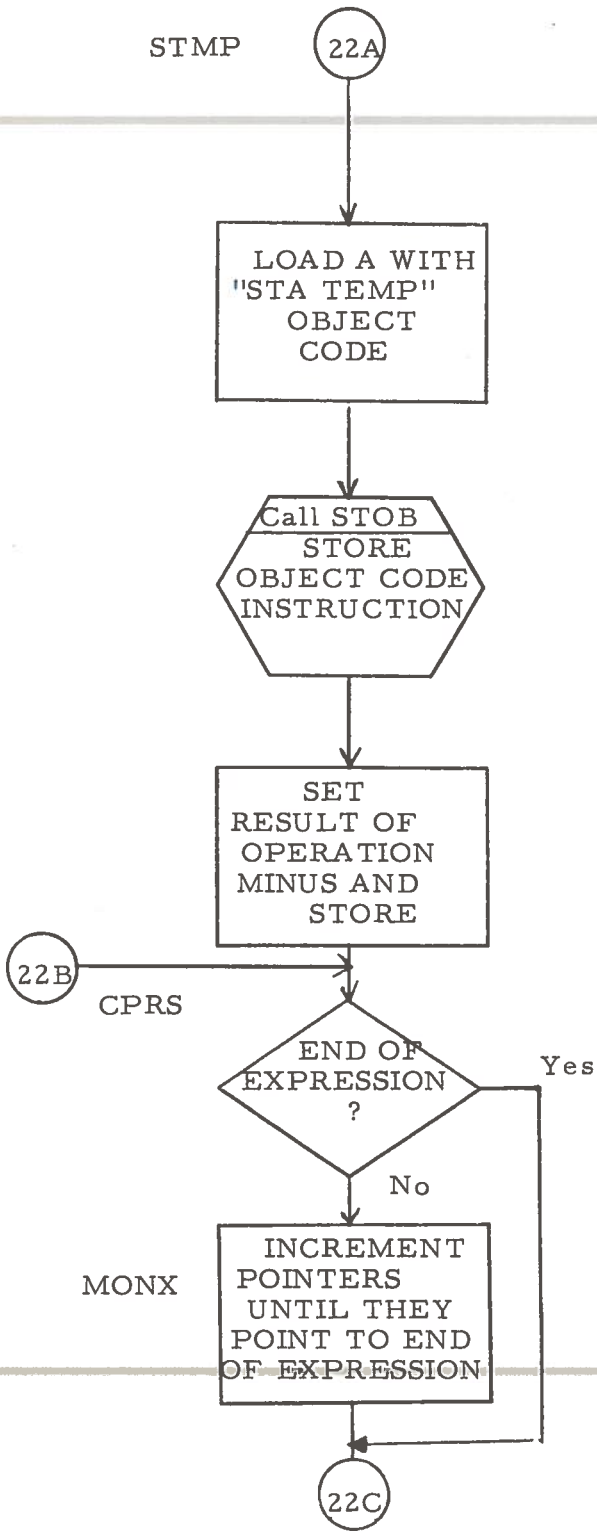
Call STOB  
STORE  
OBJECT  
CODE  
INSTRUCTION

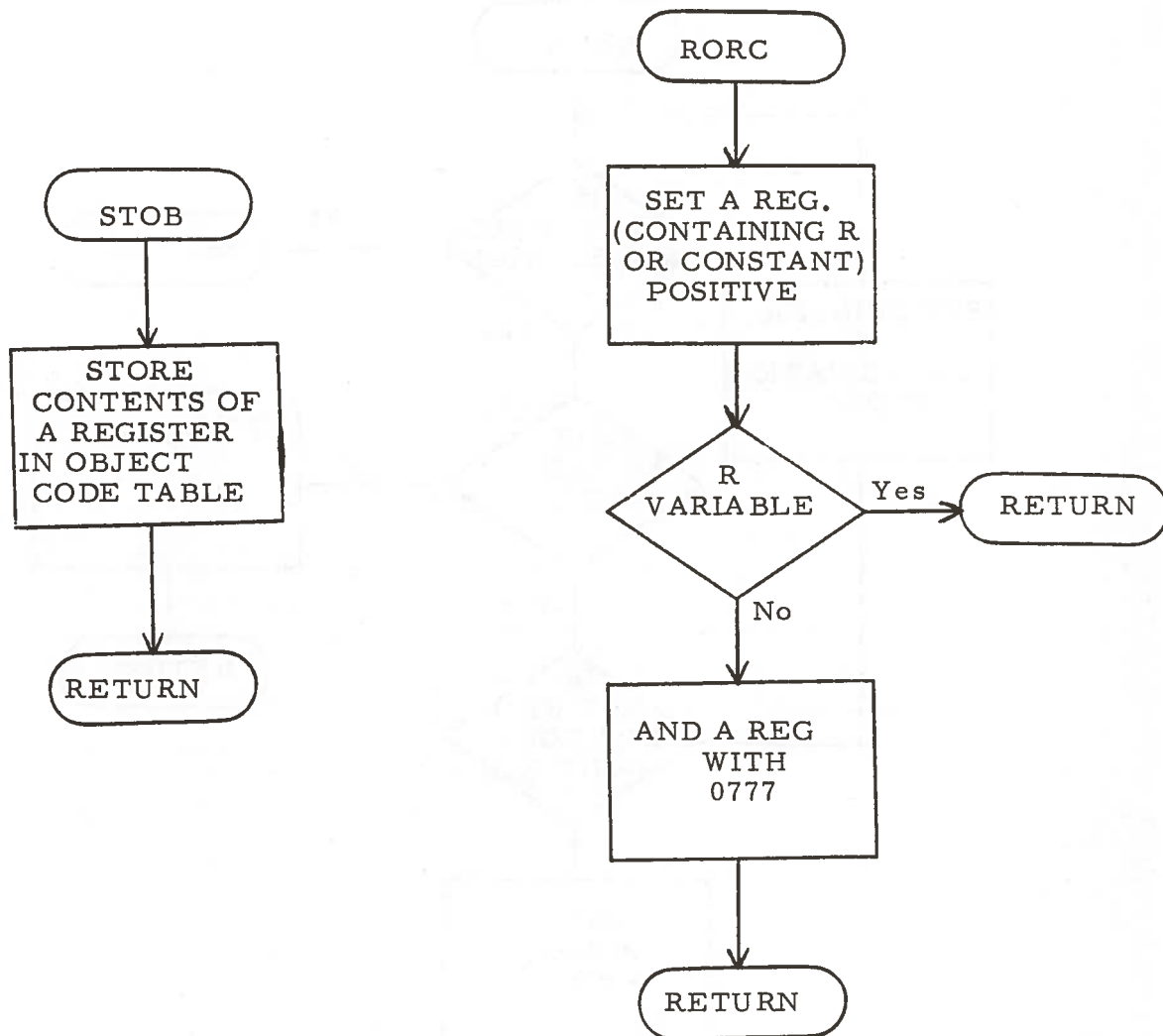
LOAD A WITH  
"SSC"  
CODE

Call STOB  
STORE  
OBJECT CODE  
INSTRUCTION

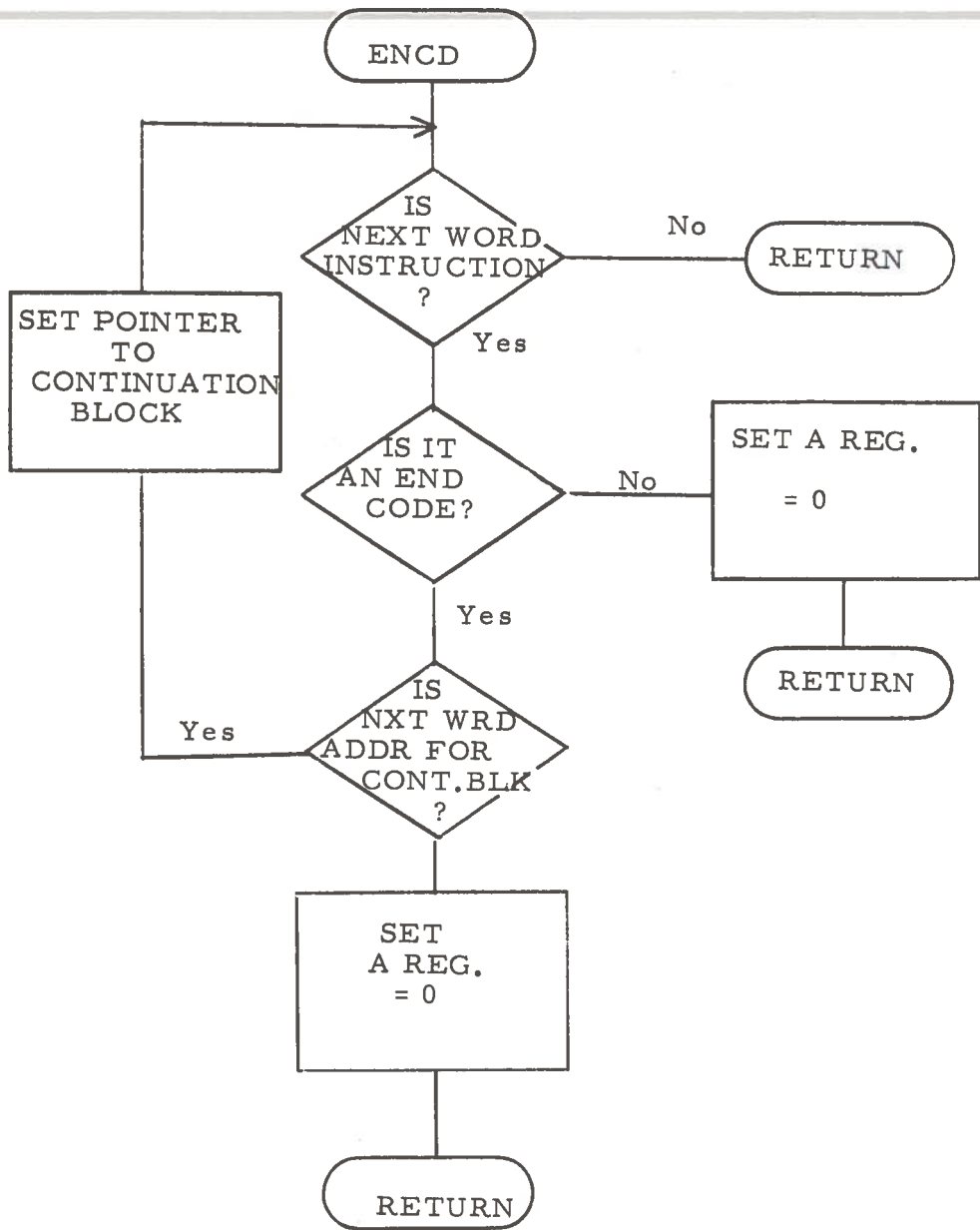
22A

STMP





ENCD: Checks each word for End Code and gets continuation block



SUBROUTINE:

STEX

Storage Exceeded

1. PURPOSE:

Type storage exceeded message

2. CALLING SEQUENCE:

CALL STEX

3. INPUT:

None

4. OUTPUT:

Storage exceeded message on teletype

5. ACTION:

Types message. Returns to calling program.

6. EXTERNAL REFERENCES:

TAO\$

7. CORE USED:

24<sub>8</sub>

STEX



SUBROUTINE:

TAII

Teletype ASCII Input - 1 Character

1. PURPOSE:

Inputs 1 character from teletype

2. CALLING SEQUENCE:

CALL TAI1

3. INPUT:

One character from teletype (no characters or control input rejected)

4. OUTPUT:

Input character right-adjusted in A-register.

5. ACTION:

Enables teletype for input. Waits for character.  
Returns after character input.

6. EXTERNAL REFERENCES:

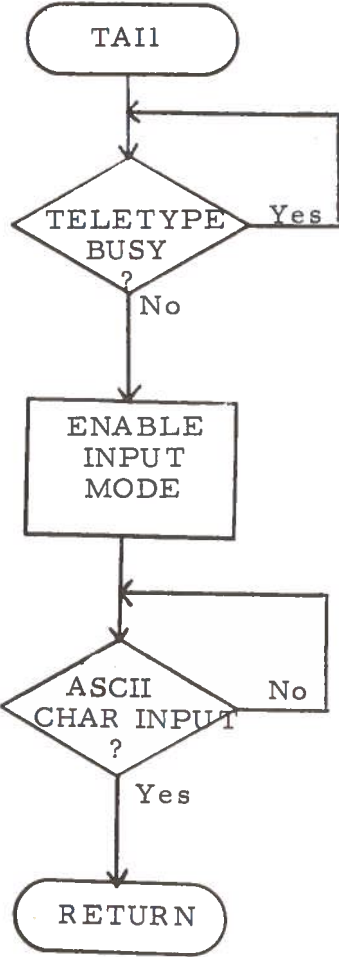
None

7. CORE USED:

7



TAI1



SUBROUTINE:

TAI\$

1. PURPOSE:

Monitors teletype input of up to 70 characters into given buffer

2. CALLING SEQUENCE:

CALL        TAI\$  
DAC        BUFL        BUFL = location of 35 word buffer

3. INPUT:

Up to 70 characters terminating with but including carriage return from teletype

4. OUTPUT:

- a) characters in given buffer
- b) number of characters input in A-register

5. ACTION:

After initialization, characters are accepted from the keyboard one at a time. The "rubout" character will cause the previously input character to be deleted from the buffer, the pointers to back up, and the typing of a reverse arrow. The program is then ready to accept another character. Input of a control X will cause a reverse arrow followed by an "X" to be typed. Control will then be returned to the calling program with a zero in the A-register to indicate the entire input buffer is invalid.

After 64 characters have been put in the buffer a bell is sounded to indicate a near full buffer. If an attempt is made to put more than 70 characters in the buffer, an error message is printed and control is returned to the calling program with a zero in the A-register.

NOTE: The "rubout" character deletes characters from the buffer,

and decrements char count).

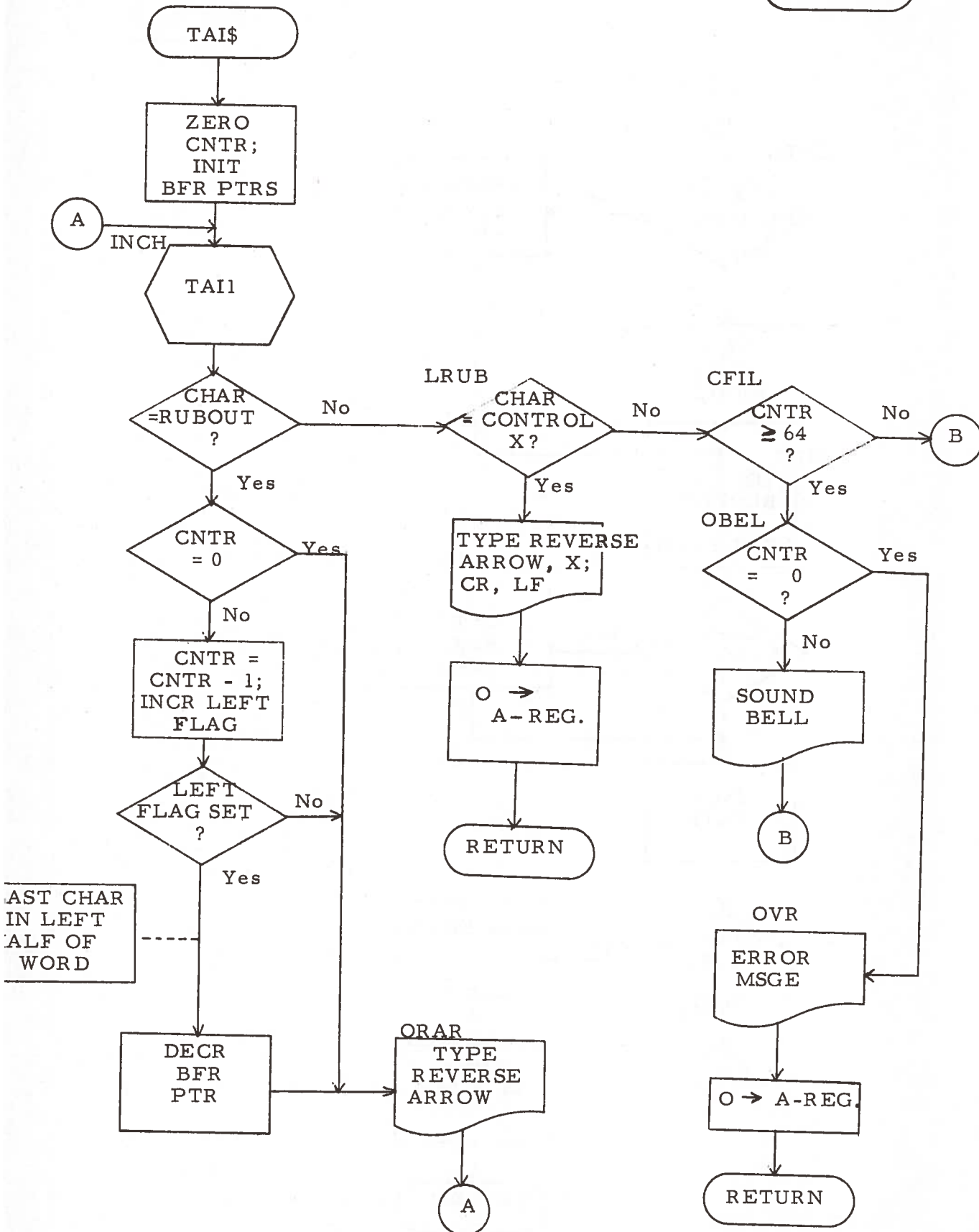
A carriage return will be entered in the buffer and will cause control to be returned to the calling program with the number of input characters in the A-register. Input characters will be packed two to a word in the given buffer. An odd character will be left-adjusted.

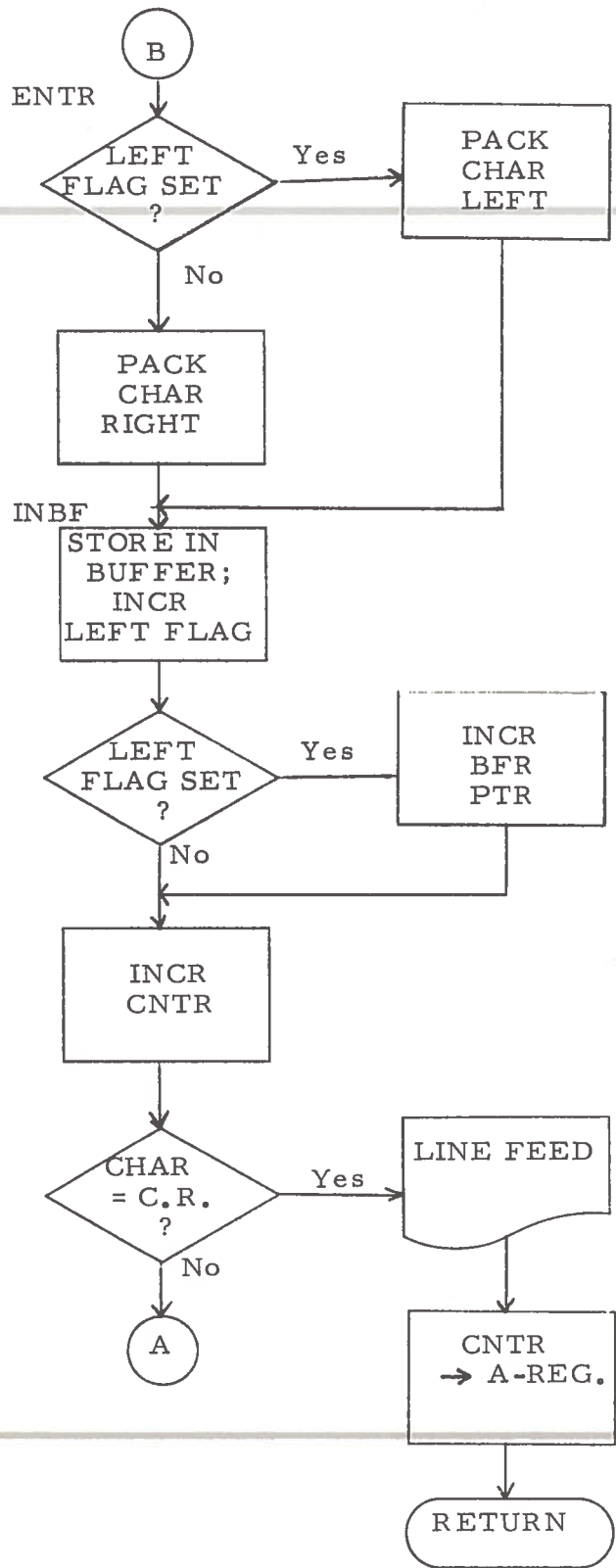
6. EXTERNAL REFERENCES:

TAI1  
TAO1  
TAO\$

7. CORE USED:

167<sub>8</sub>





SUBROUTINE:

TAO1

Teletype ASCII output - 1 character

1. PURPOSE:

Outputs given ASCII character on teletype

2. CALLING SEQUENCE:

```
LDA    CHAR
CALL   TAO1
```

3. INPUT:

One ASCII character right-adjusted in A-register.

4. OUTPUT:

Character is typed on teletype

5. ACTION:

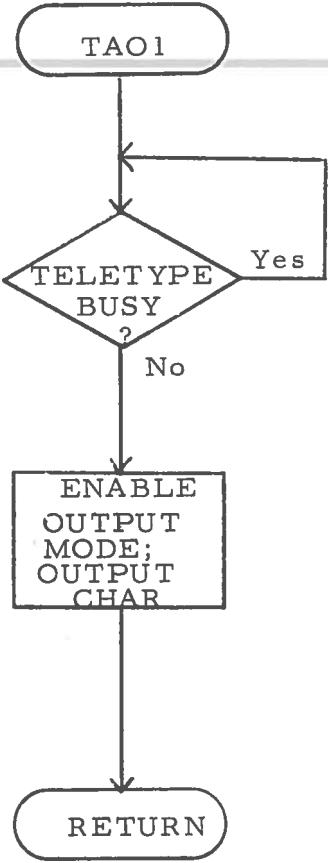
Teletype is enabled for output. Character is output.  
Return is made to calling routine.

6. EXTERNAL REFERENCES:

None

7. CORE USED:

7



SUBROUTINE:

TAO\$

Teletype ASCII output - given buffer

1. PURPOSE:

Outputs characters stored in given buffer

2. CALLING SEQUENCE:

```
LDA      N
TCA      / (optional, if N neg, CR & LF typed)
CALL     TAO$
DAC      BUF
```

3. INPUT:

N Number of characters to be output  
BUF Location of first word of buffer containing  
 characters for output.

NOTE:

If the A-register is negative on entry to TAO\$, it is assumed to be the two's complement of the number of characters, N, and a carriage return and line feed are generated after the buffer is outputted. If the A-register is positive on entry, it is two's complemented by TAO\$, and set to count the number of characters, and a carriage return is not generated after the buffer is outputted.

4. OUTPUT:

The characters in the buffer are output on the teletype, optionally followed by a carriage return and line feed.

5. ACTION:

A flag is set according to condition of A-register on entry to indicate whether a carriage return should follow the output. The



character count is set and the pointer and half word indicator are initialized.

A character from the buffer is outputted and the counter and half word indicator are incremented. If the half word indicator now designates a left-adjusted character, the output pointer is also incremented.

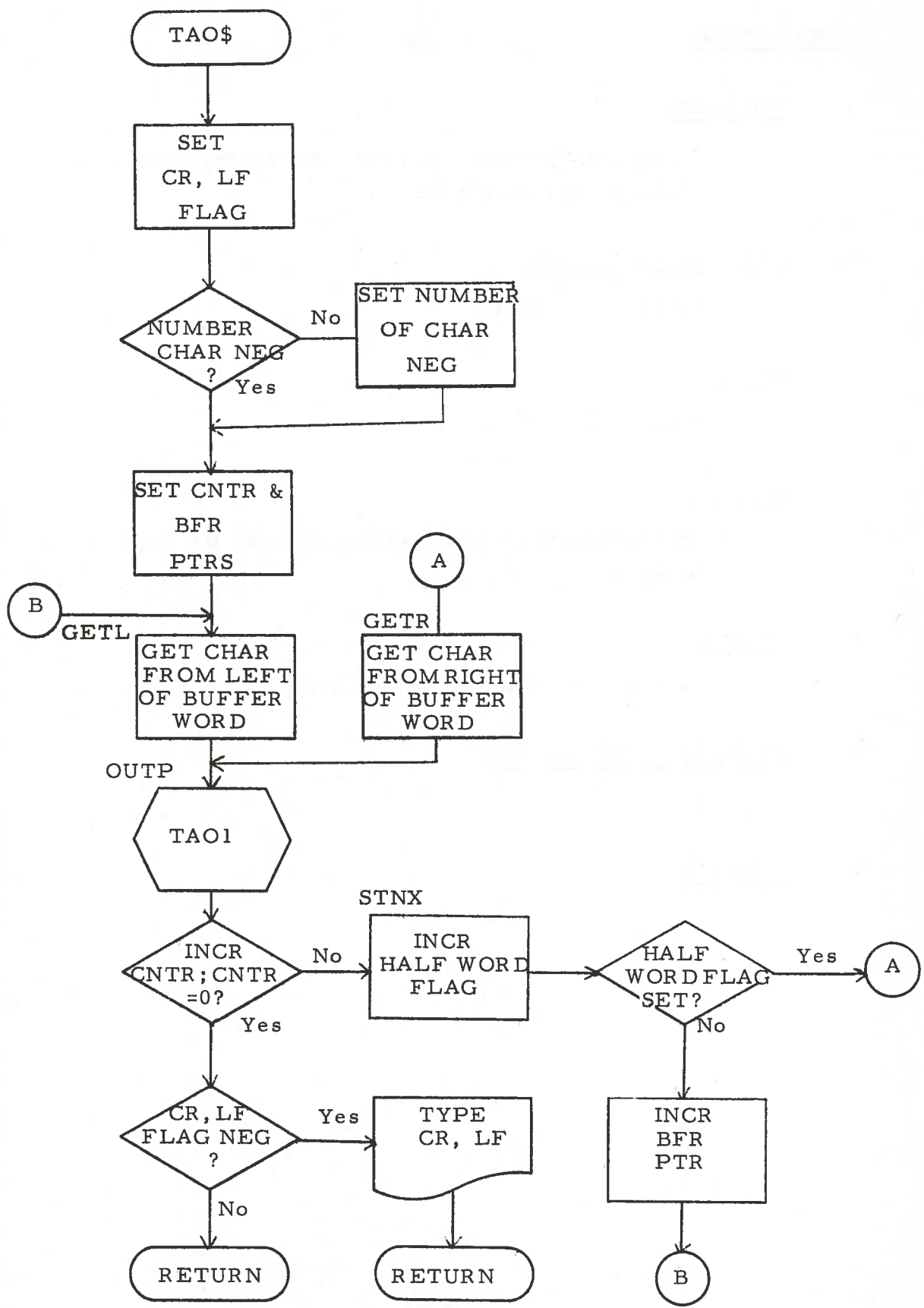
When all characters have been typed, the flag is checked to determine if a carriage return should be output. Then control is returned to the calling program.

6. EXTERNAL REFERENCES:

TAO1

7. CORE USED:

46<sub>8</sub>



SUBROUTINE:

WHAT

1. PURPOSE:

Types question mark indicating that calling routine was unable to interpret input.

2. CALLING SEQUENCE:

CALL WHAT

3. INPUT:

None

4. OUTPUT:

Outputs question mark, carriage return, line feed on teletype

5. ACTION:

Outputs ?, C.R., L.F. on teletype.

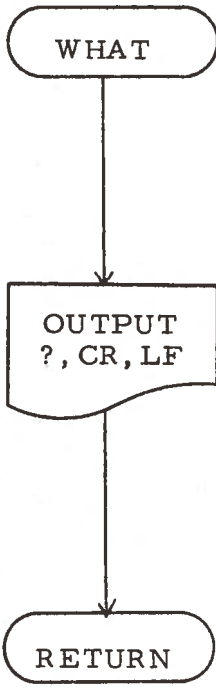
6. EXTERNAL REFERENCES:

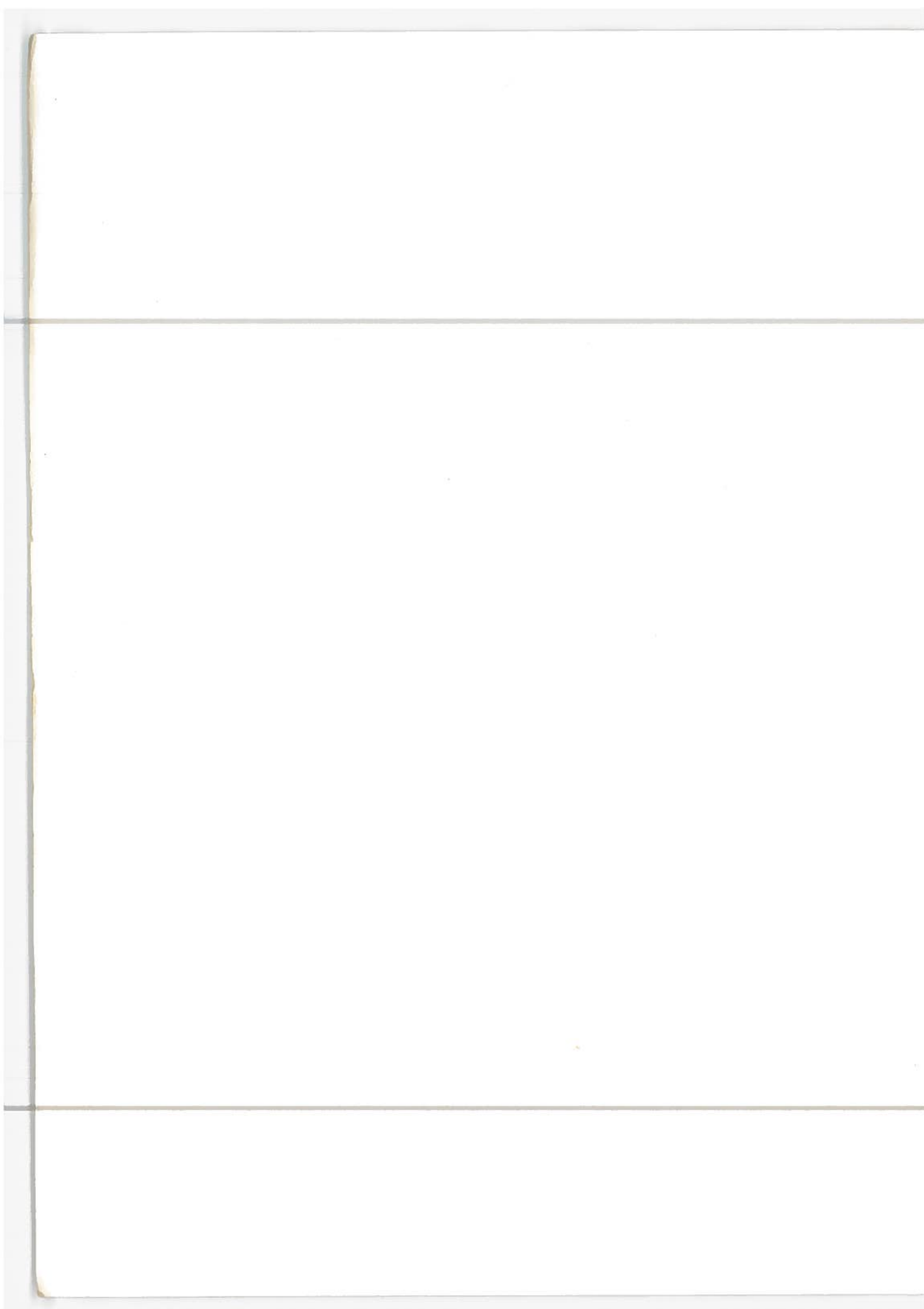
TAO\$

7. CORE USED:

10<sub>8</sub>

WHAT





## MONITOR

### 1. PURPOSE

Handles all initialization, reading of parameters, inputting and displaying the data structure from disc, servicing both photo pen and real time clock interrupts and providing linkage to all routines necessary for the required simulation.

### 2. CALLING SEQUENCE

NONE (Main program)

### 3. INPUT

Initial variables from the ASR-35:

- a) Data Structure File Name
- b) Real Time Clock Interrupt Cycle Time
- d) Poll Display Generator
- e) Poll PDP-10
- f) Poll Pilot Control Station

### 4. OUTPUT

- a) Display of data structure called in from disc
- b) Type out requests on the A SR-35
- c) Error Messages on the ASR-35

### 5. ACTION:

After initialization, the monitor requests user input information from the teletype concerning disc file name, update time interval and devices to be polled (See User Guide for type in examples).

The data structure is read in from disc via the Disc Operating System (DOS) and the first frame on the frame ring is displayed.

The monitor then calls the simulate subroutines in a following sequence;

- a) Device Polling Subroutines if polling is requested
- b) Math Model - update registers RO-R99
- c) Perspective - not yet implemented
- d) Update - update data structure
- e) Background Program

If any of the above subroutines are not present at run time the call will be a dummy subroutine. The background program may retain control on return to the monitor waiting for a real time clock interrupt.

The clock interrupt starts the update cycle. If it occurs before the completion of a full update cycle, an appropriate error message is typed indicating where the time out occurred.

The monitor processes photo pen interrupts by calling the user photo pen interrupt routine, then returning to the interrupted routine for completion of the update cycle.

## 6. EXTERNAL REFERENCES

- PP - photo pen
- PSD - pen switch depressed
- AN - ASCII control keyboard
- FND - function key depress
- PSR - pen switch release
- FNR - function key release

GC - graphic copy device

X - extra devices

PD10

PPI - photo pen interrupt

RESTORE - restore file (DOS)

TNOUA - type out character array (DOS)

TIIN - read character from teletype (DOS)

MODL- math model

PERS-perspective

UPDT -update

BKGRND-background program

IREG - start address of 100 word register (RO-R99)

PP, PSD, AN, FND, PSR, FNR, GC, and X are called when polling the display generator. The function register contains a one in the bit position related to the external reference by the same title (Refer to the ADDS-900 programmers reference manual).

The calling sequence to these routines is:

JST\* JMPT + N

return

⋮

JMPT+N XAC prog name (PP, PSD, AN, FND, etc.)

The calling sequence for polling the PDP-10 is:

JST\* JMPT +8

return

JMPT+8 XAC PD10



The calling sequence for a photo pen interrupt is:

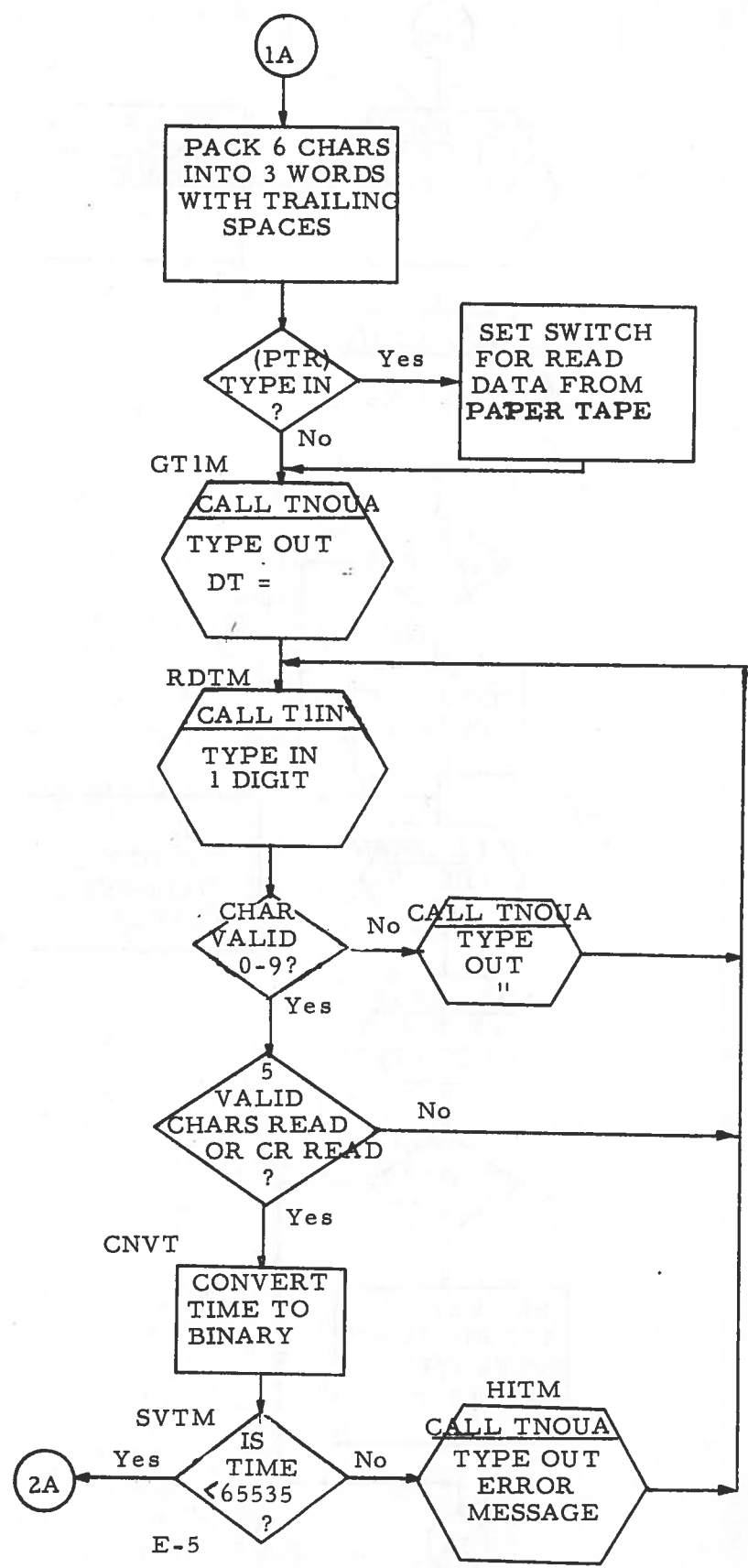
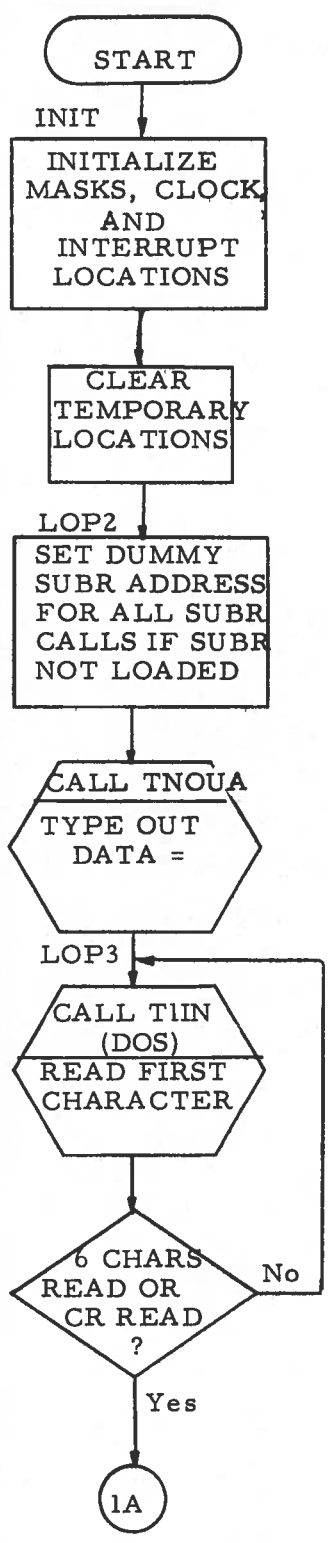
```
JST*  JMPT+9
return
.
.
.
JMPT+9  XAC  PPI
```

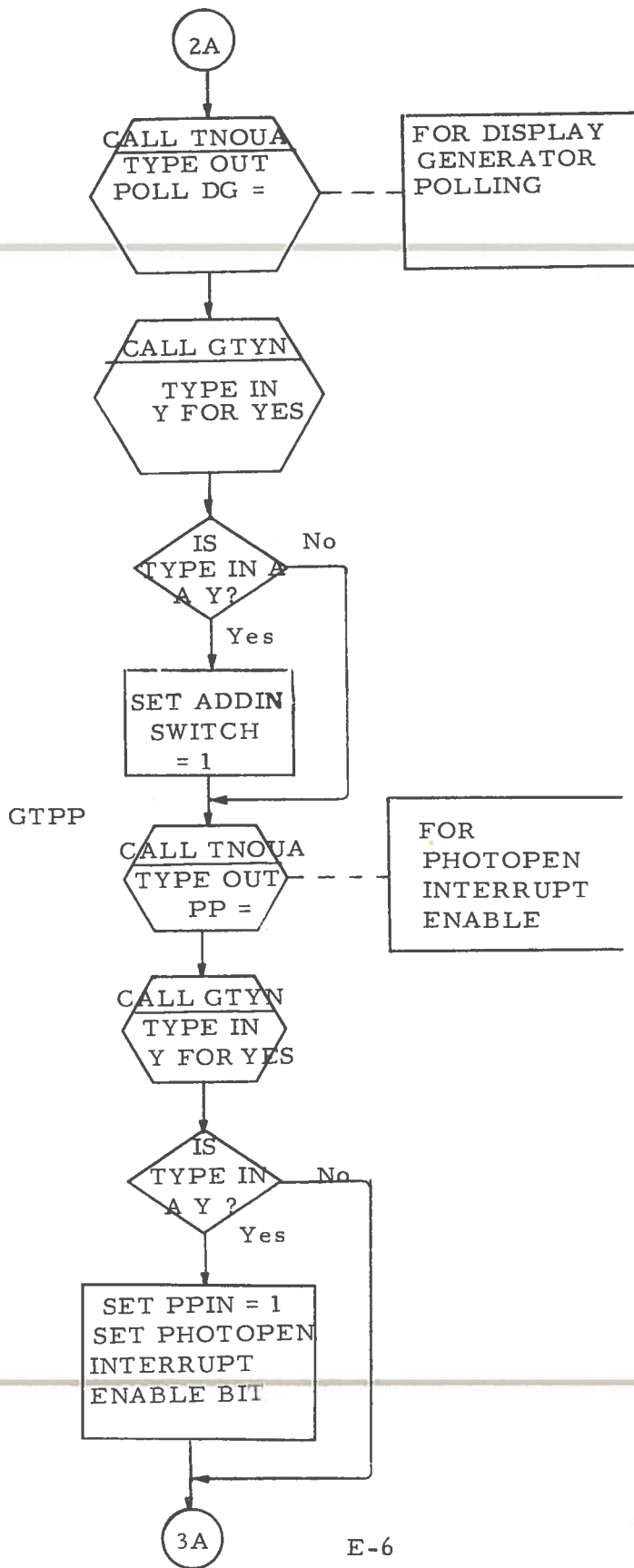
The calling sequence to the math model is

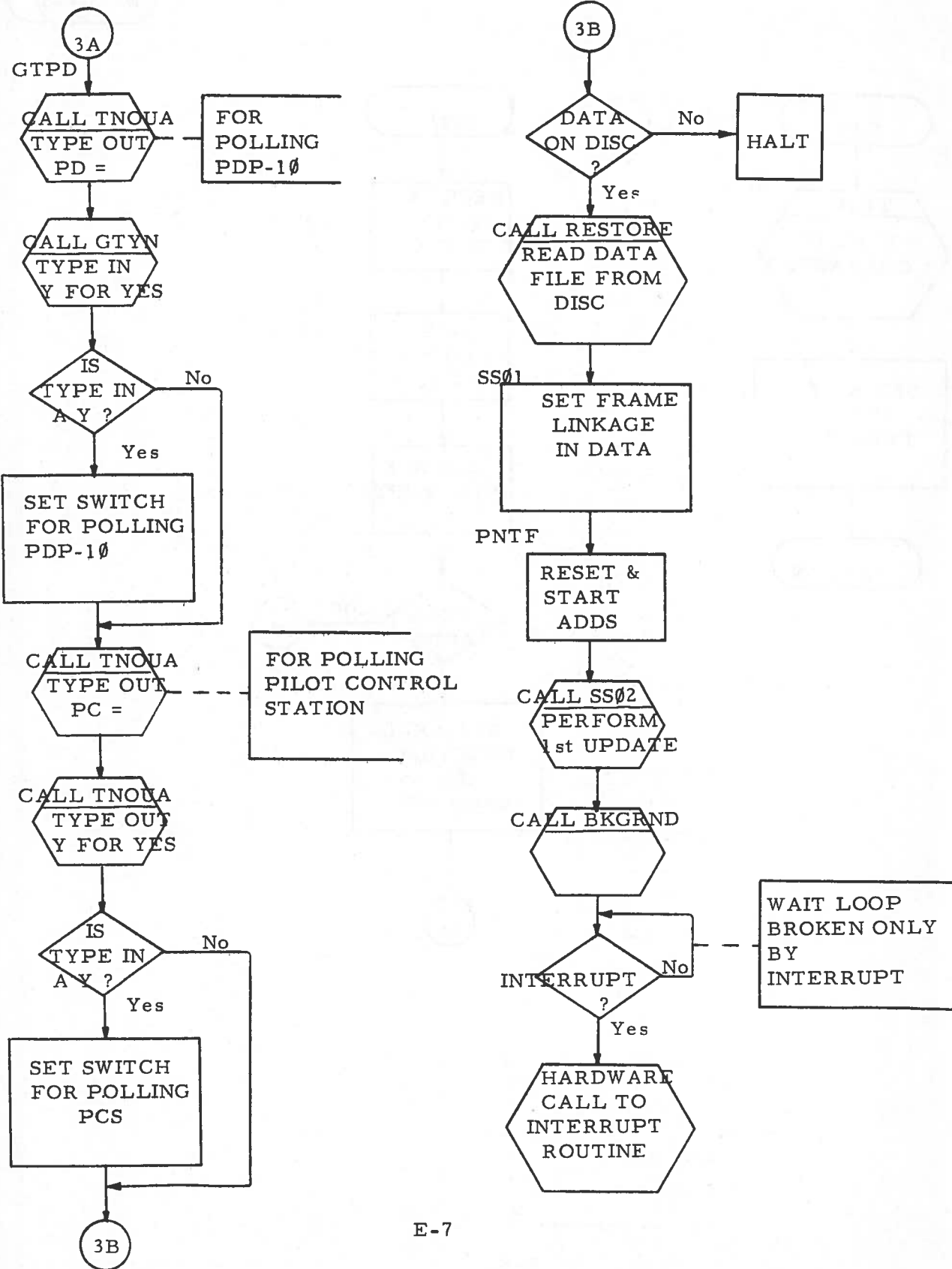
```
JST*  JMPT+13
DAC
return
.
.
JMPT+13  XAC  MODL
```

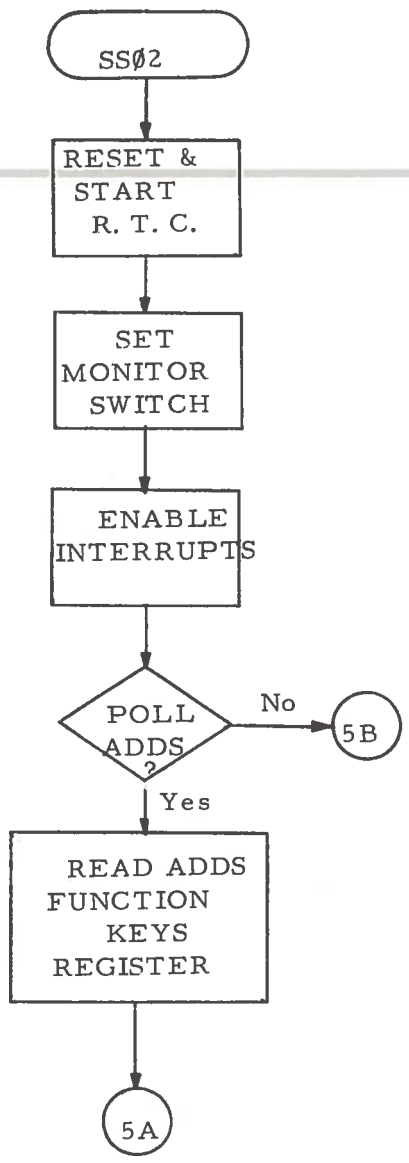
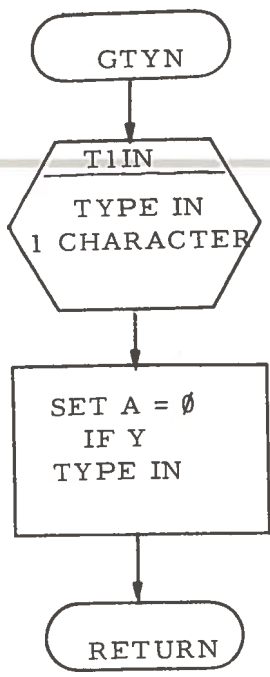
Where:	MPAR+0	Real Time Interrupt internal 0 for 1st entry
	MPAR+1	Not used
	MPAR+2	Frame ID
	MPAR+3	XJOY
	MPAR+4	YJOY
	MPAR+5	THRT
	MPAR+6	LRUD
	MPAR+7	RRUD
	MPAR+8	Not used
	MPAR+9	Not used

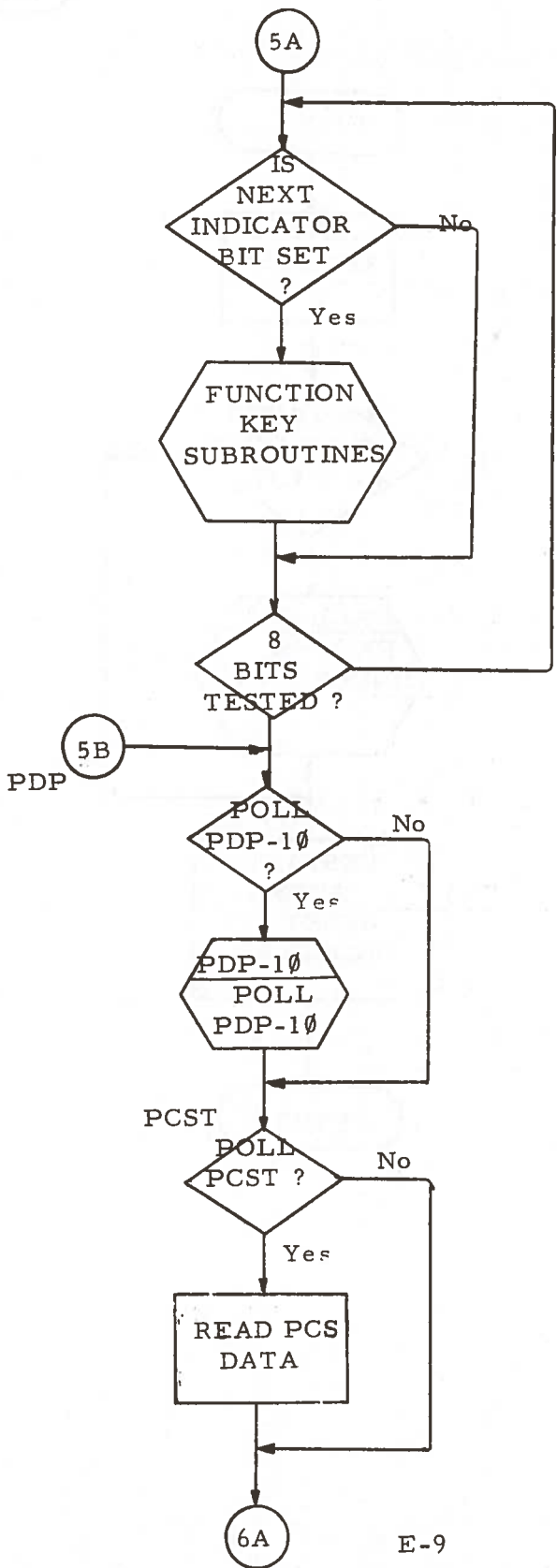
7. CORE USED  
1115<sub>8</sub>

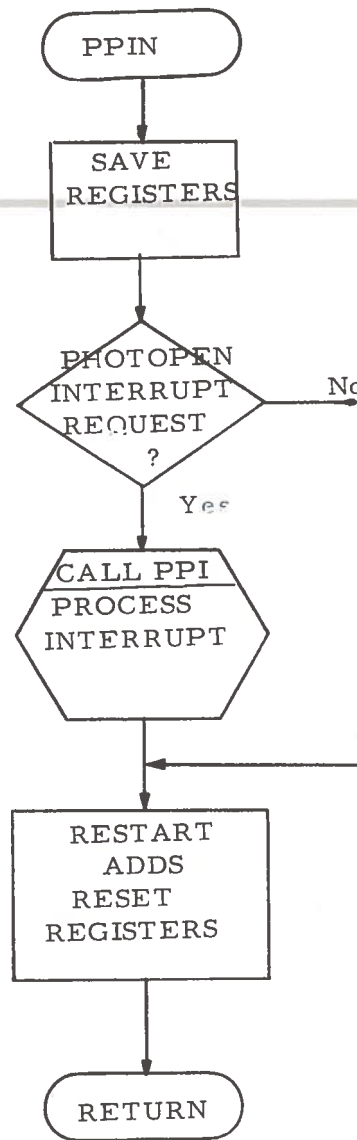
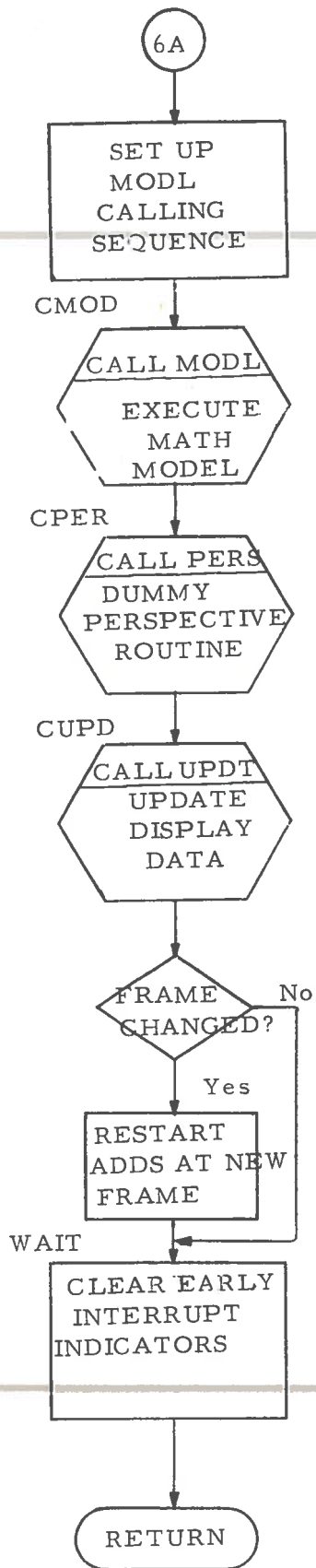


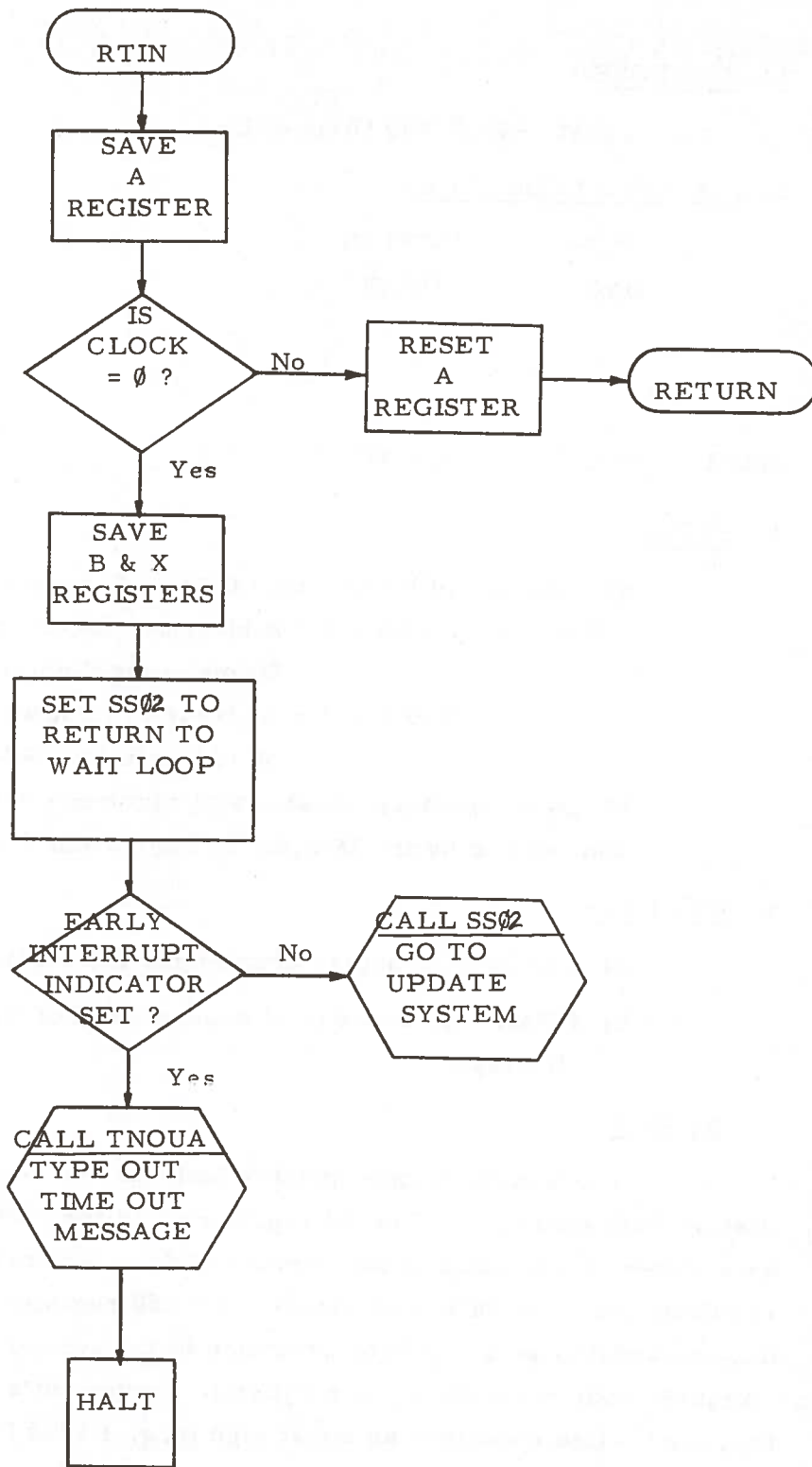














## SUBROUTINE

## UPDT

1. PURPOSE:

Update ADDS/900 Display List

2. CALLING SEQUENCE:

JST*	JMPT+N
DAC	UPAR
.	
.	
.	
JMPT+N XAC	UPDT

3. INPUT:

a) Address of UPAR from Calling Sequence

Where:  $UPAR + 0 =$  Address of second word of  
frame being displayed

$UPAR + 1 =$  Address of first word of block  
of 100 registers  $R0$  thru  $R99$ .

b) Data structure created and saved in the Create Phase  
and read in by the Monitor in Simulation Phase.

4. OUTPUT:

a) The data structure updated for the frame to be displayed.

b)  $UPAR + 0$ , address of second word of frame to be  
displayed.

5. ACTION:

The update routine updates both the 100 registers and the display data structure. The 90 registers  $R10$  thru  $R99$  are updated as a result of execution of the appropriate mini-compiled code resulting from the DRE statements. All 100 registers  $R0$  thru  $R99$  may be updated as a result of execution of the appropriate mini-compiled code resulting from a dynamic component's expression if that expression contained an equal sign (e. g.  $L100\# R0 = R90 + 5$ )

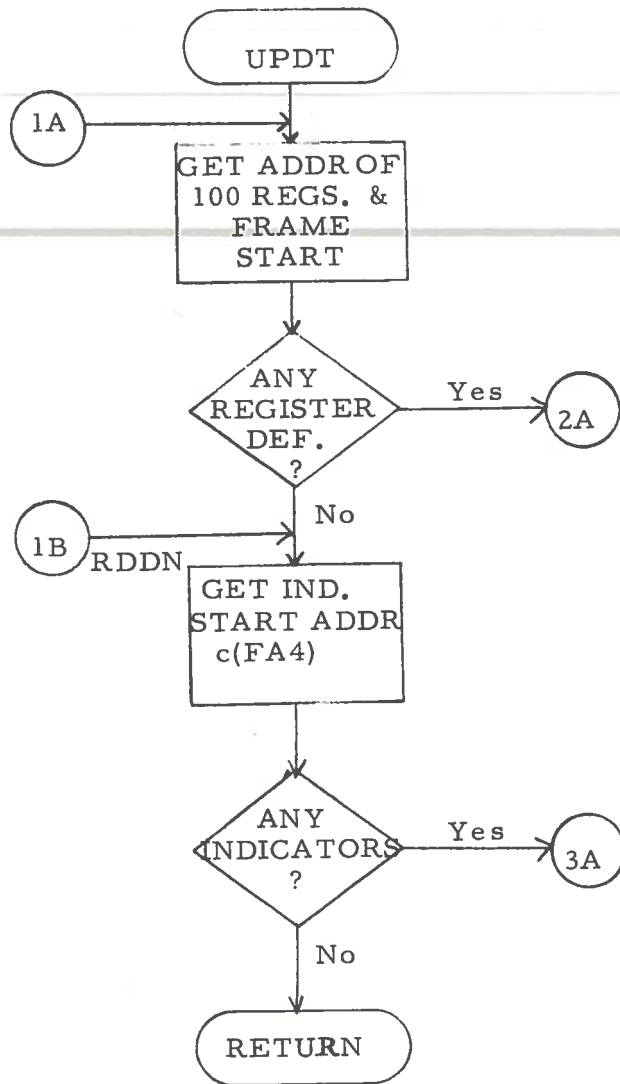
The display data structure is updated as a result of the execution of the conditioning statements at both the indicator and entity levels, and by execution of the appropriate mini-compiled code resulting from dynamic component's expression at the entity level.

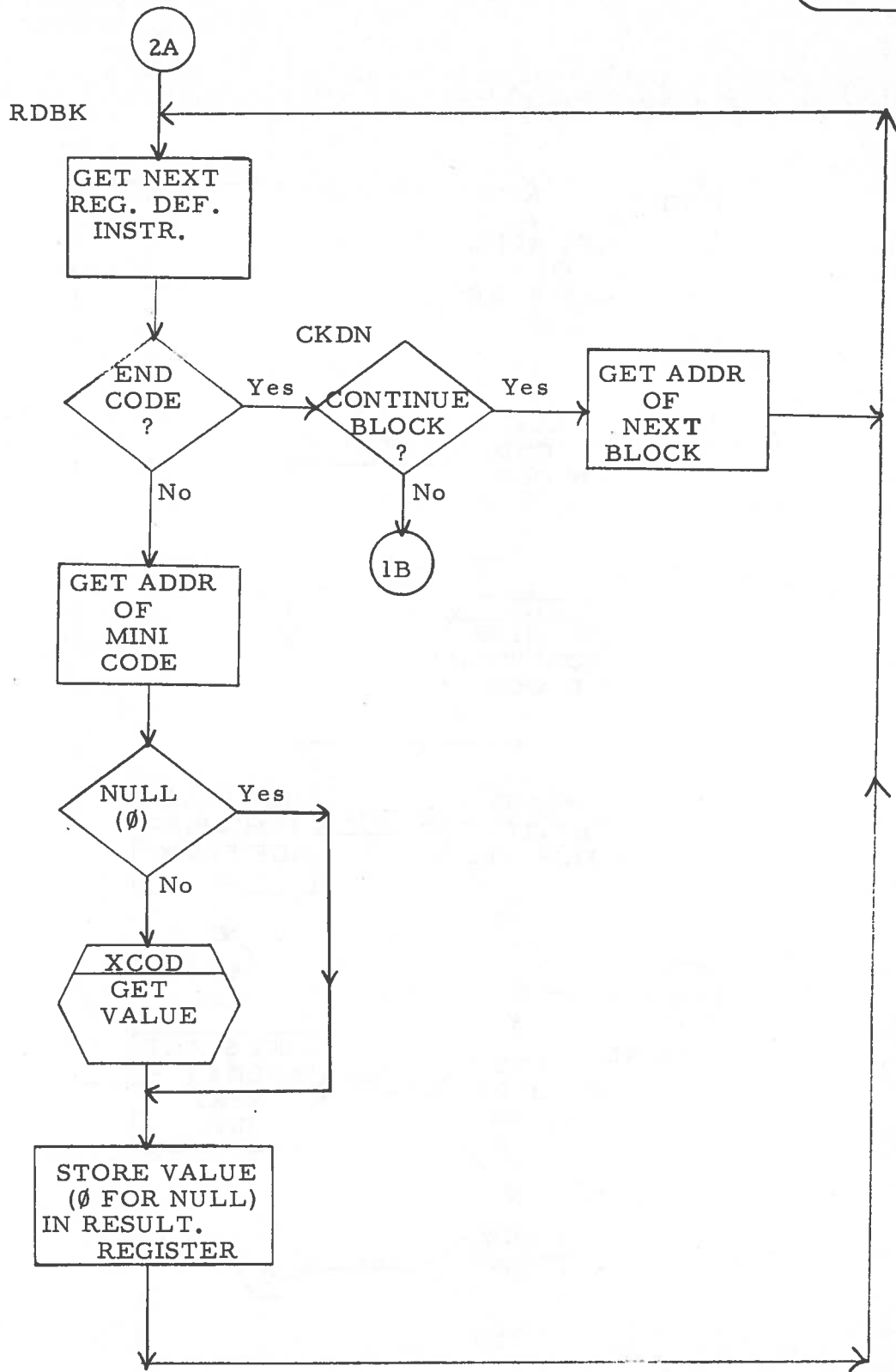
6. EXTERNAL REFERENCES:

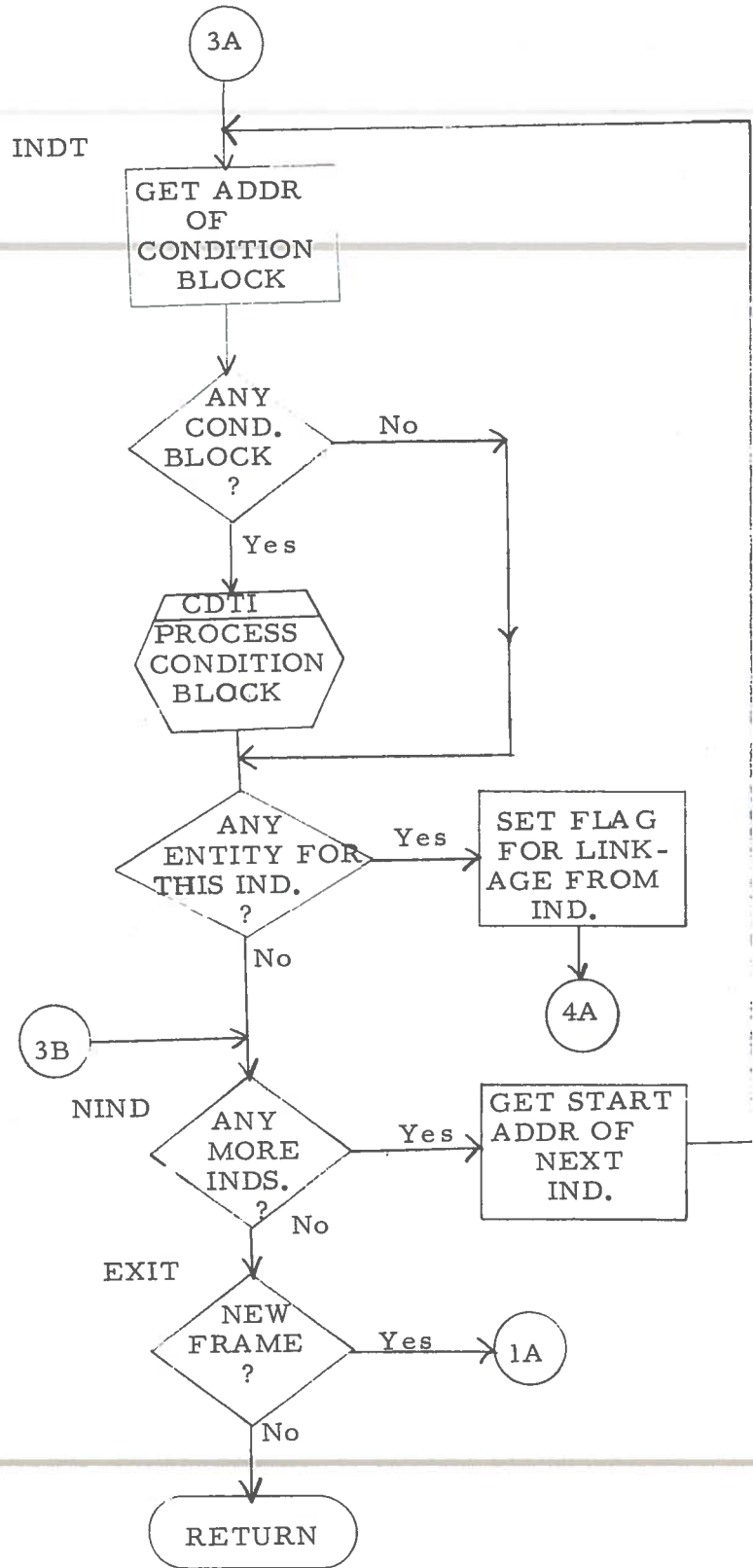
None

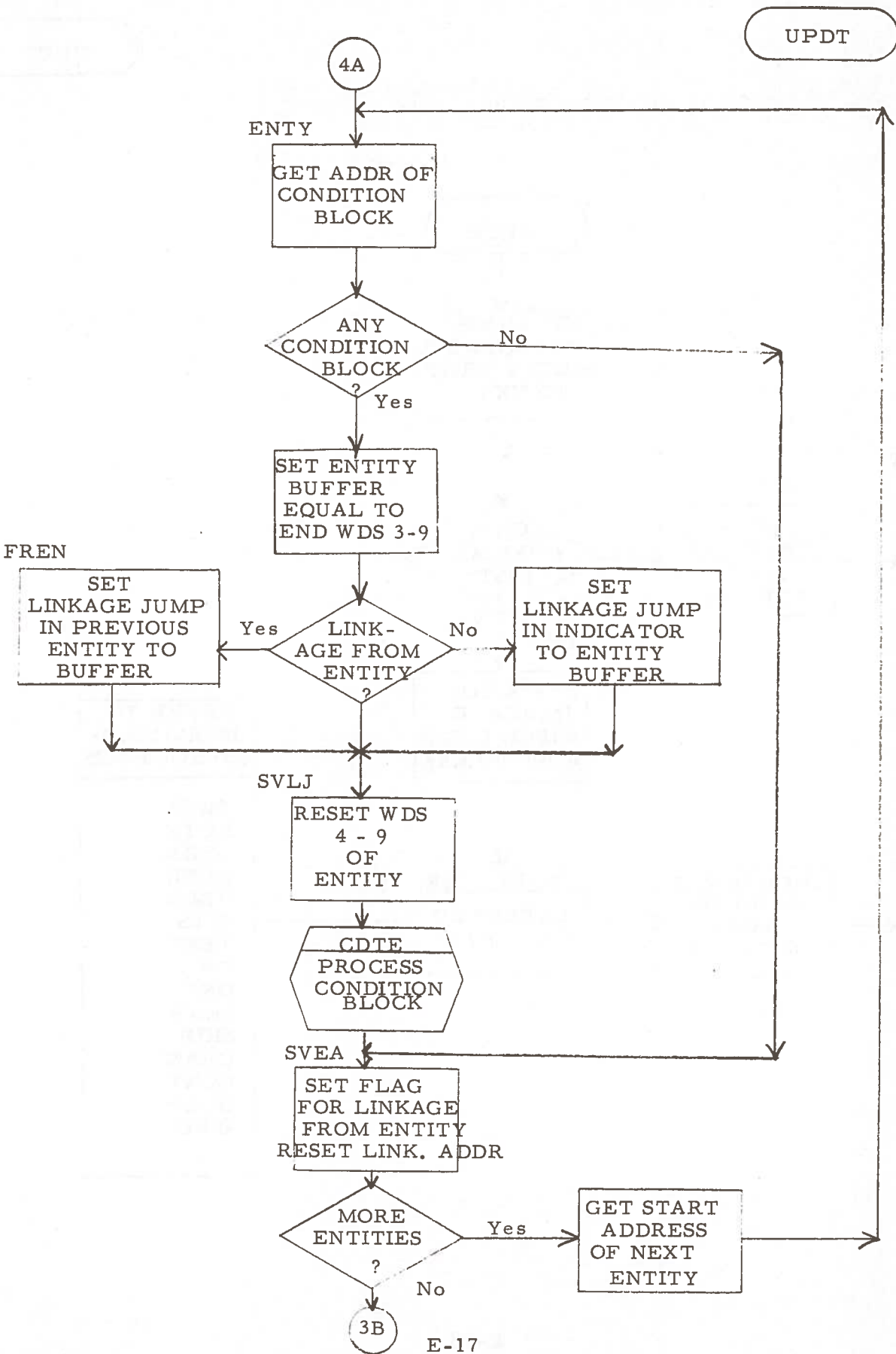
7. CORE USED:

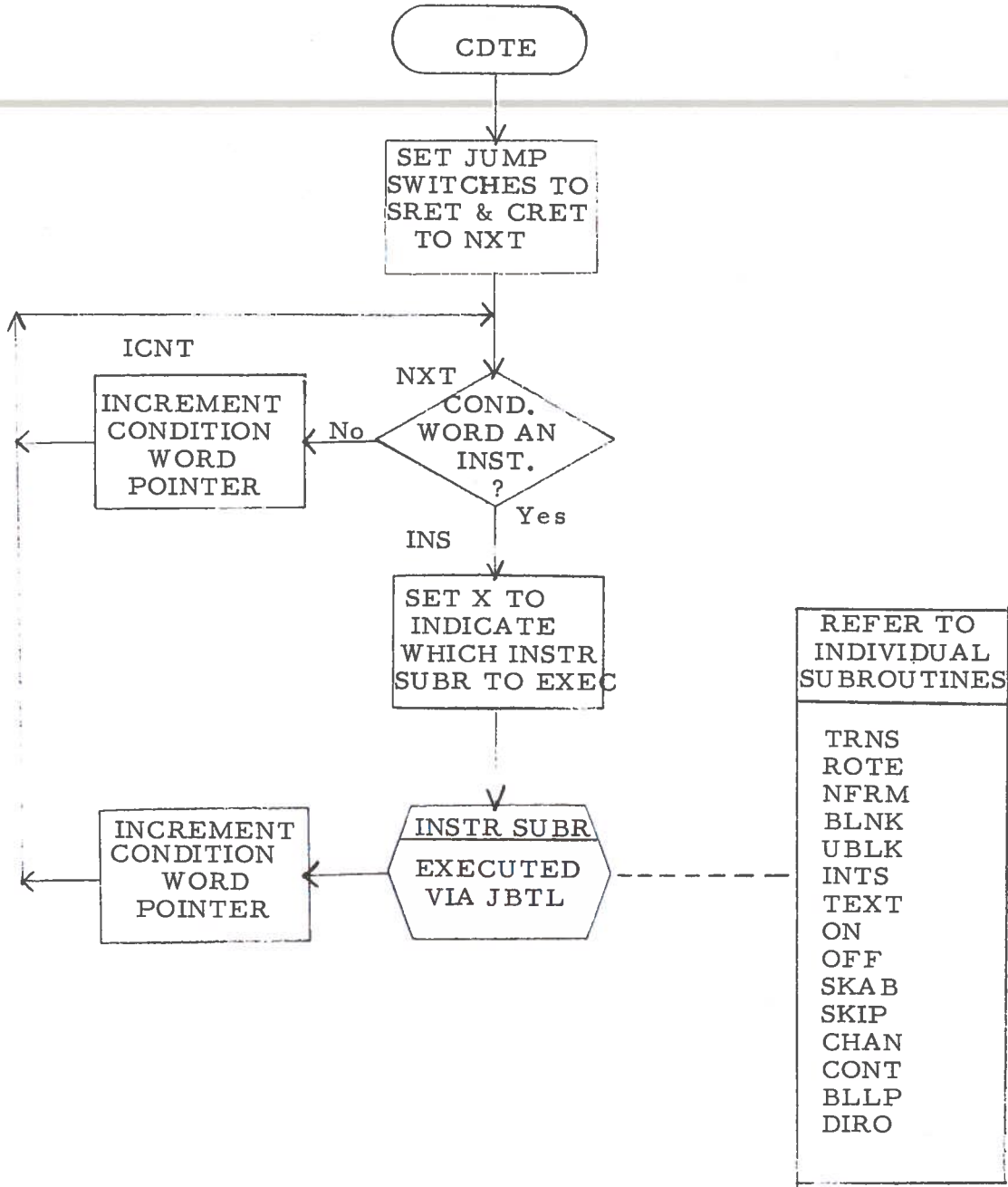
1266<sub>8</sub>

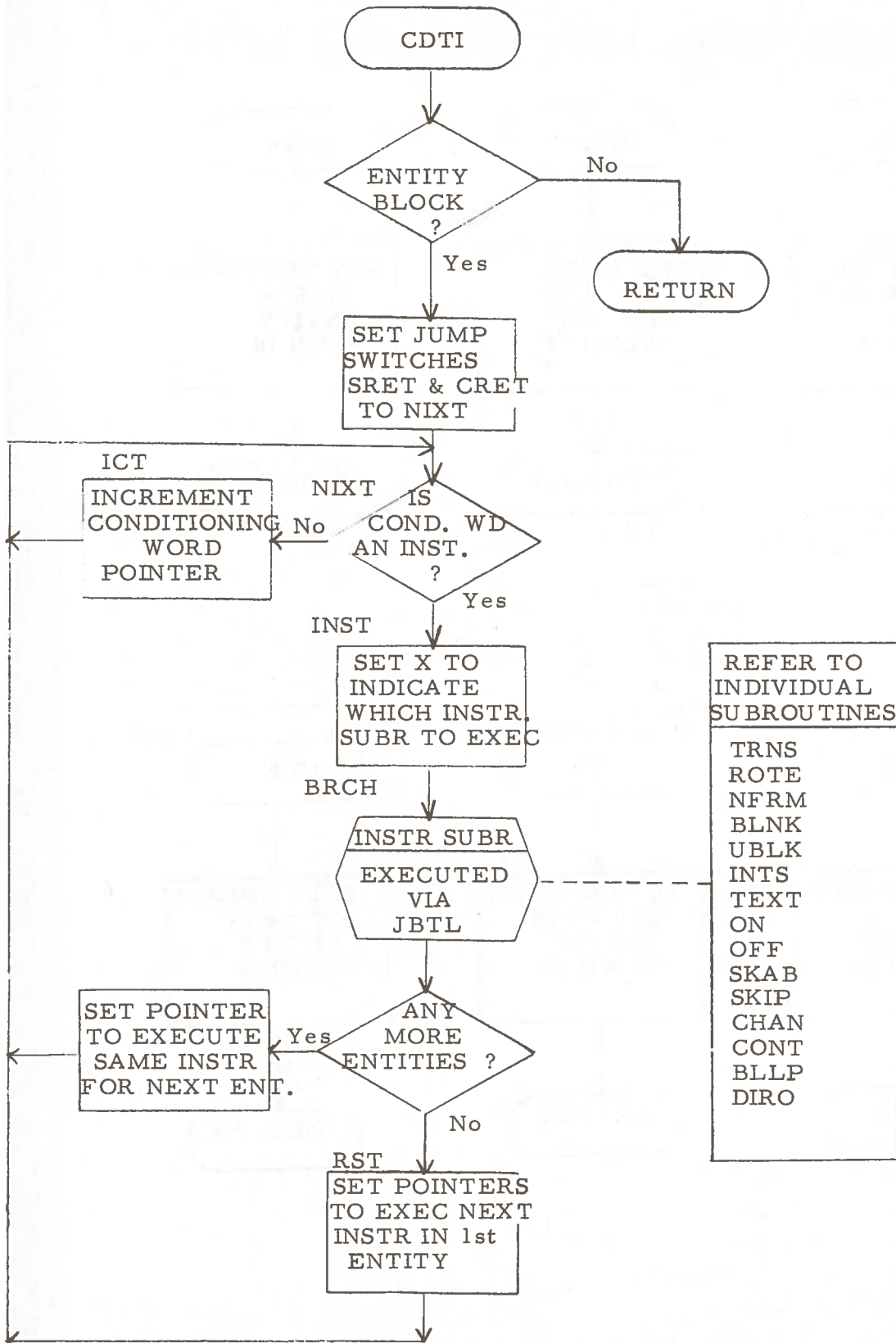












REFER TO INDIVIDUAL SUBROUTINES
TRNS
ROTE
NFRM
BLNK
UBLK
INTS
TEXT
ON
OFF
SKAB
SKIP
CHAN
CONT
BLLP
DIRO



BLNK

SET BLINK  
BIT # 1 IN  
ENTITY  
WORD 10

RETURN

UBLK

SET BLINK  
BIT = 0 IN  
ENTITY  
WORD 10

RETURN

TEXT

SET TEXTURE  
BITS IN  
ENTITY  
WORD 10

RETURN

INTS

SET INTENSITY  
LEVEL IN  
ENTITY  
WORD 10

RETURN

ON

SET UNBLANK  
BIT = 1 IN  
ENTITY  
WORD 10

RETURN

OFF

SET UNBLANK  
BIT = 0 IN  
ENTITY  
WORD 10

RETURN

